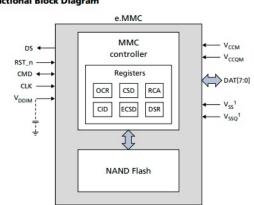


Image of 153 BGA (Top View Ball Down)

NC Not Connected
RFU reserved For future use

Vec , Veca West, and Vssa Board. Must Connect
system Board.

## Figure 7: e.MMC Functional Block Diagram



Note: 1. V<sub>SS</sub> and V<sub>SSQ</sub> are internally connected.

1/17/2020 Notes eMMC change questions

Power rail
Parte colded ell

Pins :

Transever L Band only UHF Recieve on L Band perinan UHF lister Backup

UN Requires Ablitz to shot Lova always

External Project RF Front end

Notes

Swissbit e MMC adding to schematic

Removed SD Card Transister — mm( PWR Rassistor

Pins STM 32 F 446 X C/E

LQ FP64		
	Pin #	function
2 <i>6</i> 27	34556633	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
28	53 54	SDI6 CM.

Pin Name PC 8 (20) PB 0 (27) PB 1 PC 11 PB 9 PC 7 (45) PC9 (51) PC10 (28) PBQ (63) PC 12 PDQ

Check Existing Pc 9 PC 10 PC 11 V NOT Free Not Free NOT Free Not Free PC 12 Not Free

Problems - Already in use

OPD\_SCI OPD\_SDA RADTOI\_CS RADTO2-CS PD2 TP7