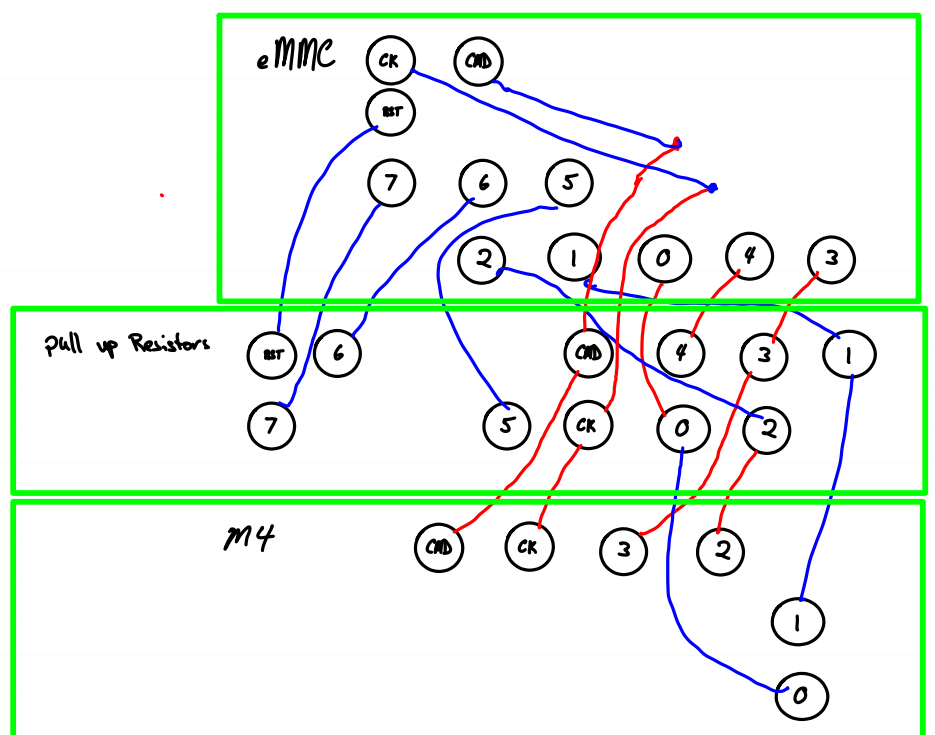# Oresat C3   v2.0

Layout Notes:

- Main goal move everything to the left side of the board
- Remove JP9 (not used), JP8 (FTDI VUSB to VBUS)
- Add flat ribbon connector to replace J7 (ARM SWD), C3_FTDI Connector
- Add From library J-TEConnectivity-X-1734592-X
  - Device   2-1734592-0
  - Footprint   TE_2-1734592-0
- Make all GND and 3.3V trace width 0.3048
- Make all Non GND and 3.3V Wire width 0.254 (or 0.2 under eMMC)
- Make sure that the edge clearance is maintained
- Make all components 0603 (Resistors, Capacitors)

- Connection Plan for eMMC to arrange connections to pull up resistor networks

- Add (per Ian) Protection Circuit for OPD
  { 0 ohm resistor (as place holder) for protection circuit.
    Make Symbol and footprint for TVS diode
    Little fuse SZ1SMB5.0AT3G