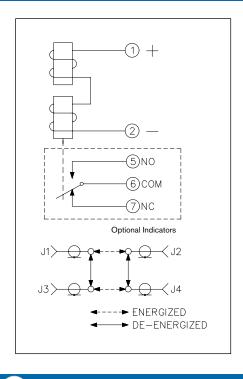
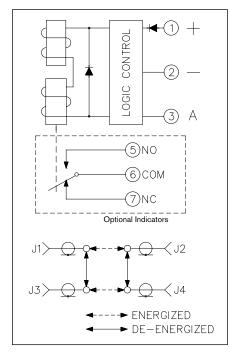
01 411C/412 Failsafe



03 Logic Truth Table





| | LOGIC TH TABLE | | |
|----------------------------|---------------------------------------|--------------------|--------------------|
| RF PATH | INDICATOR PATH | LOGIC INPUT "A" | |
| J1-J3/J2-J4 | NC-COM | 0 | |
| J1-J2/J3-J4 | NO-COM | 1 | |
| "0" = 0.0V- "1" = 2.4V- | -0.8V -5.5V | | |
| "0" = 0.0V "1" = 2.4V | -5.5V TTL - SCH # | | |
| "1" = 2.4V- | -5.5V | IC | |
| "1" = 2.4V- | -5.5V TTL - SCH # LOG | IC | LOGIC INPUT "B" |
| "1" = 2.4V- SELF CUTOFF | TTL - SCH # LOG TRUTH INDICATOR PATH | IC TABLE | LOGIC INPUT "B" |

04 411C/412 Pulse

05 411C/412 Self Cutoff

06 411C/412 Self Cutoff TTL

