



add far too many bypass caps to each of the MOSFET drivers

## Channel Configuration Instructions:

DMN61D8L-

DMN61D8L-LM

JP27

Each channel or pair has several configurable output modes. All channels can do the -500mA range. Only channel A can do the +16A with optional soft start range. Only channels B and C can do the  $\pm 1.5$ A range

JP23

GND

For the -500mA and +16A ranges:
Populate the dual MOSFET
The two channels in the pair may be configured independently

For the -500mA range: Jump the 2 position jumper for that channel For the +16A range:
Populate the P-FET, the 10K resistor, and a 1k gate resistor

Jump the 3 position jumper to select the voltage (if available)

To add soft start: Use a 680k gate resistor and populate the capacitor

For Banks B and C:

Never populate the dual MOSFET and the MOSFET driver on the same channel pair

For the -500mA range: Populate the dual MOSFET only

For the ±1.5A range: Populate the dual MOSFET driver only Both channels in the pair mmust be configured identically

Mode	Dual Driver	Dual MOSFET	P MOSFET	2pad Jumper	Rgate	С
-500mA		Yes		Jumped		
+16A		Yes	Yes	Open	1k	
+16A W/ Soft Start		Yes	Yes	Open	680k	Yes
±1.5A	Yes					

Do not populate any component not specified in the table

## Title: UniClOGS Power Distribution Board Rev: 2

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