

name: "REG_RT1"
nbits: 32
value: 0
default_value: 0
ui_data: ...



T4

name: "T4"
nbits: 1
value: 0
default_value: 0
ui_data: ...
type: L
behaviour: ["NOP",
"MV BUS_IB REG_RT1"]

BUS_IB

name: "BUS_IB"
nbits: 32
value: 0
default_value: 0
ui_data: ...