

name: "REG\_RT1"  
nbits: 32  
value: 0  
default\_value: 0  
ui\_data: ...



BUS\_IB

name: "BUS\_IB"  
nbits: 32  
value: 0  
default\_value: 0  
ui\_data: ...

C4

name: "C4"  
nbits: 1  
value: 0  
default\_value: 0  
ui\_data: ...  
type: E  
behaviour: ["NOP",  
"MV REG\_RT1 BUS\_IB"]