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This document is in draft format. It is known to be incomplet and incorrekt, and it has lots of bad fomatting. Drafting styles are defined as follows:

Text that is old and must be re-worked or removed

Shorthand note that should be incorporated into the text later

Something that must be done

Something that must be rephrased

Internal link

Citation link

External link

```
// Comment
normal text


```
pre-processor
class // Keyword
"String"
```


```

I know the meaning of plagiarism and declare that all of the work in the dissertation, save for that which is properly acknowledged, is my own.

.....

Author 1

Author 2

Author N

Abstract

The abstract should be a short summary of your dissertation. It is meant to sell your thesis to interested buyers.

This template includes advice relevant to a BSc final-year project (FYP), Masters dissertation or PhD thesis. While it has some relevance to a much smaller report, such as a course project report or even a lab report, these smaller types of reports will have less pieces and do not need nearly as much detail and depth – you are accordingly suggested to use the **Article** template in this repository; we will try to put together separate examples for a lab report and project report later.

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Nomenclature

1 Acronyms

A	Amperes
AC	Alternating Current
ADC	Analogue to Digital Converter
API	Application Programmer's Interface
ARM	Advanced RISC Machine
ASIC	Application Specific Integrated Circuit
AXI	Advanced Extensible Interface
BAR	Base Address Register
BCD	Binary-Coded Decimal
Bd	Baud, in symbols per second
CFAR	Constant False Alarm Rate
CMOS	Complimentary Metal-Oxide Semiconductor
CPLD	Complex Programmable Logic Device
dBm	Deci-Bell, relative to 1 mW
DC	Direct Current
DDC	Digital Down Converter
DDS	Direct Digital Synthesis
DMA	Direct Memory Access

DSP Digital Signal Processor (or processing)
 EDA Electronic Design Automation
 FIFO First-in, First-out (queue)
 FIR Finite Impulse Response
 FMC FPGA Mezzanine Card
 FPGA Field Programmable Gate Array
 FSM Finite State Machine
 GUI Graphical User Interface
 HDL Hardware Description Language
 HPS Hard Processor System
 HSTL High Speed Transfer Logic
 I/O Inputs/Outputs
 I²C Inter-IC
 IC Integrated Circuit
 IDE Integrated Development Environment
 LE Logic Element
 LSb Least Significant Bit
 LSB Least Significant Byte
 LUT Look-Up Table
 LVCMOS Low Voltage Complementary Metal Oxide Semiconductor
 LVDS Low Voltage Differential Signalling
 LVPECL Low Voltage Positive Emitter Coupled Logic
 LVTTL Low Voltage Transistor-Transistor Logic
 MIMO Multiple Input Multiple Output
 MISO Master Input / Slave Output
 MOSI Master Output / Slave Input

MSb Most Significant Bit
 MSB Most Significant Byte
 MSI Message Signalled Interrupt

 NCO Numerically Controlled Oscillator
 NTP Network Time Protocol

 PC Personal Computer
 PCB Printed Circuit Board
 PCI Peripheral Component Interconnect
 PCIe PCI Express
 PLL Phase Locked Loop
 PPDS Point-to-Point Differential Signalling
 PRF Pulse Repetition Frequency
 PRI Pulse Repetition Interval
 PSU Power Supply Unit

 RADAR Radio-Assisted Direction and Ranging
 REST Representational State Transfer
 RF Radio Frequency
 RISC Reduced Instruction Set Computer
 RMS Root Mean Square
 RPM Revolutions per Minute
 RSDS Reduced Swing Differential Signalling

 SI Système International d'Unités
 SoC System On Chip
 SPI Serial Peripheral Interface
 SSTL Stub Series Terminated Logic

 TCP Transmission Control Protocol
 TTL Transistor-Transistor Logic

UART Universal Asynchronous Receiver Transmitter
UDP User Datagram Protocol
UFM User Flash Memory
URL Uniform Resource Locator

V Voltage
VHDL VLSI HDL
VLSI Very Large-Scale Integration

XML eXtensible Markup Language

2 Terminology

Developer	FPGA firmware developer, using any firmware development tool.
Device	The specific target FPGA.
Megafunction	A target-specific module, typically generated from within the vendor IDE, after the Altera nomenclature.
Module	Akin to a Verilog module; i.e. unit of digital circuit that has ports to the outside world and can exist at any level of the design hierarchy.
Object file	An intermediary file used in the ALCHA compilation process. It is the result of compiling a single translation unit and describes a collection of objects.
Peripheral	Any device, external to the FPGA, that interfaces directly with the FPGA.
Platform	The platform that the ALCHA compiler runs on, including operating system and computer hardware.
Target	The platform ALCHA is compiling to, including FPGA, PCB, peripherals and vendor IDE.
Translation unit	Akin to a C language translation unit, i.e. a collection of source files and headers that translate to a single object file
User	The developer who is using ALCHA to develop FPGA firmware.
Vendor	The FPGA manufacturer.

Chapter 1

Introduction

Concerning Use of Latex

If you are new to L^AT_EX, I would suggest reading [2]. The TA and tutors can provide L^AT_EX support.

Remember that, for bibliography citations to work, you have to include running BibT_EX in the compile chain. You can, for instance, use TeXstudio [3] with a compile chain for “Build & View” set up as follows:

```
txs:///bibtex | txs:///pdflatex |  
txs:///bibtex | txs:///pdflatex |  
txs:///view-pdf-internal
```

Similar options exist for TeXmaker, VS Code and other IDEs.

Template Features

This template defines various macros aimed at assisting the author in writing consistently styled documents. You are encouraged to make use of these instead of the more low-level L^AT_EX commands.

Examples of how to use most of these macros are included throughout this example document. One not mentioned, however, is drafting markup. When the template is in draft mode, you can use various helper macros, as illustrated below:

This is old text that should be removed. *This is a note about something to remember, or comments from the proof-reader.* **This is something that still needs doing.** When compiled with `\Draftfalse`, the content of these macros are removed from the output, *except something that needs to be rephrased.*

You can also use cards, as follows:

TODO

This is a todo card.

It is a minipage environment, so you can have all sorts of stuff in it. It can be many paragraphs long, but don't make it too long, because L^AT_EX will force the whole card onto a single page.

NOTE

This is a nested note card. You can nest cards of arbitrary types as deep as you like.

Introduction Funnel – A General Guide

The introduction, and especially the first paragraph, is a key part of your thesis or dissertation. First impressions matter! This comment goes together with the abstract, but in a way the start of the introduction is setting an even more important first impression (as examiners know that an abstract is often a short piece that is highly polished and possibly not entirely representative of the quality that will appear later).

You want to start off with a “fantastic opening paragraph” and then lead-in to the topic (I call this the “once upon a time” part of the thesis, which is setting the scene for your ‘story’ ... I mean study). I suggest the use of the “funnel approach” that is illustrated below.

For introducing your dissertation, you essentially want to start broad and then focus in, getting to the main point of the focus. When you've got all the way to the scope, which explains some of the lower level details of e.g. specific context(s) of focus or aspects of exclusion, then one can transition (on something of a new tack) to explaining the outline of the dissertation, what chapters will follow and a brief summary of these, but without giving too many interesting points and findings away in these summaries of the chapters.

```

| Background |
| Terms      | - Optional
| Problem Desc | - General problem -- may want to jump into objectives
| Objective   |
\ Spec Probs / - Specific problems to study
\ Questions / - This is the research question you need to answer
\ ToR       / - Terms of reference, requirements, function
\ Scope     / - Discussing details of focus, things to leave out etc
| DiP       | - Dissemination Plan; optional - but highly recommended
-----
| |         | You kind of close off with the dissemination plan,
-----    | which is ultimately the delivery of your work to
/           \ the wider audience
/           \
/ Thesis     \ Then you can go on with (a somewhat different theme
/ Overview   \ that is the structure of the thesis
/           \

```

A suggested headings layout follows...

1.1 Background

Nice intro to background here...

1.1.1 Subsection

Some more detail follow in subsections...

1.1.1.1 Sub-subsection

Sub-subsections are rare in long style reports, and generally do not appear in the table of contents, but you might want to take it even further:

Note on numbering Note that usually this level does not need to be numbered, nor is it necessary to have it in the table of contents. If you really want to, you can modify the template to include numbering at this level and include it in the table of contents, but having more than four numbers in a row is too much and too distracting for most readers, so be sensible in choosing the number of levels used.

1.2 Important Terminology

If you have a few important terms, e.g. specially defined concepts or acronyms, it's good to introduce them early on, and this is where you could do it (i.e. possibly even before the objective if the objective depends on these terms). Note this is not the same as the nomenclature in the preamble – it is a body of text that might have an introductory passage introducing the terms and why you need to define them, then you might go on to make important definitions, for example in the follow format:

Thesis In this document, ‘thesis’ refers to a proposition put forward, and then discussed and proved or demonstrated in a scientific manner.

1.3 Objectives

Start with a statement of your broad objective

Narrow down to the specific objective of your project (but see note below).

Note

There are essentially two slightly different approaches that you may choose to follow. You can decide which you prefer (my preference tends to be the ‘reading in a hurry approach’ but you can decide as its your thesis).

Reading in a hurry

1.3 Objectives → *1.4 Problem Description / Problem Statement* →
1.5 Sub-objective / Research Questions → [1.6 Terms of Reference]

Identify board area then narrow down to specifics

1.3 Problem Description / Problem Statement → *1.4 Objectives* →
1.5 Sub-objective / Research Questions → [1.6 Terms of Reference]

1.3.1 Sub-objective / Motivational Discussions

Explain how each sub-objective fits into the main objective, or is needed for some reason to accomplish the objective.

1.4 Problem Description or Problem Statement

(note: this is an alternate to Hypothesis)

If you have a broad problem that you intend to discover insights or useful facts about, and you can not apply a “yes it works” or “no it doesn’t work” answer to your research, then use the problem description approach. This approach involves describing what you plan to observe, discover or get interesting – but as yet unknown – outputs from.

1.5 Terms of Reference – requirements and functions

This section is certainly a must-have for an engineering report, dissertation or thesis in which you are to build a prototype or system of some sort according to user requirements. If you are pursuing an entirely empirical study, this section might be irrelevant as in such a case you would probably not have anyone asking for requirements.

Start off by referring back to the objective(s) section. Then outline the specific requirements that you need to satisfy for your project. It is a good idea to briefly explain how these requirements were established and to reference the sources of this information (e.g. meetings, email correspondence, telephone calls, etc. See the APA or Harvard referencing guide for how to reference such things).

A good practice is to have a numbered list: one item for each main requirement (in the methodology you might break these top-level requirements into smaller parts; this would be an effective means to avoid having too much detail in the opening chapter).

Once you have listed the requirements, then list the functionality. It is good to have some text between the requirements and functionality explaining how you went from the one list (i.e. requirements) to the other (i.e. functionality). You may need to draw attention to particular portions of the literature review in which you investigated ways to provide functionality for a particular requirement. The functionality can also be given in a numbered list. For each function indicate which requirement it is linked to (i.e. this would essentially be following the good practice of a traceable design process).

You should also mention in this section about the testing procedures that will be used to check the performance (and/or adequate provision) of the functionality and the requirements (in some cases the requirements may be satisfied by assuring its dependent functionality is properly provided). You could have a table that relates each test to satisfying one or more functionality item (possibly clarifying the requirement that is checked as a result).

I usually like to have requirements numbered R1 to Rn (i.e. if you have three requirements you would number them R1, R2, and R3). In a similar manner, label the functionality F1, F2 and so on. In summary, you should end up with a structure something like this:

The main requirements:

R1. The first requirement described

R2. The second requirement described

R3. So on

R3.1 Although the `SpecList` environment does support nesting (up to 4 levels) you are encouraged not to make use of this feature.

The functionality needed to achieve these requirements:

F1. The first function described (derived from `R1`)

F2. The second function described (derived from `R2`)

F3. So on (derived from `R3`)

Then introduce the table for doing the acceptance testing, as shown in table 1.1. You can refer to a specific test like this: Test `T2`. Ideally you should only have one function and one requirement per test, otherwise the table becomes too wide. If you do want to add more to the list, reduce the `\TestTableDescriptionWidth` length. Feel free to modify the `TestTable` environment to suite your use-case.

TABLE 1.1

BREAKDOWN OF SUB-TESTS TO BE PERFORMED IN THE ACCEPTANCE TESTING.

Test	Description	Func	Req
T1	This is the test description of the first test. It can be a paragraph long.	F1	R1
T2	This is the test description of the second test. It can be a paragraph long.	F2	R2
T3	This is the test description of the last test. It can be a paragraph long.	F3	R3

1.6 Hypotheses

(note: this is an alternate to Problem Description)

A hypothesis is appropriate if you have a well focused project in which you want a binary result as in: “yes it works” or “no it doesn’t work”. Of course there is still likely to be some ‘fuzziness’ in the result such as “yes it works well in situation A, kind of in situation B and not at all in case C”, but essentially you’d be trying to show a yes/no result (or multiple yes/no results) in the end.

Following the hypothesis approach, you would mention hypotheses somewhere in this introduction chapter, not necessarily at this particular point – maybe earlier, maybe later, depending on whatever makes it easier to read.

Generally, it’s a good idea to start with a broad hypothesis, typically labelled H0.

You then refine H0 into a few sub-hypotheses, e.g. H1 to H2, which are often related to the sub-objectives described in 1.3.1. You would then clearly be deciding a series of yes or no answers to H1 to H2, which would then imply something about H0. If you wanted to be fancy you might go as far as using mathematical logic to explain these implications, but that’s more commonly found in math / applied math theses.

Example of how the main hypothesis and sub-hypotheses could be presented:

Hypothesis H0: A 600 bps data transmission modulated upon a 1 s sonar ping of base frequency 3 kHz can sustaining data transmissions of 75 bytes per ping over a 2 km distance, with bit error rate below 2%.

Hypothesis H1: Data packets of length 75 bytes, overlaid on 3 kHz sonar pings, are sufficient to provide encrypted submarine identification codes.

Hypothesis H2: Data transmissions overlaid on 3 kHz sonar pings cause minimal degradation in the accuracy of echo location results.

1.7 Scope and Limitations

What were the restrictions on your project? Usually, they are related to time and budgetary constraints. Other scope limitations taking measure to compensate for, or eliminate, potential ethics conflicts, include avoiding invasive testing techniques, contention concerning IP rights, and taking human or animal test subjects out of the research design to circumvent potential injury, subjectivity or other factors.

A general structure for the scope could read as follows:

- short blurb making mention of major scope and reasons for this
- point form list of scope details
- rounding off blurb reflecting on the scope

1.8 Dissemination Plan

PhD must have this!

In this section you explain the way you plan to disseminate this project (i.e. share this work and its findings with others). If you've completed the dissemination section of the proposal, then you know pretty much what goes here, but, unlike the proposal perhaps, don't make too many promises in this one, because the examiner will see it.

Ideally, if you have a paper published already you would have it in the list of planned outputs and be able to make a song and dance that it has been accepted / published and has gone through a thorough peer review process and been accepted by the scientific community.

That can be said for either a conference or journal – but only, of course, if it is one that has a peer review process, if there's just a review of the abstract, or even no review (i.e. just pay to attend the conference) then you don't want to really have a mention of what sort of low value output, if anything it would just reinforce difficulty of getting the topic accepted,

so you only want to mention options and accomplishments that are peer reviewed, and, even better, accredited.

Aim for being accredited and indexed by Thomson / Clarivate Analytics Web of Science (this ‘Web of Science’ is top notch indexing), or Scopus Indexed (if it is Scopus indexed it is also Scopus accredited), which is prestigious also (all ‘Web of Science’ tend to be Scopus as well, but not all Scopus items are in ‘Web of Science’). If it’s not indexed by either of these, then the lower (but still somewhat OK) is the Directory of Open Access Journals (DOAJ). Copernicus is varied, from great to not great, somewhat OK but not great if just this one. INSPEC has increased greatly in quality but is not as prestigious as a ‘Web of Science’ or Scopus indexing.

If the journal raves about something like ‘Google Scholar’ or ‘CiteSeer’ indexing, it doesn’t really mean much as these are automatic engines that are impartial to assessing the quality of the journal.

1.9 Document Outline

Suggestion: Discuss layout of thesis at the end of this chapter. Ideally, the preceding subsections should have given a sufficiently clear and concise explanation of what is to be done without the reader having to delve into this part.

You could optionally have a figure in this section to augment your description. You could start each paragraph summarising the chapter with the chapter and its number in bold as below (but you could e.g. leave out the bold if you think that makes it more consistent with the rest of the manuscript style.)

Chapter 2 concerns the literature review. It presents technologies, theories and techniques on which this thesis builds.

Chapter 3 (usually) presents the research methodology for this project. The methodology should give a good amount of detail about the acceptance test. i.e. For the acceptance test, explain how you are going to meticulously prove that each item of functionality works, and by implication each requirement as satisfied (as was listed in the introduction).

Chapter 4 gives the design (often called Prototype Design if indeed you are developing a prototype to experimentally test a design concept). You may decide to include a specification prior to the design (usually this is not expected in a BSc or MSc project, but should be given in a PhD thesis). If you have a significant amount to write up about the implementation (i.e. to show pictures of your PCBs, development environment setup, various code and screenshots) then add a separate chapter for implementation. Note that it is preferred to have lengthy blocks of code in the appendix, keeping only snippets (or code fragments) to explain parts of the implementation within the main part of the write-up.

Chapter 5 presents the results, showing results of the design / system in action, and results of the tests (i.e. carrying out the stages of the acceptance tests).

Chapter 6 provides the conclusions. This may be preceded by a discussion of what was done, a discussion of results and testing (e.g. what limitations were found related to ways tests were done, good or bad things about the methodology, amongst other observations related to results or the way they were obtained), followed by a more generalised discussion and conclusions. Provide also future work plans and suggestions.

And don't forget the references at the end of the last chapter!

Chapter 2

Literature Review

2.1 Suggested Approach

Here you reference other, related work, as well as a summary relating to how you improve upon said work [4].

As with suggestions provided concerning the Introduction, in which a funnel type of approach was suggested, essentially going from broader issues to more specific ones, a similar approach can be highly effective for the literature review.

A literature review is one of the most difficult chapters to write, and particularly when it comes to deciding what should – and should not – be provided. Often students provide too much, and go into an excessive amount of detail, on things covered in the literature review. A guiding principle is demonstrating your awareness of the field and related works that will (or could) guide your work, are important to consider (e.g. standards) or that you are building upon.

You do not really want to have a literature review that are essentially recreating lecture notes or summaries on the theory; certainly in some case you do need brief recaps of techniques or theories (especially with references to direct the reader to resources where they can touch up on their understanding of the issues covered). MSc student in particular are likely going to need advice from their supervisor in making an effective

choice of what should be, and should not be, recapped in the literature review.

Fig. 2.1 provides an illustration of a suggested “funnel structure” around which you could build your literature review. This structure should work for either BSc, MSc or PhD. The difference between them (i.e. BSc towards PhD) is the scale, scope, complexity and novelty of these. For example, a BSc will be small scale (often just a very specific gap in knowledge, if any; it might be more a build and test rather than a finding something to fill a knowledge gap) and not so complex and might not even have any novelty.

A MSc would be a bit larger in scale (but not too much), more complex (likely more things happening and more complicated connections between these) and hopefully (but not necessarily) having some novelty (e.g. using a different platform – a Raspberry Pi instead of an Arduino to trial an embedded solution).

A PhD is of course quite a step up from a MSc in terms of scope, complexity. It is required to prove that you’ve made a novel contribution in that it has added new knowledge (i.e. filled a non-trivial gap in the research literature).

The parts of the literature review funnel structure are explained briefly in the points below. Note that you do not really want to just use these bullet points for the names of the subheadings of your lit review: you need to think a bit more about what would be suitable heading names and sequencing of these. These tips are only suggestions to help you think about how to construct your literature review and should not be considered a prescribed structure or method to use (theses do tend to be quite unique, there is not really one method that would work for all cases, generally it is built through a process of reviewing what others have done, writing and reworking the document).

The Lit Review Funnel

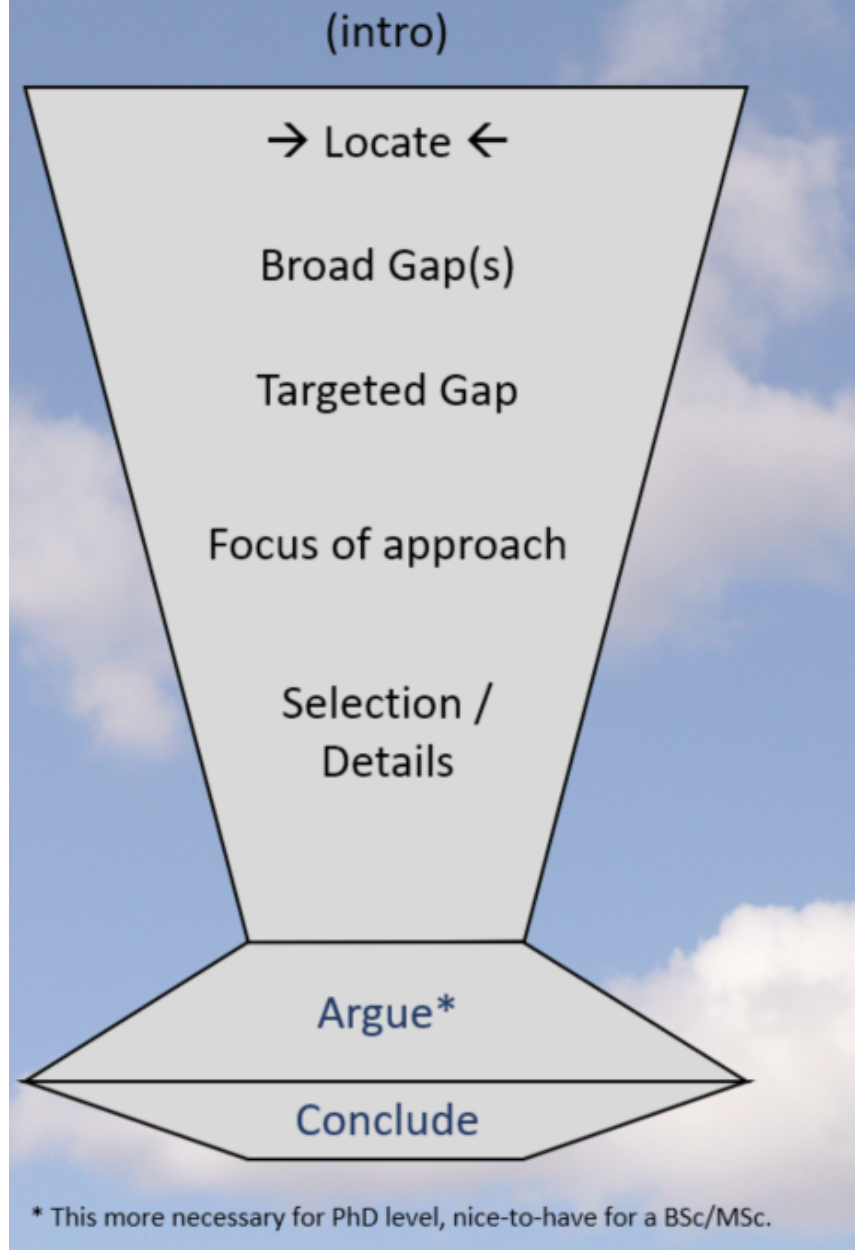


Fig. 2.1. Literature review funnel structure

- **Intro:** Start with explaining the approach to the lit review and its structure.
- **Locate:** Entry to the top funnel, outlining what you will cover. Start drilling down into the field and broad theories. Cover significant aspects of main thinking / trends you are plugging in to.
- **Gap(s):** Ideally, bring the discussion towards a point where you identify gaps in research or areas that need further investigation.
- **Targeted gap:** Of the various gaps identified, indicate which one(s) you will focus on in this project (for BSc / MSc probably just one gap; PhD may have more but not too many).
- **Focus of approach:** Get into the more specific issues, i.e. techniques that will be used to solve the problem and review these theories / methods / tools. At the same time you may cover a few ‘alternate approaches’ that could inspire your approach or what you are learning from to refine the approach you have chosen, learning from past attempts or similar types of investigation (they obviously don’t need to be trying to do the same thing, but have some relation / relevance to your project).
- **Selection / Details:** At this part you get into more specifics of potential development tools or libraries you are planning to use and why. This can be handled fairly easily, e.g. doing brief literature surveys, e.g. web searches, on what are the most popular tools. Could expanded by describing a methodology to use for selecting these tools, the result of this could be tables showing the tool / library options, as well as their pros and cons*. The tool rated best could be the one to choose in the project.
- **Argue:** Nice-to-haves (emphasising ‘the researcher’ and research benefits); broaden out with argument; identifying new knowledge needed; how this research investigation will contribute new understanding. This part depends on space availability*.
- **Conclude / Summary:** Try to close in some elegant way. This could be done with a short summary of key observations / gaps and useful theories / methods you’ll build on. For e.g. PhD proposal in particular you could end it by justifying (reaffirming the need for) this research.
- *: Items marked by asterisk are more necessary for PhD level, but still a nice-to-have for a BSc/MSc dissertation.

Chapter 3

Methodology

In this section you should describe the method followed in your research project. For an engineering case, this is typically involves the development and experimentation of the system that is developed to solve a problem together with how testing of the system was carried out. Testing results are used to show that it indeed solved the problem, or at least to what extent it solved the problem (note that, yes, unfortunately it is sometimes not possible to provide an effective solution – it may be too expensive or take too long to solve properly – but even then it can still be considered a contribution of knowledge and still have some value as a research investigation).

Methodology vs Design: A vitally important terminology consideration is to realize that ‘methodology’ is not the same as ‘design’. This is a common confusion amongst students who have not done a research project or written a thesis before. This short explanation should suffice to ensure you understand the difference:

- **Methodology:** This describes how you are going to go about doing the research project. It describes the main high-level steps (or phases) of the project. It does not detail design or pieces of your design. You should not need any block diagrams in the methodology.
- **Design:** This is what you have built (or are going to build). It includes diagrams, system block diagram, flow charts, pseudocode / algorithms, schematics, etc. Generally, in a computing-related project, it will end off with implementation details

(actual code snippets). These implementation details, depending on how much there are of them, may be better placed in a separate Implementation chapter. Similarly, you might decide to have a separate Integration chapter if there are a lot of pieces that takes quite a bit of explaining and diagrams to describe how they connect and work together.

3.1 Experiment Procedure

Include detail relating to the experiment itself: what did you do, in what order was this done, why was this done, etc. What are you trying to prove / disprove? Here you can refer back to the hypothesis in the introduction.

Chapter 4

Design

4.1 Hardware

Include detail such as the hardware used. It's generally a good idea to include a block diagram at this point, such as the one presented in Fig. 4.1. This figure was drawn in [InkScape](#) [5]. When you want to import an InkScape figure (SVG format) into L^AT_EX, simply save it to PDF (use the drawing extents as the media box area) and include the figure.

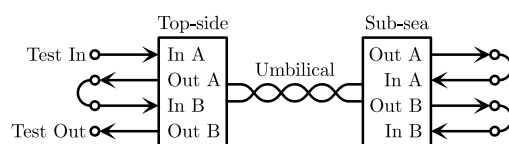


Fig. 4.1. Test setup used to test the implementation [1].

4.2 Implementation

Also mention the implementation source code:


```
# You can include inline Matlab / Octave code
x = linspace(0, 2*pi, 1000);
y = sin(x);
plot(x, y); grid on;
```

Or you could turn it into a float: see listing 4.1. Floats are tables, figures and listings that appear at a different place than in the source code. This template is set up to put floats at the top of the next column, as prescribed by the IEEE article specification.

```
__kernel void Multiply(
    __global float* A, // Global input buffer
    __global float* B, // Global input buffer
    __global float* Y, // Global output buffer
    const int N // Global uniform
){
    const int i = get_global_id(0); // 1st dimension index
    const int j = get_global_id(1); // 2nd dimension index

    // Private variables
    int k;
    float f = 0.0;

    // Kernel body
    for(k = 0; k < N; k++) f += A[i*N + k] * B[k*N + j];
    Y[i*N + j] = f;
}
```

Listing 4.1. OpenCL kernel to perform matrix multiplication

Only list what is relevant. Don't give too much detail – just enough to show what you've done. This template supports the following languages:

- Matlab (Octave)
- GLSL
- OpenCL
- Verilog
- VHDL
- TCL
- Python
- C++ (use the name 'Cpp')

Chapter 5

Experimentation

Chapter 6

Results

The results section is for presenting and discussing your findings. You can split it into subsections if the experiment has multiple sections or stages.

6.1 Figures

Include good quality graphs (see Fig. 6.1). These were produced by the Octave code presented in listings 6.1 and 6.2. You can play around with the `PaperSize` and `PaperPosition` variables to change the aspect ratio. An easy way to obtain more space on a paper is to use wide, flat figures, such as Fig. 6.2.

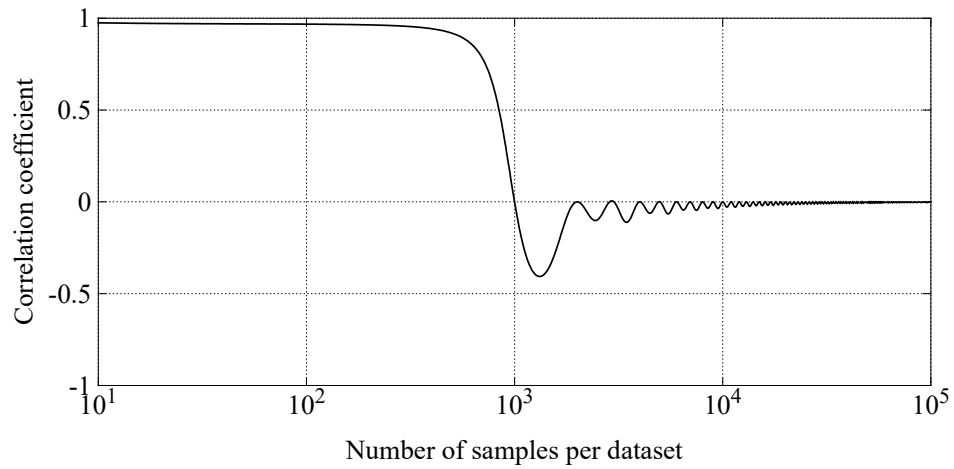


Fig. 6.1. The correlation coefficient as a function of sample count.

```
function FormatFig(X, Y, File);
    set(gcf, 'PaperUnits'      , 'inches');
    set(gcf, 'PaperOrientation', 'landscape');
    set(gcf, 'PaperSize'      , [8, 4]);
    set(gcf, 'PaperPosition'   , [0, 0, 8, 4]);

    set(gca, 'FontName', 'Times New Roman');
    set(gca, 'Position', [0.1 0.2 0.85 0.75]);

    xlabel(["\n" X]);
    ylabel(["Y "\n\n"]);

    setenv("GSC", "GSC"); # Eliminates stupid warning
    print(...
        [File '.pdf'],...
        '-dpdf'...
    );
end
```

Listing 6.1. Octave function to format a figure and save it to a high quality PDF graph

```

figure;                                     # Create a new figure
# Some code to calculate the various variables to plot...
plot(N, r, 'k', 'linewidth', 4); grid on; # Plot the data
xlim([0 360]);                             # Limit the x range
ylim([-1 1]);                              # Limit the y range
set(gca, 'xtick', [0 90 180 270 360]);    # Set the x labels

FormatFig(...                             # Call the function with:
'Phase shift [\circ]',...                 # The x title
'Correlation coefficient',...             # The y title
['r_vs_N;_f=' num2str(f) ';_P=' num2str(P)]... # Format the file name
);
close all;                                # Close all open figures

```

Listing 6.2. Example of how to use the FormatFig function

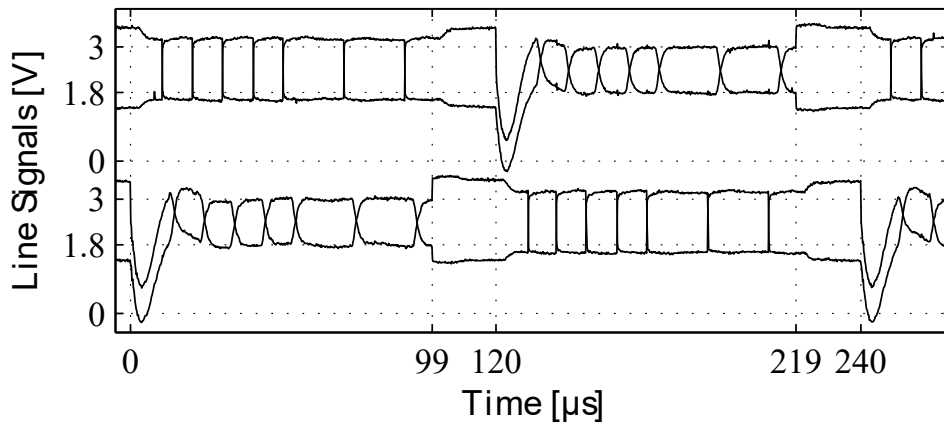


Fig. 6.2. Oscilloscope measurement showing physical line signals on both ends of a transmission line during master switch-over [1].

Always remember to include axes text, units and a meaningful caption in your graphs. When typing units, a μ sign has a tail! The letter “u” is not a valid unit prefix. When typing resistor values, use the Ω symbol.

6.2 Tables

Tables are often a convenient means by which to specify lists of parameters. An example table is presented in table 6.1. You can use [Tablesgenerator](#) to make your L^AT_EX tables.

TABLE 6.1
MY INFORMATIVE TABLE

Heading 1	Heading 2	Heading 3
Data	123	321
Data	456	654
Data	789	987

6.3 Pictures and Screen-shots

When you include screen-shots, pdfL^AT_EX supports JPG and PNG file formats. PNG is preferred for screen-shots, as it is a loss-less format. JPG is preferred for photos, as it results in a smaller file size. It’s generally a good idea to resize photos (not screen-shots) to be no more that 300 dpi, in order to reduce file size. For 2-column article format papers, this translates to a maximum width of 1024. **Never change the aspect ratio of screen-shots and pictures!**

It is highly recommended to make use of the `\Figure` macro for figures. It puts all the formatting tweaks in one place, so that you don’t need to update all the individual figure inclusion points when you want to do a styling update. The file name is used for the L^AT_EX label, such as “Fig. 6.3”.



Fig. 6.3. An example image with custom scaling

Make sure to always use the best quality image possible. Use JPEG for photos, PNG for screen-shots and PDF (scalable vector graphics) for everything else. JPEG is lossy, but good for photos, whereas PNG is lossless and good for images with large areas of solid colour, as can be seen in Fig. 6.4.



Fig. 6.4. Comparison of various image format qualities

6.4 Maths

L^AT_EX has a very sophisticated maths rendering engine, as illustrated by equation 6.1. When talking about approximate answers, never use ± 54 V, as this implies “positive or negative 54 V”. Use ≈ 54 V or ~ 54 V instead.

$$y = \int_0^{\infty} e^{x^2} dx \tag{6.1}$$

Chapter 7

Conclusion

The conclusion should provide a summary of your findings. Many people only read the introduction and conclusion of a paper. They sometimes scan the tables and figures. If the conclusion hints at interesting findings, only then will they bother to read the whole paper.

You can also include work that you intend to do in future, such as ideas for further improvements, or to make the solution more accessible to the general user-base, etc.

Bibliography

- [1] J. Taylor and J. G. Hoole, “[Robust Protocol for Sending Synchronisation Pulse and RS-232 Communication over Single Low Quality Twisted Pair Cable](#),” in *Proceeding of ICIT*. Taiwan: IEEE, Mar. 2016.
- [2] T. Oetiker, H. Partl, I. Hyna, and E. Schlegl, “[The Not So Short Introduction to L^AT_EX 2_ε](#),” <https://tobi.oetiker.ch/lshort/lshort.pdf>, Jul. 2015, version 5.05.
- [3] B. van der Zander, J. Sundermeyer, and T. Hoffmann, “[TeXstudio – A L^AT_EX Editor](#),” <https://www.texstudio.org/>.
- [4] A. Baboon, B. Charles, D. Ester, and F. Generalson, “An Amazing Title,” Their Not-so-awesome University, Technical Report, Apr. 1492.
- [5] “[InkScape Website](#),” <http://www.inkscape.org/>.

Appendix A

Item 1

Appendix B

Item 2