```
— Module lww_register -
2 EXTENDS Integers, Sequences, TLC
3 Constants N
5 \ Procs \stackrel{\triangle}{=} 1 \dots N
   INITIAL \stackrel{\triangle}{=} [t \mapsto 0, val \mapsto ""] initial value [t \mapsto 0, val \mapsto ""]
    --algorithm lww_register
10
    variables
11
      msgs = [j \in Procs \mapsto INITIAL]; to send the messages
12
    define
14
      \_Compare(p1, p2) \stackrel{\triangle}{=} p1.t < p2.t return TRUE if timestamp\_1 < timestamp\_2
15
      \_Merge(p1, p2) \stackrel{\triangle}{=} \text{ if } \_Compare(p1, p2) \text{ THEN } p2 \text{ ELSE } p1
17
    end define;
18
     assign a value and timestamp into local payload
20
    macro \_Assign(v)begin
21
      payload := [t \mapsto JavaTime, val \mapsto v];
22
      print ToString(self) \circ "assigned" \circ ToString(payload);
23
    end macro;
24
     send the payload 'p' to a random proc
26
    macro \_Send(p)begin
27
      if payload \neq INITIAL then
28
        with j \in Procs \setminus \{self\} do
29
          msgs[j] := payload;
30
          print ToString(self) \circ "sent" \circ ToString(msgs[j]) \circ "to" \circ ToString(j);
31
        end with;
32
      end if;
33
    end macro;
34
36
     receive the payload
    macro _Receive()begin
37
      if msgs[self] \neq INITIAL then
38
        print ToString(self) \circ "received" \circ ToString(msgs[self]);
39
        if payload = INITIAL then payload is empty, just receive the message
40
          payload := msgs[self];
41
         else merge the payload and received message
42
          payload := \_Merge(payload, msgs[self]);
43
        end if;
44
        msgs[self] := INITIAL;
        print ToString(self) \circ "merged" \circ ToString(payload);
46
      end if;
47
    end macro;
```

```
fair process Register \in Procs
50
    variables
51
       i = 0, count iterations
52
       payload = INITIAL; local payload
53
    begin Main:
54
       while i < N do
55
         either Assign:
56
            \_Assign(self);
57
         or Send:
58
            _Send(payload);
59
         or Receive:
60
            \_Receive();
61
         end either;
62
          Loop:
63
            i := i + 1;
64
       end while ;
65
    end process;
66
    end algorithm ;
68
      BEGIN TRANSLATION
70
71
    VARIABLES msgs, pc
      define statement
73
    Compare(p1, p2) \stackrel{\Delta}{=} p1.t \leq p2.t
    \_Merge(p1, p2) \stackrel{\triangle}{=} \text{ if } \_Compare(p1, p2) \text{ THEN } p1 \text{ ELSE } p2
    Variables i, payload
    vars \triangleq \langle msqs, pc, i, payload \rangle
    ProcSet \stackrel{\triangle}{=} (Procs)
     Init \stackrel{\triangle}{=} Global variables
84
                \land msgs = [j \in Procs \mapsto INITIAL]
85
                 Process Register
86
                \land i = [self \in Procs \mapsto 0]
87
                \land payload = [self \in Procs \mapsto INITIAL]
88
                \land \ pc = [\mathit{self} \in \mathit{ProcSet} \mapsto \text{``Main''}]
89
     Main(self) \triangleq \land pc[self] = "Main"
91
                         \land if i[self] < N
92
                                 THEN \land \lor \land pc' = [pc \text{ EXCEPT } ! [self] = \text{"Assign"}]
93
                                             \lor \land pc' = [pc \text{ EXCEPT } ! [self] = \text{``Send''}]
94
                                             \lor \land pc' = [pc \text{ EXCEPT } ! [self] = \text{``Receive''}]
95
                                 ELSE \land pc' = [pc \text{ EXCEPT } ! [self] = \text{"Done"}]
96
                         \land UNCHANGED \langle msgs, i, payload \rangle
97
```

```
Loop(self) \stackrel{\triangle}{=} \wedge pc[self] = \text{``Loop''}
 99
                           \wedge i' = [i \text{ EXCEPT } ![self] = i[self] + 1]
100
                           \land pc' = [pc \text{ EXCEPT } ! [self] = \text{"Main"}]
101
                           \land UNCHANGED \langle msgs, payload \rangle
102
      Assign(self) \stackrel{\triangle}{=} \land pc[self] = \text{``Assign''}
104
                             \land payload' = [payload \ \text{EXCEPT} \ ![self] = [t \mapsto JavaTime, \ val \mapsto self]]
105
                             \land \ PrintT(\ ToString(self) \circ \ ``assigned \ " \circ \ ToString(payload'[self]))
106
                             \land pc' = [pc \text{ EXCEPT } ! [self] = \text{``Loop''}]
107
                             \land UNCHANGED \langle msgs, i \rangle
108
      Send(self) \stackrel{\Delta}{=} \wedge pc[self] = "Send"
110
                          \land IF payload[self] \neq INITIAL
111
                                  THEN \land \exists j \in Procs \setminus \{self\}:
112
                                                 \land msgs' = [msgs \ EXCEPT \ ![j] = payload[self]]
113
                                                 \land PrintT(ToString(self) \circ "sent" \circ ToString(msgs'[j]) \circ "to" \circ ToString(j))
114
                                  ELSE \land TRUE
115
                                            \wedge msqs' = msqs
116
                          \land pc' = [pc \text{ EXCEPT } ! [self] = \text{``Loop''}]
117
                          \land UNCHANGED \langle i, payload \rangle
118
      Receive(self) \stackrel{\Delta}{=} \land pc[self] = "Receive"
120
                              \land IF msgs[self] \neq INITIAL
121
                                     THEN \land PrintT(ToString(self) \circ "received" \circ ToString(msgs[self]))
122
                                               \land IF payload[self] = INITIAL
123
                                                      THEN \land payload' = [payload \ EXCEPT \ ![self] = msgs[self]]
124
                                                      ELSE \land payload' = [payload \ EXCEPT \ ![self] = \_Merge(payload[self], msgs)]
125
                                               \land msgs' = [msgs \ EXCEPT \ ![self] = INITIAL]
126
                                               \land PrintT(ToString(self) \circ "merged" \circ ToString(payload'[self]))
127
                                      ELSE ∧ TRUE
128
                                               \land UNCHANGED \langle msgs, payload \rangle
129
                              \land pc' = [pc \text{ EXCEPT } ! [self] = \text{``Loop''}]
130
                              \wedge i' = i
131
      Register(self) \triangleq Main(self) \lor Loop(self) \lor Assign(self) \lor Send(self)
133
                                   \vee Receive(self)
134
      Next \triangleq (\exists self \in Procs : Register(self))
136
                     V Disjunct to prevent deadlock on termination
137
                        ((\forall self \in ProcSet : pc[self] = "Done") \land UNCHANGED vars)
138
      Spec \stackrel{\Delta}{=} \wedge Init \wedge \Box [Next]_{vars}
140
                   \land \forall self \in Procs : WF_{vars}(Register(self))
141
      Termination \stackrel{\triangle}{=} \lozenge(\forall self \in ProcSet : pc[self] = "Done")
143
       END TRANSLATION
145
```