

Jacqueline Radding

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EDUCATION

California Polytechnic State University, San Luis Obispo (Cal Poly)
BS, Electrical Engineering, Minor in Computer Science, GPA: 3.5

September 2020–June 2024
San Luis Obispo, CA

California Polytechnic State University, San Luis Obispo (Cal Poly)
Pursuing blended MS, Electrical Engineering

September 2023–June 2025
San Luis Obispo, CA

RELATED COURSEWORK

- Digital Design, Fundamentals of Computer Science, Data Structures, Continuous Time Signals, Electronics Manufacturing, Circuit Analysis, Discrete Mathematics

SKILLS

- **Languages:** Python (pandas, scikit-learn), SystemVerilog, C/C++, Java, RISC-V ISA
- **Hardware:** Multimeter, Oscilloscope, Function Generator, Solder Iron
- **Software:** Xilinx Vivado, Altium, EAGLE, MATLAB, LTspice, Revit, Adobe Premiere Pro
- **Technician Amateur Radio License, FCC, October 2020:** Call Sign: KN6LTD, ID: L02373378

WORK AND VOLUNTEER EXPERIENCE

Hewlett Packard Enterprise
Electrical Engineering Intern

June 2023–September 2023
Milpitas, CA

- Used MATLAB to simulate radiated emissions from a drive carrier which lead to the discovery of an unintentional antenna in the casing
- Ensured compliance standards were met for the development of a hybrid cloud-native storage product

Cal Poly IEEE
President

June 2023–Present
San Luis Obispo, CA

- Newly elected president of Cal Poly IEEE

Silverman and Light
Electrical Engineering Intern

June 2022–September 2022
Emeryville, CA

- Used Revit to design single-line electrical diagrams for office and lab buildings
- Created a python app, DocuPET (document productivity enhancement tool), where 200 document headers could be edited at a time, saving tens of hours

Best Buy Geek Squad
Technology Consultant

July 2019–April 2020
Dublin, CA

- Demonstrated excellent teamwork, time management, customer service, and communication skills
- Repaired and diagnosed Windows and IOS devices using command line languages batch and bash

ELECTRICAL PROJECT EXPERIENCE

- **ETIOILES Power Satellite Research Laboratory Engineer, March 2022–September 2022:** Worked with a team to create electrical power system boards using Altium.
- **Digital Combination Lockbox:** Led a small team and created a digital combination lockbox SystemVerilog program on a FPGA Basys3 board that allowed a user to create and enter 3 sequences of three-bit passwords using a finite state machine and memory registers. If the entered passwords matched the created passwords, the seven-segment display would say “OPEN”.
- **Bounce Back Fall Detection System:** Created Bounce Back, a C/C++ and Bluetooth-based fall detection system that uses an accelerometer to detect when a patient falls and alerts senior center workers with an IOS or Android device. Bounce Back was designed to be price competitive with stand-alone fall buttons because elderly patients often cannot press any emergency buttons when they have a medical emergency.
- **32-bit Computer:** Worked with a team to program a 32-bit computer using RISC-V and SystemVerilog on the Basys 3 FPGA board.
- **Continuity Tester:** Designed and assembled a continuity tester PCB using Eagle and created a 3D modeled continuity tester holder.
- **WaterWise / WaterWatcher:** Pitched a Raspberry Pi algae bloom detection system to the Cal Poly Center of Innovation and Entrepreneurship and presented live during the second round of finalists. Due to the rise in temperatures, water resources are becoming more susceptible to algae blooms, which is why a low maintenance detection system is necessary.

Hobbies and Interests

- I enjoy playing guitar, video editing, 3D printing, Pilates, and going hiking