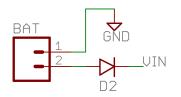
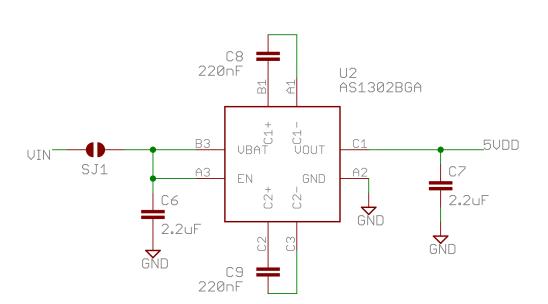
## Power Supply

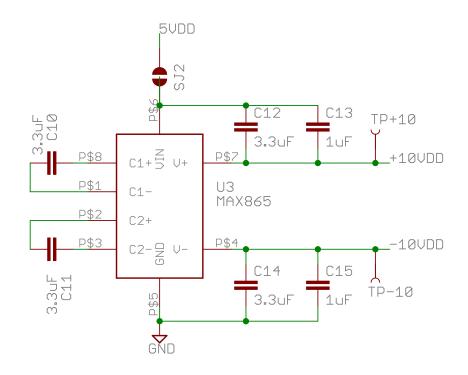
Dual LDO. TPD MIC5392/3 DVDD VOUT1 VIN<sup>-</sup> VOUT2 P\$6 EN1 EN2 P\$4 AVDD C2 С3 TPA 1uF 1uF \_ 1uF P\$7 P\$3 Need to edit the footprint of this 8pin part.

Battery Connector









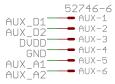
Dual charge pump.

#### PSoC 1uF C27 DVDD DVDD $\ll$ DVDD IR Sync Add series R to phototransistor? SYNC SYNC Q1 P2551 VDD102 P2131 P2131 P2121 P2121 P2151 P1551 VDD VCCD PR671 PR671 PR671 PR671 PR671 P\$51 SS P2[6] P0[3] LED2 P\$2 P\$50 SCLK P2[7] P0[2] Must connect both VCCD LED3 P\$49 MOSI P12[4],SIO pins together with a P0[1] SYNC P\$48 MISO P12[5],SIO P0[0] short trace. P\$47 STIMSEL1 P12[3],SI0 P12[2],SI0 VSSD **USSB** P\$46 STIMSELØ P\$45 P\$44 IND VBOOST VDDA VBAT P\$43 U4 CY8C3866 USSA VSSD Reset Button P\$42 P\$41 P\$40 P\$39 P\$38 VCCA XRES XRES P\$11 P15[3],KHZXI SW1 SWDIO P\$12 P1[0],SWDIO P15[2],KHZXO P12[1],SIO P12[0],SIO P3[7] P1[1],SWDCK SWDCK P\$13 P1[2],XRES RN\_RESET P\$14 P1[3],SWV,TD0 SWV P\$15 UART\_TX P\$16 P\$37 P\$36 P\$35 P1[4],TDI P1[5],NTRST P3[6] UART\_RX VDDIO1 VDDI03 ➤ DVDD C22 DVDD CNTR C20 0.1uF 0.1uF LED's GND UCCD\_ LED1 --\\\\---GND

R21 1k WW R22 1k

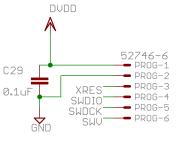
RGB1

### Aux I/O Header



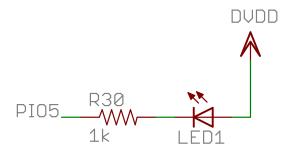
DVDD

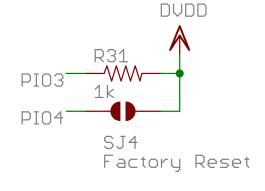
### Programming Header Molex FPC Connector



#### DVDD 813 U10 RN42 30 35 VDD AI00 RESET AIO1 RN\_RESET 7 8 9 10 19 PI02 PCMCLK GPI02 20 \_PI03 GPI03 **PCMSYNC** \_PI04 PCMIN GPI04 \_PI05 **PCMOUT** GPI05 3 GPI06 4 31 32 13 UART\_TX UARTRX GPI07 UART\_RX 14 UARTTX GPI08 UART\_RTS\_ 15 **UARTRTS** GPI09 33 UART\_CTS\_ 16 **UARTCTS** GPI010 34 GPI011 Do I need to add <u>18</u> <u>17</u> USBD-100R series resistors USBD+ for UART? SPICLK GND SPICSB GND1 **SPIMISO** GND2 SPIMOSI GND3

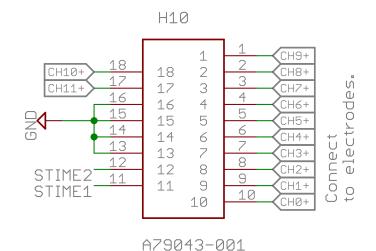
# Bluetooth





#### CH9+ CH2+ CH6+ CH7+ CH8+ CH11+ CH10+ GND 8 IN10+ IN10-IN11+ CH4+ IN11-IN4+ IN12+ IN3-40 CH3+ IN12-IN3+ 39 IN13+ IN2-→ GND IN13-CH2+ IN2+ IN14+ IN1-CH1+ IN14-IN1+ IN15+ INO-U5 CHØ+ IN15-INO+ RHD2216\_QFN ELEC\_TEST GND0 **♪** GND VESD GND1 AVDD GND 4 GND2 VDD3 VDD1 LVDS\_EN AUXIN1 AUXOUT ADC\_REF GND GND4 AUXIN2 AUXIN3 MISO-GND3 SCLK-MOSI-MOSI+ UDD2 CS-CS+ SCLK CENTER CTR 7 7 10nF SS MOSI MISO **→** GND AUDD C40 100nF AVDD

# ADC/Amplifier



Confirm stimulator layout with SRD and new stim board.

## Stimulator

