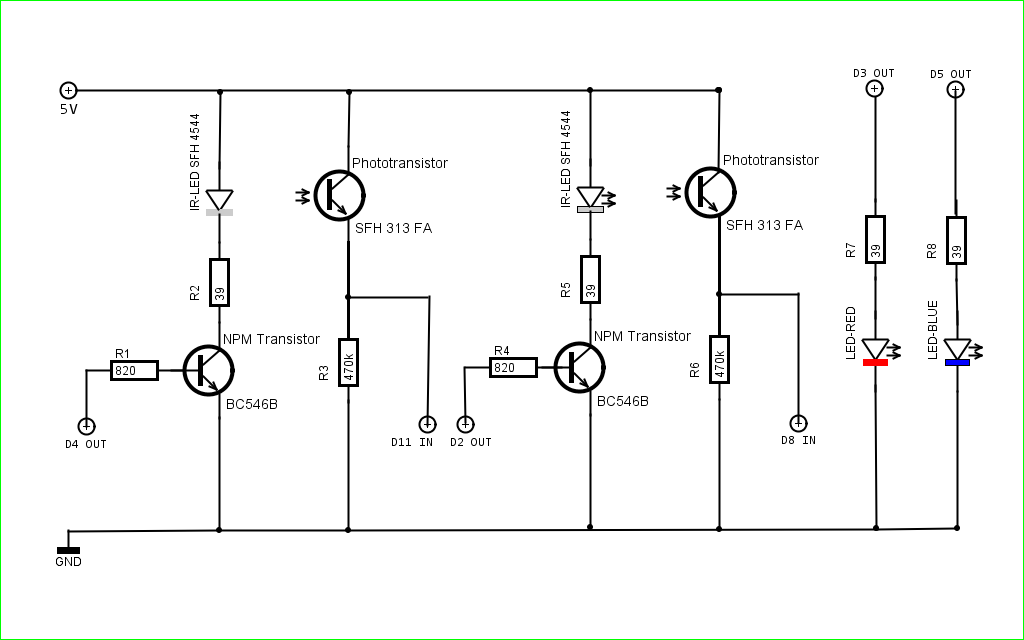
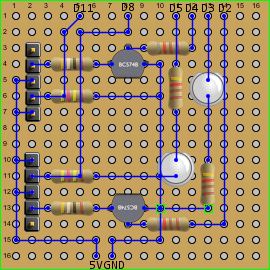
# Schaltplan



# Platinen-Layout



# Platinen-Layout gespiegelt

