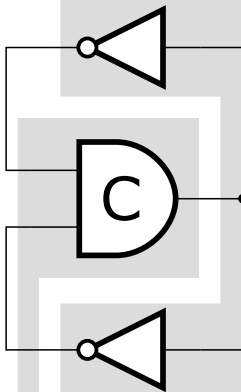


Initial state concept

Environment concept

$a=0$

$b=0$



$c=0$

*System
concept*

Circuit concept