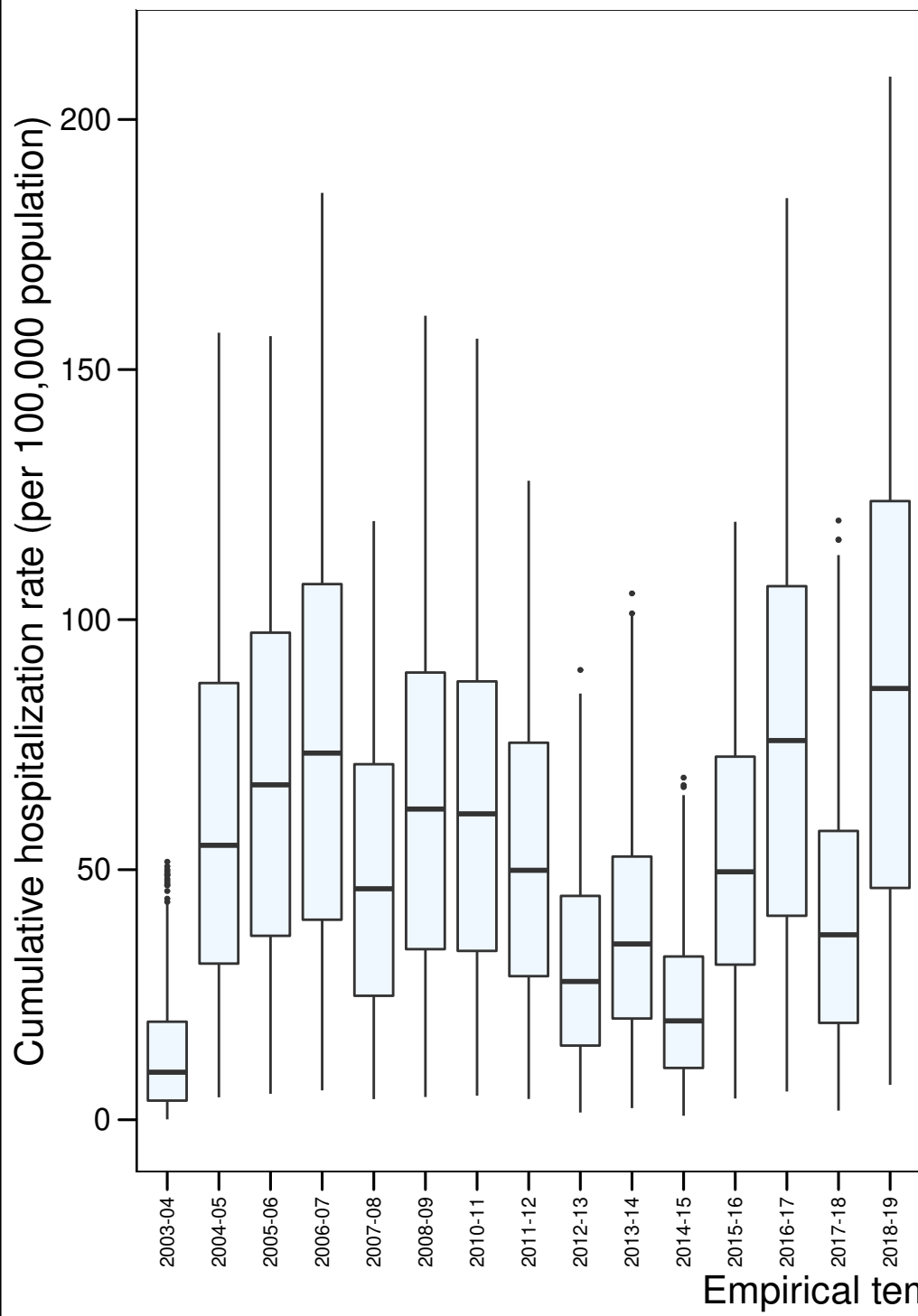


sim.lmin



sim.l1se

