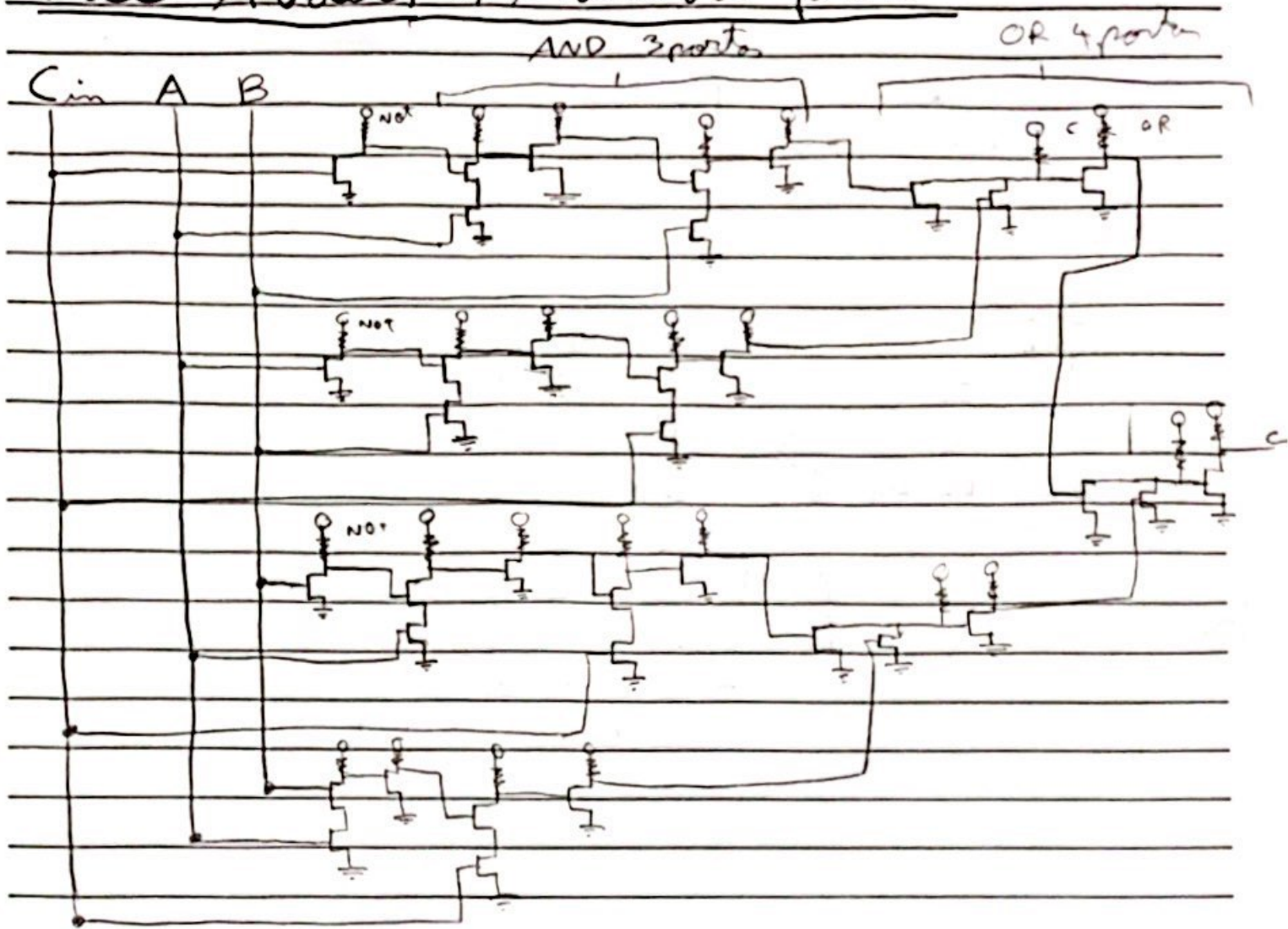


## Full Adders c/ transistors pte 2



\* Saída C