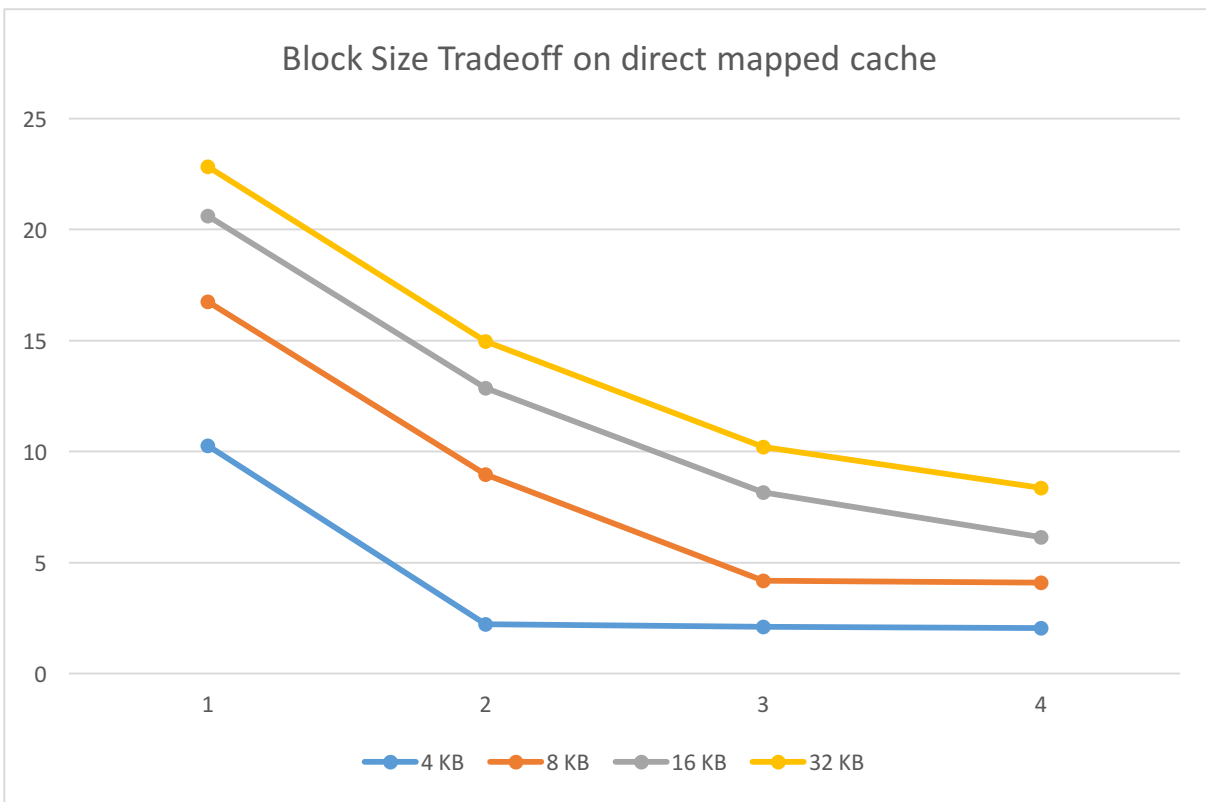*** Simulation base on address trace: "go_ld_trace.txt"

a. Block Size Tradeoff on direct mapped cache.

Block Size (Bytes)	Direct mapped			
	Cache Size (KB)			
	4	8	16	32
4	10.259193	6.492396	3.866931	2.220332
8	2.220332	6.738595	3.913197	2.095532
16	2.095532	2.095532	3.963931	2.045065
32	2.045065	2.045065	2.045065	2.228865

Block Size Tradeoff on direct mapped cache

b. N-way associativity cache with replacement algorithms: Least recently used (LRU), and Round Robin (RR).

Cache Size (KB)	Two-Way		Four-Way	
	LRU	RR	LRU	RR
1	32.937443	34.674175	30.927246	33.479313
4	11.251392	12.328125	8.924661	10.439993
8	4.422997	4.89053	2.697731	3.341798
32	0.647	0.6996	0.503666	0.561733
512	0.1856	0.185067	0.185067	0.185067
1024	0.185333	0.185067	0.185067	0.185067