```
define i32 @example(i32 %n) {
                                                                define i32 @example(i32 %n) {
entry:
                                                                entry:
                                                                                                             At source line 3:
 %y = alloca i32 ① allocates stack space, %y points to this storage
                                                                  @dbg.value(i32 0, "y" l3)
                                                                                                             \vee = 0
  @dbg.declare(i32* %y, "y" l3) ② source var y is stored at %y
                                                                     ① source var y = constant(0)
  store i32 0, i32* %y, l3
                                                                for.cond.cleanup.loopexit:
    ③ stores constant (0) for source var y
                                                                  \%0 = add i32 \%n, -1, 14
                                             At source line 3:
                                                                  %add = add i32 %n, 4
                                                                                                  Value mapping lost, should be:
                                               = 0
for.bodv:
                                                                  %mul = shl i32 %n, 1, l2
                                                                                                  v = %4 = (Add 4)
                                                                  %add1 = add i32 %add, %mul
  %3 = load i32, i32 * %x, l5
                                                                                                    (Add
                                                                                                     (Mul (Add -1 n)
  %add = add i32 %3, 4, 15
                                                                  %1 = mul i32 %0, %add1, l4
                                                                                                      (Add 4
 %4 = load i32, i32* %n.addr, l5
                                                                  \%2 = \text{mul i} 32 \% \text{n}, 3, 14
                                                                                                       (Add n (Shl n 1))))
                                                                  %3 = add i32 %1, %2, 14
  %add1 = add i32 %add, %4, l5
                                                                                                     (Mul 3 n)))
                                                                  %4 = add i32 %3, 4, 14
 \%5 = load i32, i32 * \%y, l5
  %add2 = add i32 %5, %add1, l5
                                                                     ② should be mapped to y, but debug mapping lost!
                                         At source line 5:
  store i32 %add2, i32* %y, l5
                                                                  @dbg.value(i32 undef, "y" l3)
                                          y = (Add 4 (Add))
                                           (Mul 2 n) n))
    4 stores %add2 for source var v
                                                                     3 dead debug mapping without an input value
                    Unoptimised LLVM IR (00)
                                                                                      Optimised LLVM IR (01)
```