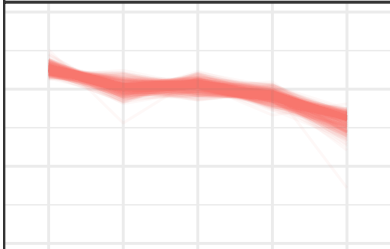
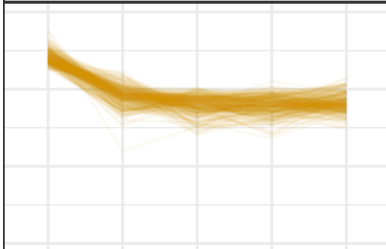


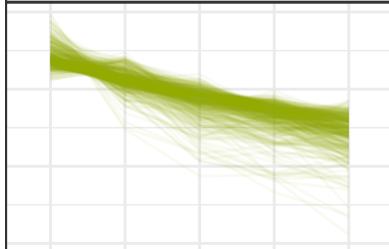
I



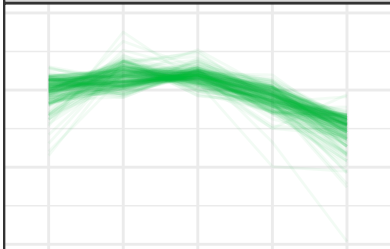
I+H [1]



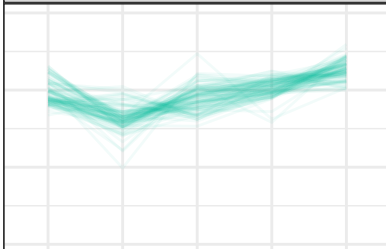
I+H [2]



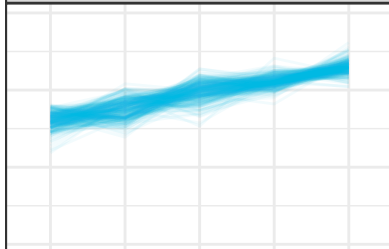
Mix1



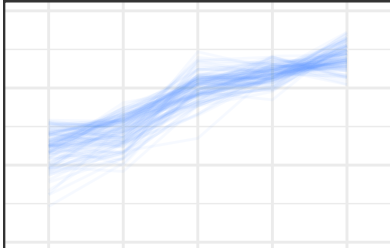
Mix2



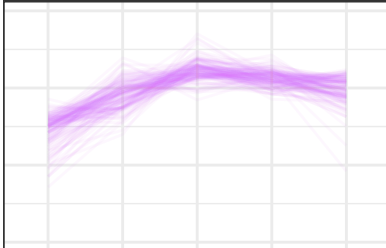
Mix3



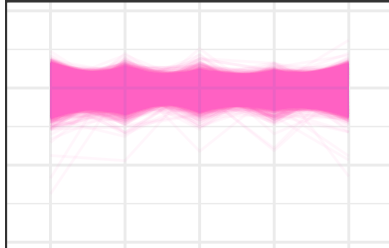
Mix4



Mix5



stable



ATAC_aYA
FPM_ATAC_aD3
FPM_ATAC_aD7
FPM_ATAC_aD10
FPM_ATAC_aD14

ATAC_aYA
FPM_ATAC_aD3
FPM_ATAC_aD7
FPM_ATAC_aD10
FPM_ATAC_aD14

ATAC_aYA
FPM_ATAC_aD3
FPM_ATAC_aD7
FPM_ATAC_aD10
FPM_ATAC_aD14

stage