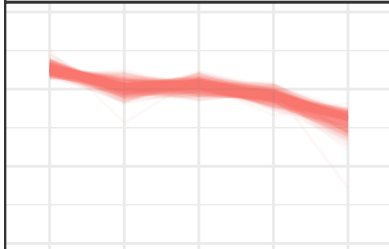
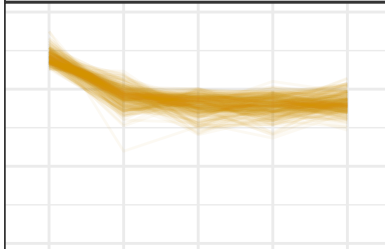


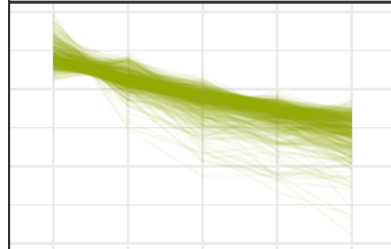
I



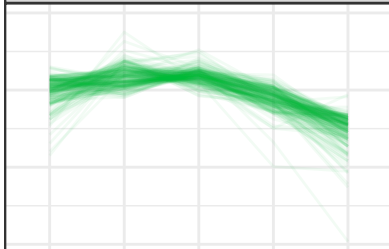
I+H [1]



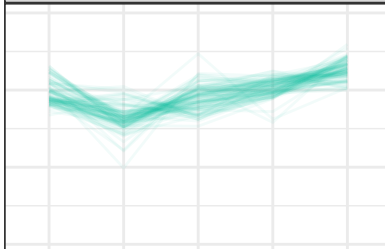
I+H [2]



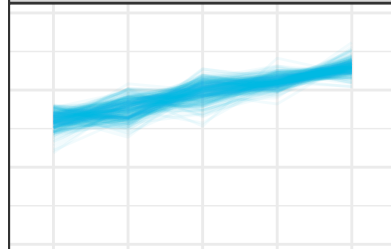
Mix1



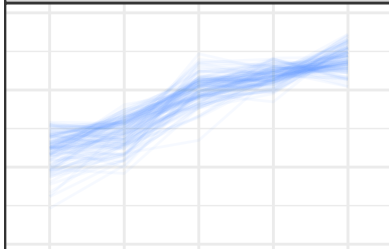
Mix2



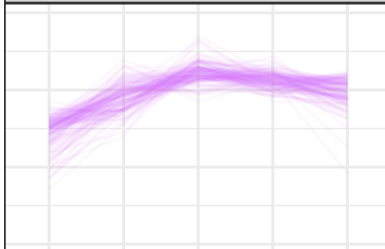
Mix3



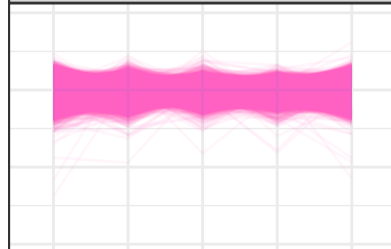
Mix4



Mix5



stable



ATAC\_aYA  
FPM\_ATAC\_aD3  
FPM\_ATAC\_aD7  
FPM\_ATAC\_aD10  
FPM\_ATAC\_aD14

ATAC\_aYA  
FPM\_ATAC\_aD3  
FPM\_ATAC\_aD7  
FPM\_ATAC\_aD10  
FPM\_ATAC\_aD14

ATAC\_aYA  
FPM\_ATAC\_aD3  
FPM\_ATAC\_aD7  
FPM\_ATAC\_aD10  
FPM\_ATAC\_aD14

stage