uiz 4 (Shared mem	nory) https://moodle.xsede.org/mod/quiz/review.php?at
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Starte	ed on Saturday, 1 February 2020, 4:57 PM
	State Finished
Complete	ed on Saturday, 1 February 2020, 5:01 PM
Time t	aken 3 mins 17 secs
N	Marks 18.00/18.00
G	Srade 100.00 out of 100.00
Question 1	Which of the following is not a Shared Memory Programming library/interface?
Correct	
Mark 1.00 out of	Select one:
1.00	a. TBB
	b. OpenMP
	© c. MPI ✓
	d. CILK
	The correct answer is: MPI
Question 2 Correct	In Pthreads if a thread tid1 was created via a fork command, then in order to synchronize and wait for it to finish you have to do
Mark 1.00 out of	Select one:
1.00	Select one:
	a. coend tid1
	b. join tid1 ✓
	c. barrier tid1
	d. wait tid1

The correct answer is: join tid1

Correct Mark 1.00 out of 1.00	element of a large 2d array is Select one: a. a program in Pthreads cannot create more threads than the number of physical cores b. code will not compile c. it can create deadlock upon thread execution d. thread overhead will be very high
	The correct answer is: thread overhead will be very high
Question 4 Correct Mark 1.00 out of 1.00	The type of a thread_id as required by pthread_create is Select one: a. pthread_t b. bool c. long int d. int
	The correct answer is: pthread_t
Question 5 Correct Mark 1.00 out of 1.00	A barrier in Pthreads can Select one: a. synchronize to wait on 1,2 or all threads b. synchronize to wait on any specific number of threads, also all c. only synchronize between threads declared in the same array of pthread_t d. only synchronize between all threads
	The correct answer is: synchronize to wait on any specific number of threads , also all

m. One strong age a me	The name used for a lock within Pthreads is $\frac{\text{nttps://moodle.xsede.org/mod/quiz/review.pnp?a}}{\text{nttps://moodle.xsede.org/mod/quiz/review.pnp?a}}$
Correct	
Mark 1.00 out of	Select one:
1.00	a. lock
	b. mutex ✓
	c. semaphore
	d. mux
	The correct answer is: mutex
Question 7	Semaphores are
Correct	
Mark 1.00 out of	Select one:
1.00	a. a normal type of lock in Pthreads
	b. generalized locks that allow k threads to access them \checkmark
	c. locks that automatically release after a set time to avoid deadlock
	d. locks that prevent the memory accessed by a thread holding a lock from being accessed by any other thread
	The correct answer is: generalized locks that allow k threads to access them
Question 8	Transactional memory is
Correct	Select one:
Mark 1.00 out of 1.00	a. the part of shared memory that is assigned for thread data structures and overhead
	 b. the memory used by cache coherency protocols to keep directories of updated values and locations
	c. an attempt at fast shared memory for use by locks, barriers and other shared memory constructions
	 ■ d. an attempt to implement locks for certain memory locations using extra hardware resources
	The correct answer is: an attempt to implement locks for certain memory locations using

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extra hardware resources

uiz 4 Shaged me Correct Mark 1.00 out of 1.00	Select one: a. library calls b. assembly instructions c. compiler directives d. environment calls
	The correct answer is: compiler directives
Question 10 Correct Mark 1.00 out of 1.00	Which of the following is not easily achieved in converting a code to OpenMP? Select one: a. synchronization across all threads b. easy split of program into serial and parallel regions c. little code modification d. freedom from data races
	The correct answer is: freedom from data races
Question 11 Correct Mark 1.00 out of 1.00	In OpenMP dynamic allocation for loop scheduling should be used Select one: a. whenever the work being done has large variations in execution time and have reasonably small amounts of work b. always c. whenever the work being done has small variations but have large amounts of work d. whenever the work being done has large variations in execution time and have very large amounts of work

The correct answer is: whenever the work being done has large variations in execution time and have reasonably small amounts of work

Correct	the code within the region)?
Mark 1.00 out of	Select one:
1.00	a. #pragma omp parallel 🎺
	b. #pragma omp for
	c. #pragma omp task
	d. #pragma opm critical
	Your answer is correct.
	The correct answer is: #pragma omp parallel
Question 13 Correct	In the OpenMP/Pthreads data comparison presented, the likely cause for poor performance with Pthreads on the smaller ocean was
Mark 1.00 out of	Select one:
1.00	a. non-minimization of surface-to-volume ratio
	b. OpenMP's superrior range of available shared memory options
	c. Pthreads likely implementation consisted of OpenMP commands that weren't
	optomised as well as they could have been
	■ d. thread creation overhead
	The correct answer is: thread creation overhead
Question 14 Correct	In order to have a single-thread region within a parallel OpenMP region we can use which construct below?
Mark 1.00 out of	Select one:
1.00	a. #pragma omp critical
	b. #pragma omp parallel
	c. #pragma omp barrier
	d. #pragma omp master 🇸

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The correct answer is: #pragma omp master

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Qui z 4 (Sharg d mei	^{morX)} critical section' in OpenMP is similar to whith policepted exactly memory of gyria many examples. The properties of the control of the
Correct Mark 1.00 out of 1.00	Select one: a. lock b. fork/join c. semaphore d. barrier
Question 16	The correct answer is: lock When does false sharing happen?
Correct Mark 1.00 out of 1.00	Select one: a. when a single thread writes to memory locations that are far away from each other b. when multiple threads write to the same exact memory location
	 c. when multiple threads write to different but nearby memory locations that fall on the same cache line d. when multiple threads write to memory locations that are far away from each other

Your answer is correct.

The correct answer is: when multiple threads write to different but nearby memory locations that fall on the same cache line

Correct	accessing a shared array
Mark 1.00 out of	Select one or more:
1.00	a. Run that region of the code using a single thread only
	b. Pad the array so that elements accessed by multiple threads fall on distinct cache
	lines. 🗸
	\sim c. Use thread local variables instead of a shared array \checkmark
	d. Disable the cache coherence protocol.
	Your answer is correct.
	The correct answers are: Pad the array so that elements accessed by multiple threads fall
	on distinct cache lines., Use thread local variables instead of a shared array
Question 18	OpenMP's flush (#omp pragma flush) operator forces data to be updated in memory so
Correct	other threads see the most recent value. Flush itself is not a synchronization primitive, however some OpenMP synchronization primitives imply a flush. Mark all OpenMP
Mark 1.00 out of 1.00	synchronization primitives that imply a flush.
	Select one or more:
	a. entry/exit of critical regions 🗸
	b.
	entry to worksharing regions (i.e. #omp pragma for)
	c. whenever a lock is set or unset 🇸
	d. implicit and explicit barriers 🗸
	entry/exit of parallel regions ✓
	Your answer is correct.
	The correct answers are: entry/exit of parallel regions
	, implicit and explicit barriers, entry/exit of critical regions, whenever a lock is set or unset

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