

## ELEEC2202 2013/14

### D1 Design Completion Form

Name: Yubo Zhi

Username: y239g13

Sequencer simulation shows correct functionality	<u>Y. Zhi</u>
Whole system simulation shows correct functionality	<u>B. H</u>
Sequencer for 4-bit multiplication implemented on MachXO2, goes through sequence of states, stops and displays READY	<u>Y. Zhi</u>
4-bit adder implemented and shows correct functionality	<u>Y. Zhi</u>
C, A, Q registers for 4-bit operation implemented and show correct functionality	<u>Y. Zhi</u>
Whole 4-bit system implemented and shows correct functionality	<u>Y. Zhi</u>
ADD and SHIFT combined in a single state	<u>Y. Zhi</u>
Extension to 8 bits implemented and shows correct functionality	<u>Y. Zhi</u>

Supervisor's signature 

Date 21/11/14

Formal report to be submitted through C-Bass/ECS front desk, by 16:00, one calendar week after the scheduled laboratory session.

**Include a copy of this completion form with your formal report.**