ELEC2202 2013/14

D1 Design Completion Form

Name: _Yubo Zhi_

Username: _yz39g13_

Sequencer simulation shows correct functionality	B.H
Whole system simulation shows correct functionality	B.H
Sequencer for 4-bit multiplication implemented on MachXO2, goes through sequence of states, stops and displays READY	TCS
4-bit adder implemented and shows correct functionality	TCS
C,A,Q registers for 4-bit operation implemented and show correct functionality	TCS
Whole 4-bit system implemented and shows correct functionality	TCS
ADD and SHIFT combined in a single state	TCS
Extension to 8 bits implemented and shows correct functionality	TCS

Supervisor's signature _____

Date ___21/11/14_____

Formal report to be submitted through C-Bass/ECS front desk, by 16:00, one calendar week after the scheduled laboratory session.

Include a copy of this completion form with your formal report.