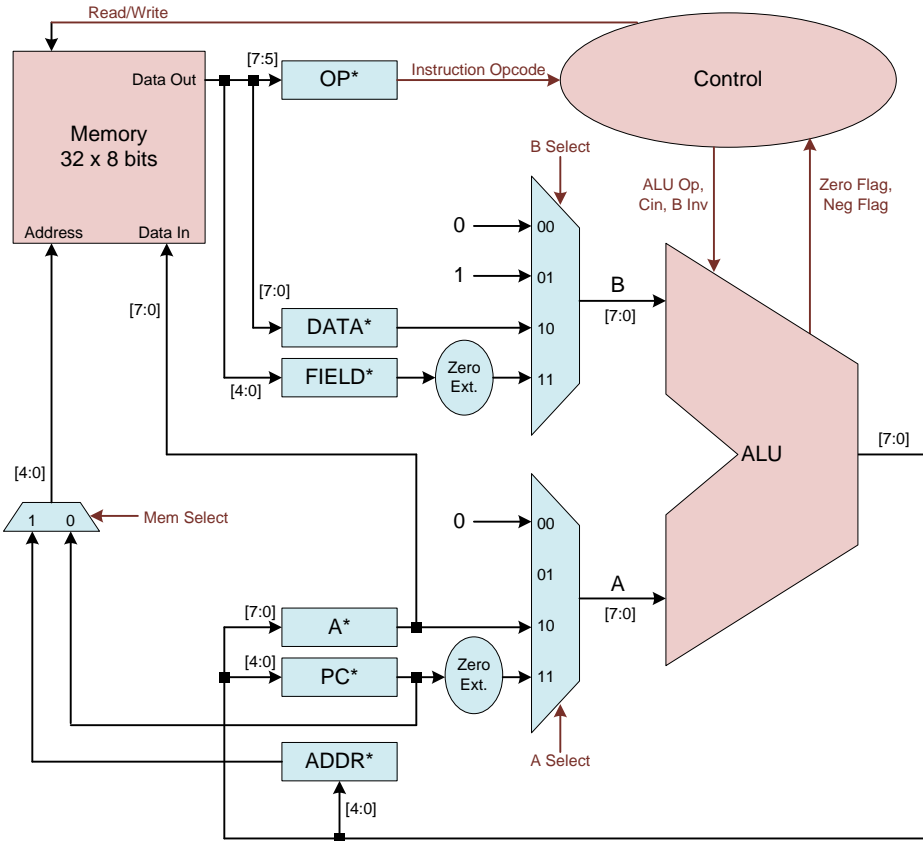


Datapath



**Assume CLK, RESET, and EN control signals go to all registers*