

Feature:	Singlemaster	Multimaster	Slave	Masterslave
Start/Stop condition	I	I	I	I
Acknowledge	I	I	I	I
Synchronization	X	I	X	I
Clockstretching	I	I	I	I
7-bit address	I	I	I	I
Repeated Start	I	I	X	I
10-bit address	N	N	N	N
General Call addr	N	N	N	N
Software Reset	N	N	N	N
Start Byte	X	N	X	N
Device ID	X	X	N	N

I = Implemented

N = Not implemented

X = Not applicable

Timing :

All timing constraints, as described in the user manual (p.48), should be met if the clock of the peripheral is set correctly. The unidirectional ultra fast mode is not supported.