Joseph Sharp Halpin CpE 403 Section 1001 10/9/2018

### Task 01:

Youtube Link: <a href="https://youtu.be/wzHs0uEpBBw">https://youtu.be/wzHs0uEpBBw</a>

# Code:

```
@ main.c ⊠
  1#include <stdint.h>
 1 #include <stdint.h>
2 #include <stdbool.h>
3 #include "inc/hw_memmap.h"
4 #include "inc/hw_types.h"
5 #include "driverlib/sysctl.h"
6 #include "driverlib/gpio.h"
7 #include "driverlib/debug.h"
 8 #include "driverlib/pwm.h"
9 #include "driverlib/pim_map.h"
10 #include "inc/hw_gpio.h"
11 #include "driverlib/rom.h"
13
 14 #define PWM_FREQUENCY 55
 16 int main(void)
 17 {
         volatile uint32_t ui32Load;
volatile uint32_t ui32PWMClock;
 19
          volatile uint8 t ui8Adjust;
         ui8Adjust = 83;
          ROM_SysCtlClockSet(SYSCTL_SYSDIV_5|SYSCTL_USE_PLL|SYSCTL_OSC_MAIN|SYSCTL_XTAL_16MHZ);
          ROM_SysCtlPWMClockSet(SYSCTL_PWMDIV_64);
          ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_PWM1);
          ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOD);
          ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOF);
          //set output PWM pin
          ROM_GPIOPinTypePWM(GPIO_PORTD_BASE, GPIO_PIN_0);
          ROM_GPIOPinConfigure(GPIO_PD0_M1PWM0);
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          //allow SW0 and SW1 to operate
          HWREG(GPIO_PORTF_BASE + GPIO_O_LOCK) = GPIO_LOCK_KEY;
         HWREG(GPIO_PORTF_BASE + GPIO_O_CR) | = 0x01;
HWREG(GPIO_PORTF_BASE + GPIO_O_LOCK) = 0;
ROM_GPIODirModeSet(GPIO_PORTF_BASE, GPIO_PIN_4|GPIO_PIN_0, GPIO_DIR_MODE_IN);
          ROM_GPIOPadConfigSet(GPIO_PORTF_BASE, GPIO_PIN_4|GPIO_PIN_0, GPIO_STRENGTH_2MA, GPIO_PIN_TYPE_STD_WPU);
          //get PWM clock to set the period
          ui32PWMClock = SysCtlClockGet() / 64;
          ui32Load = (ui32PWMClock / PWM_FREQUENCY) - 1;
         PWMGenConfigure(PwM1_BASE, PwM_GEN_0, PwM_GEN_MODE_DOWN);
PWMGenPeriodSet(PwM1_BASE, PwM_GEN_0, ui32Load);
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          //initalize the duty cycle and enable the PWM
          ROM_PWMPulseWidthSet(PWM1_BASE, PWM_OUT_0, ui8Adjust * ui32Load / 1000);
          ROM_PWMOutputState(PWM1_BASE, PWM_OUT_0_BIT, true);
         ROM_PWMGenEnable(PWM1_BASE, PWM_GEN_0);
              //check if SW1 is pressed and adjust the \underline{\texttt{SECNO}} motor if(ROM_GPIOPinRead(GPIO_PORTF_BASE,GPIO_PIN_4)==0x00)
                   ui8Adjust--;
if (ui8Adjust < 1)
                         ui8Adjust = 1;
                    ROM_PWMPulseWidthSet(PWM1_BASE, PWM_OUT_0, ui8Adjust * ui32Load / 1000);
               //check if SWO is pressed and adjust the servo motor
              if(ROM_GPIOPinRead(GPIO_PORTF_BASE,GPIO_PIN_0)==0x00)
                    ui8Adjust++;
                    if (ui8Adjust > 120)
                        ui8Adjust = 120;
                    ROM_PWMPulseWidthSet(PWM1_BASE, PWM_OUT_0, ui8Adjust * ui32Load / 1000);
              //delay for 100000 cycles
ROM_SysCtlDelay(100000);
82
83 }
```

## **Task 02:**

Youtube Link: https://youtu.be/6oyUPCQ3CdA

Code:

```
c main.c ⊠
 1 #include <stdint.h>
  2 #include <stdbool.h>
 3 #include "inc/hw_memmap.h"
 4#include "inc/hw_types.h"
5#include "driverlib/sysctl.h"
 6 #include "driverlib/gpio.h"
7 #include "driverlib/debug.h"
/#include driverlib/debug.n

##include "driverlib/pwm.h"

9 #include "driverlib/pin_map.h"

10 #include "inc/hw_gpio.h"

11 #include "driverlib/rom.h"
13 #define PWM FREQUENCY 55
15 int main(void)
16 {
17
        volatile uint32_t ui32Load;
18
        volatile uint32_t ui32PWMClock;
19
        int i;
20
21
        //set clock rate
22
        ROM_SysCtlClockSet(SYSCTL_SYSDIV_5|SYSCTL_USE_PLL|SYSCTL_OSC_MAIN|SYSCTL_XTAL_16MHZ);
23
24
        //enable PWM1 and GPIOF
25
        ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_PWM1);
26
        ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOF);
        //set PWM clock rate
 29
        ROM SysCtlPWMClockSet(SYSCTL PWMDIV 64);
30
        //configure the PWM and PF1
31
        HWREG(GPIO_PORTF_BASE + GPIO_O_LOCK) = GPIO_LOCK_KEY;
32
        HWREG(GPIO_PORTF_BASE + GPIO_O_CR) |= 0x01;
33
 34
        ROM_GPIOPinConfigure(GPIO_PF1_M1PWM5);
35
        ROM_GPIOPinTypePWM(GPIO_PORTF_BASE, GPIO_PIN_1);
37
        //configure the PWM
 38
        PWMGenConfigure(PWM1_BASE, PWM_GEN_2, PWM_GEN_MODE_DOWN);
39
        PWMGenConfigure(PWM1 BASE, PWM GEN 3, PWM GEN MODE DOWN);
 40
 41
        //get the period
42
        ui32PWMClock = SysCtlClockGet() / 64;
        ui32Load = (ui32PWMClock / PWM_FREQUENCY) - 1;
44
45
        //set the PWM period
        PWMGenPeriodSet(PWM1_BASE, PWM_GEN_2, ui32Load);
PWMGenPeriodSet(PWM1_BASE, PWM_GEN_3, ui32Load);
46
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49
        //set the PWM duty cycle
50
        PWMPulseWidthSet(PWM1_BASE, PWM_OUT_5, ui32Load * 0.1);
 52
        //enable PWM1
 53
        PWMGenEnable(PWM1_BASE, PWM_GEN_2);
        PWMGenEnable(PWM1 BASE, PWM GEN 3);
 55
 56
57
        PWMOutputState(PWM1_BASE, PWM_OUT_5_BIT, true);
 58
59
        while(1)
             //go from 10% duty cycle to 90% for(i = ui32Load * 0.1; i < ui32Load * 0.9; i++)
 60
 61
 62
                   //reset the duty cycle
                  PWMPulseWidthSet(PWM1_BASE, PWM_OUT_5, i);
 65
                  ROM_SysCtlDelay(20000);
 66
        }
68 }
```

#### **Task 03:**

Youtube Link: https://youtu.be/-9-JnY1XDV4

# Code:

```
€ main.c 🛭
 1 #include <stdint.h>
  2 #include <stdbool.h>
 3 #include "inc/hw_memmap.h"
 #include "inc/hw_types.h"
5 #include "driverlib/sysctl.h"
6 #include "driverlib/gpio.h"
 7 #include "driverlib/debug.h"
8 #include "driverlib/pwm.h"
 9 #include "driverlib/pin_map.h"
10 #include "inc/hw_gpio.h"
11 #include "driverlib/rom.h"
12
14 #define PWM FREQUENCY 55
15
16 int main(void)
17 {
 18
        volatile uint32_t ui32Load;
       volatile uint32_t ui32PWMClock;
 19
 20
       int i, j, k;
 21
 22
        //set clock rate
 23
       ROM_SysCtlClockSet(SYSCTL_SYSDIV_5|SYSCTL_USE_PLL|SYSCTL_OSC_MAIN|SYSCTL_XTAL_16MHZ);
 24
 25
        //enable PWM1 and GPIOF
 26
        ROM SysCtlPeripheralEnable(SYSCTL PERIPH PWM1);
 27
       ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOF);
 28
 29
        //set PWM clock rate
 30
       ROM_SysCtlPWMClockSet(SYSCTL_PWMDIV_64);
 31
32
        //allow PWM to be edited
 33
       HWREG(GPIO_PORTF_BASE + GPIO_O_LOCK) = GPIO_LOCK_KEY;
 34
       HWREG(GPIO_PORTF_BASE + GPIO_O_CR) |= 0x01;
 35
 36
        //configure the LED pins
 37
        ROM_GPIOPinConfigure(GPIO_PF1_M1PWM5);
 38
        ROM_GPIOPinConfigure(GPIO_PF2_M1PWM6);
39
40
        ROM_GPIOPinConfigure(GPIO_PF3_M1PWM7);
        ROM_GPIOPinTypePWM(GPIO_PORTF_BASE, GPIO_PIN_1 | GPIO_PIN_2 | GPIO_PIN_3);
 41
42
43
44
        //configure the PWM
        PWMGenConfigure(PWM1_BASE, PWM_GEN_2, PWM_GEN_MODE_DOWN);
        PWMGenConfigure(PWM1_BASE, PWM_GEN_3, PWM_GEN_MODE_DOWN);
 45
46
        //get the period
        ui32PWMClock = SysCtlClockGet() / 64;
        ui32Load = (ui32PWMClock / PWM_FREQUENCY) - 1;
```

```
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47
48
        //get the period
        ui32PWMClock = SysCtlClockGet() / 64;
        ui32Load = (ui32PWMClock / PWM FREQUENCY) - 1;
//set the PWM period
PWMGenPeriodSet(PWM1_BASE, PWM_GEN_2, ui32Load);
        PWMGenPeriodSet(PWM1_BASE, PWM_GEN_3, ui32Load);
        //set the duty cycle of the PWM
        PWMPulseWidthSet(PWM1_BASE, PWM_OUT_5, ui32Load * 0.9);
        PWMPulseWidthSet(PWM1_BASE, PWM_OUT_6, ui32Load * 0.9);
PWMPulseWidthSet(PWM1_BASE, PWM_OUT_7, ui32Load * 0.9);
        //enable the PWM
        PWMGenEnable(PWM1_BASE, PWM_GEN_2);
        PWMGenEnable(PWM1_BASE, PWM_GEN_3);
        //set the PWM to output
        PWMOutputState(PWM1_BASE, PWM_OUT_5_BIT | PWM_OUT_6_BIT | PWM_OUT_7_BIT, true);
        while(1)
             //make the red led go from 90% duty cycle to 10% for(i = ui32Load * 0.9; i > ui32Load * 0.1; i--)
                  PWMPulseWidthSet(PWM1_BASE, PWM_OUT_5, i);
                  ROM_SysCtlDelay(10000);
                  //make the green led go from 90% duty cycle to 10% for(j = ui32Load * 0.9; j > ui32Load * 0.1; j--)
                  {
                        PWMPulseWidthSet(PWM1_BASE, PWM_OUT_6, j);
                       ROM_SysCtlDelay(10000);

//make the blue led go from 90% duty cycle to 10%

for(k = ui32Load * 0.9; k > ui32Load * 0.1; k--)
                             PWMPulseWidthSet(PWM1_BASE, PWM_OUT_7, k);
                             ROM_SysCtlDelay(10000);
                      }
           }
        }
```