

Lab 1 — GHDL and GTKWave

- Go to the [GitHub repository](#) of Digital System Design (DSD)
 - Study VHDL and GHDL
- Go to the [GHDL](#) folder
 - Install GHDL and GTKWave
 - Run the Half Adder example
 - Run another example such as D Flip-Flop or 4-to-1 Multiplexer
 - Document the results on your GitHub repository
- Exploration: [Icarus Verilog](#)