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Lab # 2

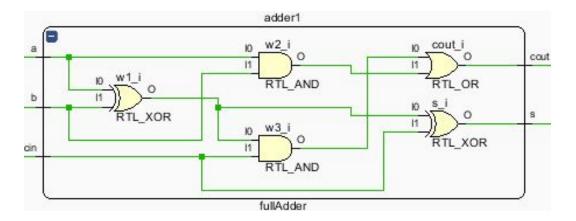
Lab Section - 1B

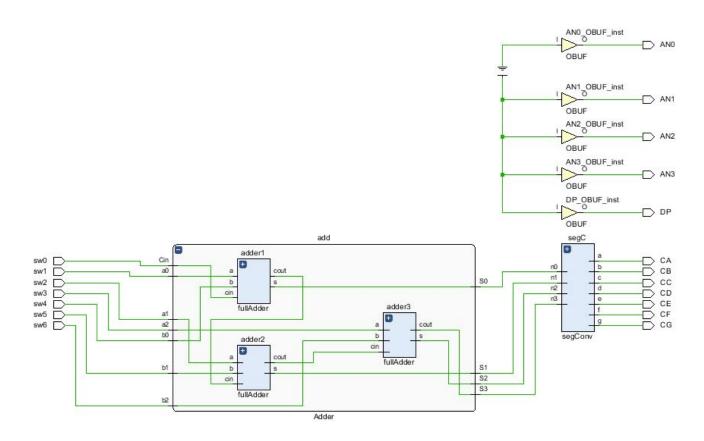
1/24/2020

For this lab, the objective was to create a 7bit adder to add 2, 3bit numbers and one carry in. It was also a test of how to nest different functions in a hierarchy. My hierarchy was as follows

- Main Pieced all the functions together with the board.
 - Adder Combined each of the three full adders
 - Full Adder Contained the gates to give a Sum and Carry out, when given an Ain, Bin, and Carry in
 - 7seg translated the 4 bit number into something that can be displayed on the seven segment screen on the board.

This resulted in the following schematic:





The 7seg part was the hardest part because it involved making a long truth table, tying the 4 bit number to segments being turned on, on the board. I then had to make a sum of products for what turned each segment of the display on. The display was also activated low, as in a 0 bit turned on the segment. I was able to get it done without having to debug any errors.

