

EE5683: Computer Architecture

Course Schedule

Homework and Project Assignments and Submission Areas will be available in the Homework and Projects area on the left panel...

Assignment		Points
Module 1: Introductio	n and Performance Metrics (1/9/2017 - 1/15/2017)	
M1 Quiz	Available: Monday 1/9 - Sunday 1/15	10
Module 2: Instruction	Set Architecture (1/16 - 1/29)	
Homework 1	Due: Friday 1/27	100
M2 Quiz	Available: Monday 1/16 - Sunday 1/29	10
Module 3: Instruction	Level Parallelism (1/30 - 2/12)	
Homework 2	Due: Friday 2/10	100
M3 Quiz	Available: Monday 1/30 - Sunday 2/12	10
Module 4: Thread Leve	el Parallelism (2/13- 2/26)	
M4 Quiz	Available: Monday 2/13 - Sunday 2/26	10
Midterm	Available: Monday 2/27 - Sunday 3/5	100
Spring Break (3/6 - 3/	12)	
Module 5: Cache and I	Memory (3/13 - 3/26)	
Project 1	Due: Friday 3/24	100
Homework 3	Due: Friday 3/24	100
M5 Quiz	Available: Monday 3/13 - Sunday 3/26	10
Module 6: Multiproces	sors (3/27 - 4/16)	
Project 2	Due: Friday 4/14	100
M6 Quiz	Available: Monday 3/27 - Sunday 4/16	10
Module 7: On-Chip Ne	tworks (4/17 - 4/23)	'
Homework 4	Due: Friday 4/21	100
M7 Quiz	Available: Monday 4/17 - Sunday 4/23	10
Final	Available: Monday 4/24 - Sunday 4/30	100

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Updated 11/29/16