

**SEARCH REQUEST FORM****2600****Scientific and Technical Information Center**Requester's Full Name Zele Krista Examiner # **67644** Date: 08/07/06Art Unit: 2600 Phone Number 2-7288 Serial Number 11/408,669

Mail Box Location: jeff 2d61

Results Format Preferred (circle): PAPER DISK E-MAIL

**If more than one search is submitted, please prioritize searches in order of need.**

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Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: \_\_\_\_\_

Inventors (please provide full names): \_\_\_\_\_

Earliest Priority Filing Date: \_\_\_\_\_

\*For Sequence Searches Only\* Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.

US6320574

**STAFF USE ONLY****Type of Search****Vendors and cost where applicable**Searcher: Kimberly Johnson

Sequence (#) \_\_\_\_\_

STN \_\_\_\_\_

Searcher Phone #: \_\_\_\_\_

AA Sequence (#) \_\_\_\_\_

Dialog \_\_\_\_\_

Searcher Location: \_\_\_\_\_

Structure (#) \_\_\_\_\_

Questel/Orbit \_\_\_\_\_ \*

Date Searcher Picked Up: 8/07/06

Bibliographic \_\_\_\_\_

WEST \_\_\_\_\_

Date Completed: 8/07/06

Litigation \_\_\_\_\_ \*

Lexis/Nexis \_\_\_\_\_ \*

Searcher Prep &amp; Review Time: \_\_\_\_\_

Fulltext \_\_\_\_\_

Sequence Systems \_\_\_\_\_

Clerical Prep Time: \_\_\_\_\_

Patent Family \_\_\_\_\_

WWW/Internet \_\_\_\_\_

Online Time: 30


Other \_\_\_\_\_

Courtlink (specify) \_\_\_\_\_ \*

## Query/Command : prt max legalall


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1 / 1 PLUSPAT - ©QUESTEL-ORBIT

**PN** -  US6320574 B1 20011120 [US6320574]  
**TI** - (B1) Circuit and method for generating pixel data elements from analog image data and associated synchronization signals  
**PA** - (B1) GENESIS MICROCHIP CORP (US)  
**PA0** - Genesis Microchip, Corporation, Alviso CA [US]  
**IN** - (B1) EGLIT ALEXANDER J (US)  
**AP** - US8207098 19980520 [1998US-0082070]  
**FD** - Cont. of US803824 19970224 [1997US-0803824]  
Continuation of: US5796392  
**PR** - US8207098 19980520 [1998US-0082070]  
US80382497 19970224 [1997US-0803824]  
**IC** - (B1) G09G-005/12  
**ICAA** - G09G-003/20 [2006-01 A - I R M EP]; H03L-007/07 [2006-01 A - I R M EP];  
H03L-007/085 [2006-01 A - I R M EP]; H03L-007/093 [2006-01 A - I R M EP]  
**ICCA** - G09G-003/20 [2006 C - I R M EP]; H03L-007/07 [2006 C - I R M EP]; H03L-  
007/08 [2006 C - I R M EP]  
**EC** - G09G-003/20  
H03L-007/07  
H03L-007/085  
H03L-007/093  
**PCL** - ORIGINAL (O) : 345213000; CROSS-REFERENCE (X) : 345501000  
348512000 348537000  
**DT** - Corresponding document  
**CT** - US4018990; US4346407; US4488170; US4527145; US4554582; US4590602;  
US4616259; US4686560; US4694327; US4700217; US4703340; US4802009;  
US4893319; US5005079; US5010403; US5086295; US5101197; US5184091;  
US5185603; US5202669; US5280345; US5335018; US5359366; US5459766;  
US5497202; US5515108; US5528307; US5539473; US5574406; US5623311;  
EP0299724 A2; EP0405523 A2; EP0609843 A1; EP615222 A1; JP56-137736;  
JP63-14522; JP5-22277; JP7-334141; JP8-172353; JP8-340254; WO8905065  
**STG** - (B1) U.S. Patent (no pre-grant pub.) after Jan. 2, 2001  
**AB** - A clock recovery circuit in a digital display unit for recovering a time reference  
signal associated with analog display data. The clock recovery circuit includes a  
phase-locked loop (PLL) implemented in digital domain and an analog filter to  
eliminate any undesirable frequencies from the output signal of the PLL. The  
PLL includes independent control loops to track long term frequency drifts of the  
time reference signal and the transient phase differences respectively. By  
providing such independent control loops, the generated clock can be better  
synchronized with the time reference signal.  
**UP** - 2001-48


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1 / 1 LGST - ©EPO

**PN** -  US6320574 B1 20011120 [US6320574]  
**AP** - US8207098 19980520 [1998US-0082070]  
**ACT** - 20041019 US/RF-A  
REISSUE APPLICATION FILED  
EFFECTIVE DATE: 20031120  
**UP** - 2004-46

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*1 / 1 CRXX - ©CLAIMS/RRX*

**PN** -  6,320,574 A 20011120 [US6320574]  
**PA** - Genesis Microchip Corp  
**ACT** - 20031120 REISSUE REQUESTED  
ISSUE DATE OF O.G.: 20041019  
REISSUE REQUEST NUMBER: 10/720001  
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2673

Reissue Patent Number:

Search statement 4

LEVEL 1 - 1 OF 1 PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6320574

November 20, 2001

Circuit and method for generating pixel data elements from  
analog image data and associated synchronization signals

REISSUE: November 20, 2003 - Reissue Application filed Ex. Gp.: 2673; Re. S.N.  
10/720,001 (O.G. October 19, 2004)

APPL-NO: 082070 (09)

FILED-DATE: May 20, 1998

GRANTED-DATE: November 20, 2001

CORE TERMS: display, frequency, clock, analog, digital, phase, correction,  
pixel, loop, screen ...

**LEXIS-NEXIS**  
**Library: PATENTS**  
**File: ALL**

6,320,574 OR 6320574

**LEXIS-NEXIS**  
**Library: PATENTS**  
**File: CASES**

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For further explanation, press the H key (for HELP) and then the ENTER key.

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**LEXIS-NEXIS**  
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**File: JNLS**

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6,320,574 OR 6320574

**LEXIS-NEXIS**  
**Library: PATENTS**  
**File: CURNWS**

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Patent Search 6320574 8/7/2006

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(Charges for search still apply)

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