

FIG. 1

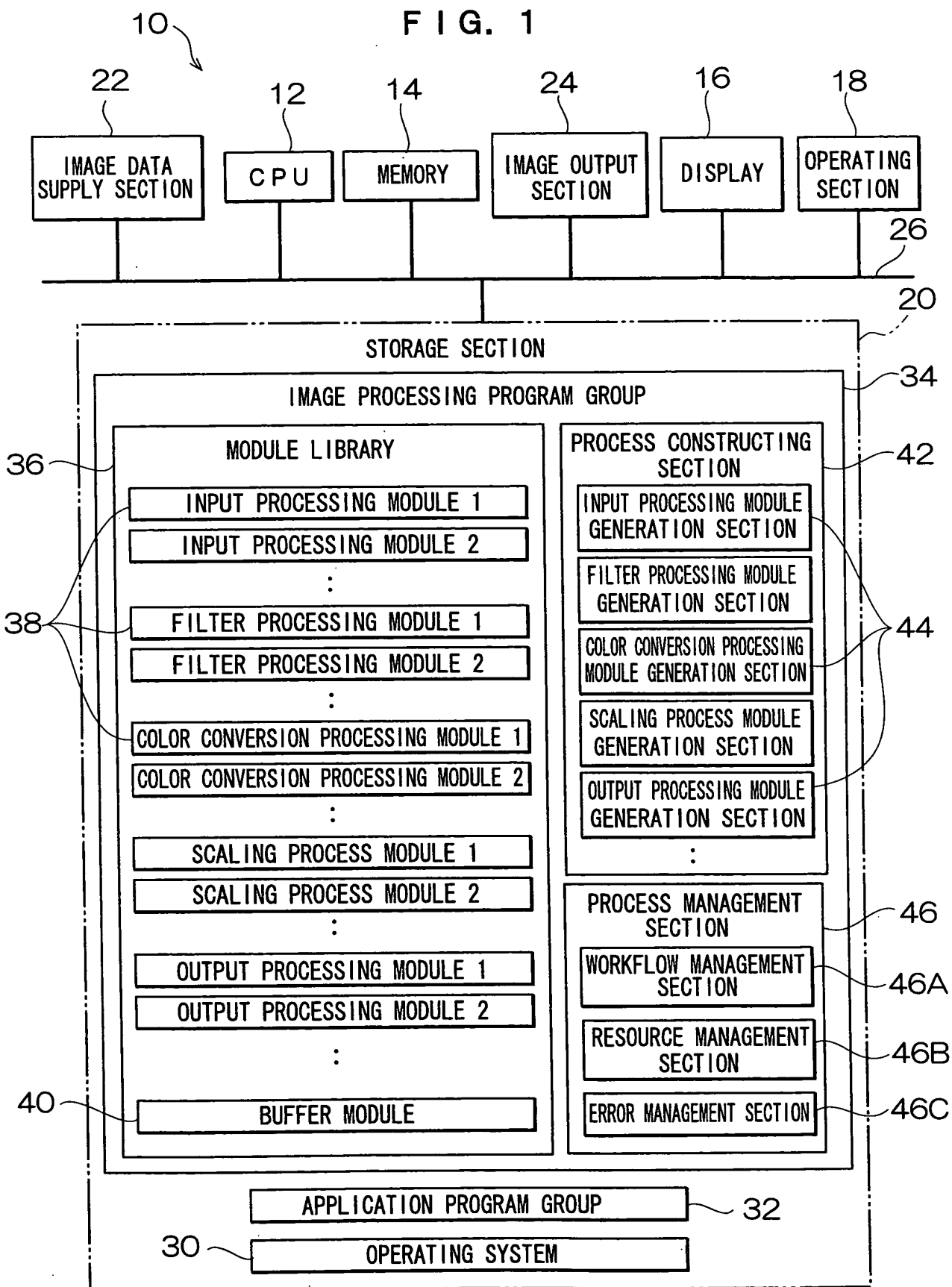


FIG. 2A

STRUCTURAL EXAMPLES OF IMAGE PROCESSING SECTION

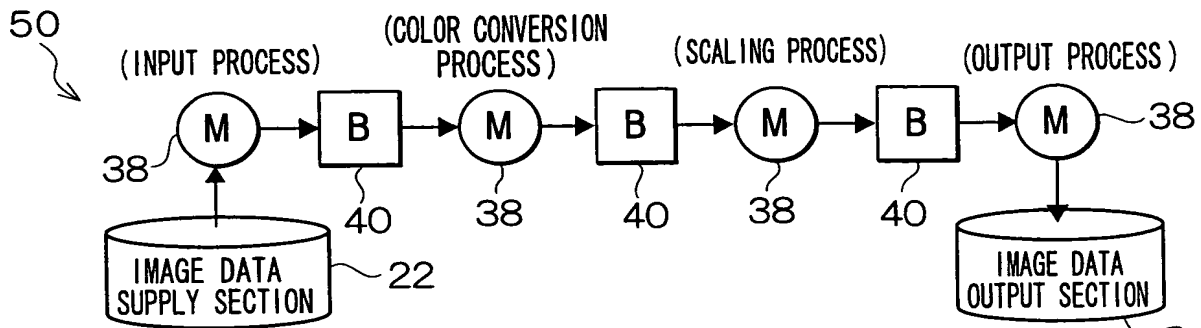


FIG. 2B

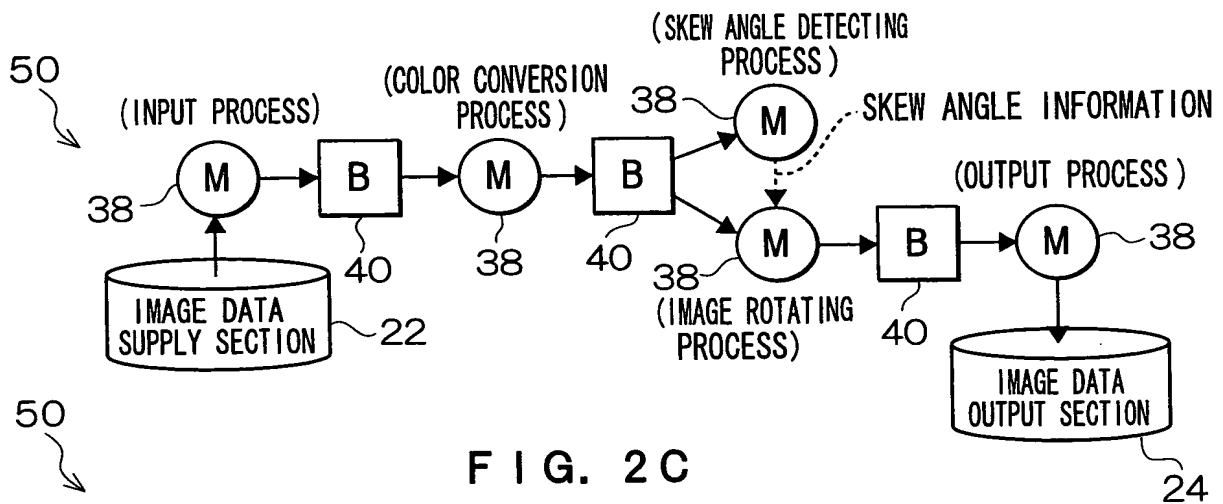
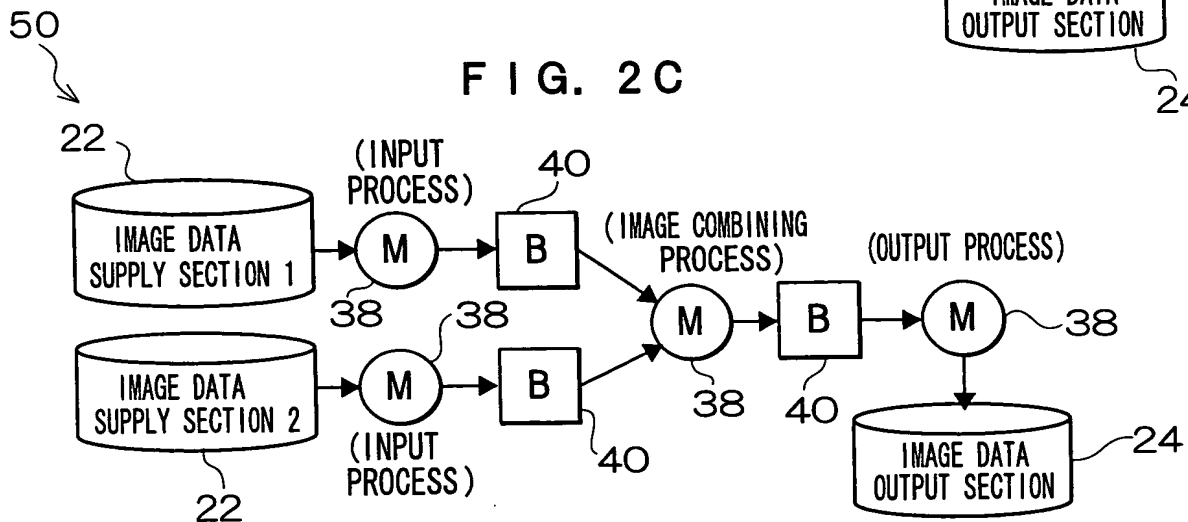


FIG. 2C



Wherein, (M) : IMAGE PROCESSING MODULE (B) : BUFFER MODULE

FIG. 3A

SCHEMATIC STRUCTURE AND PROCESS OF IMAGE PROCESSING MODULE

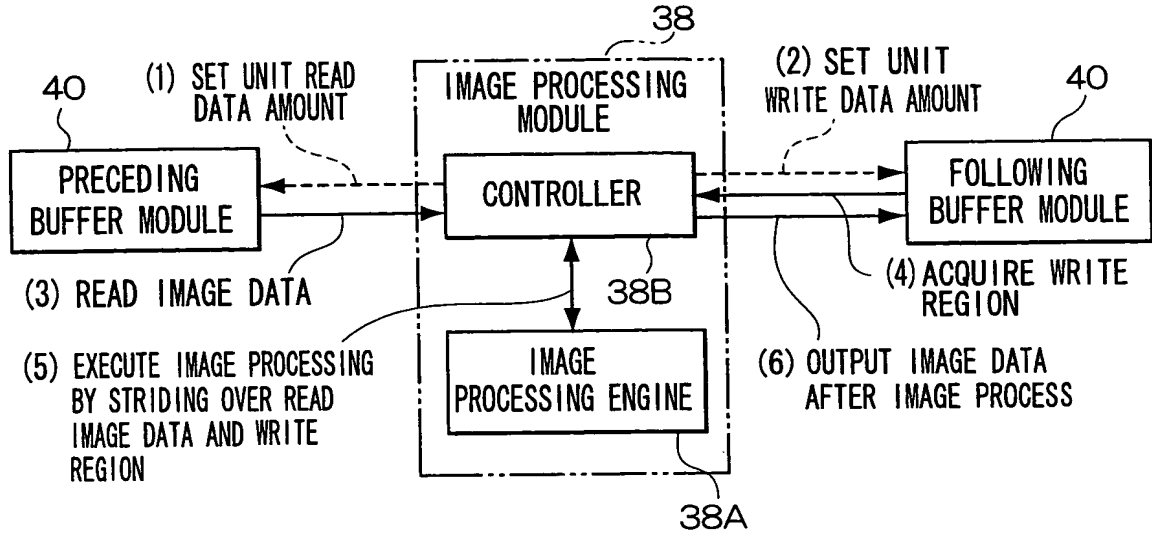


FIG. 3B

SCHEMATIC STRUCTURE AND PROCESS OF BUFFER MODULE (CASE THAT IMAGE DATA REQUESTED FROM FOLLOWING STAGE IS NOT STORED IN BUFFER)

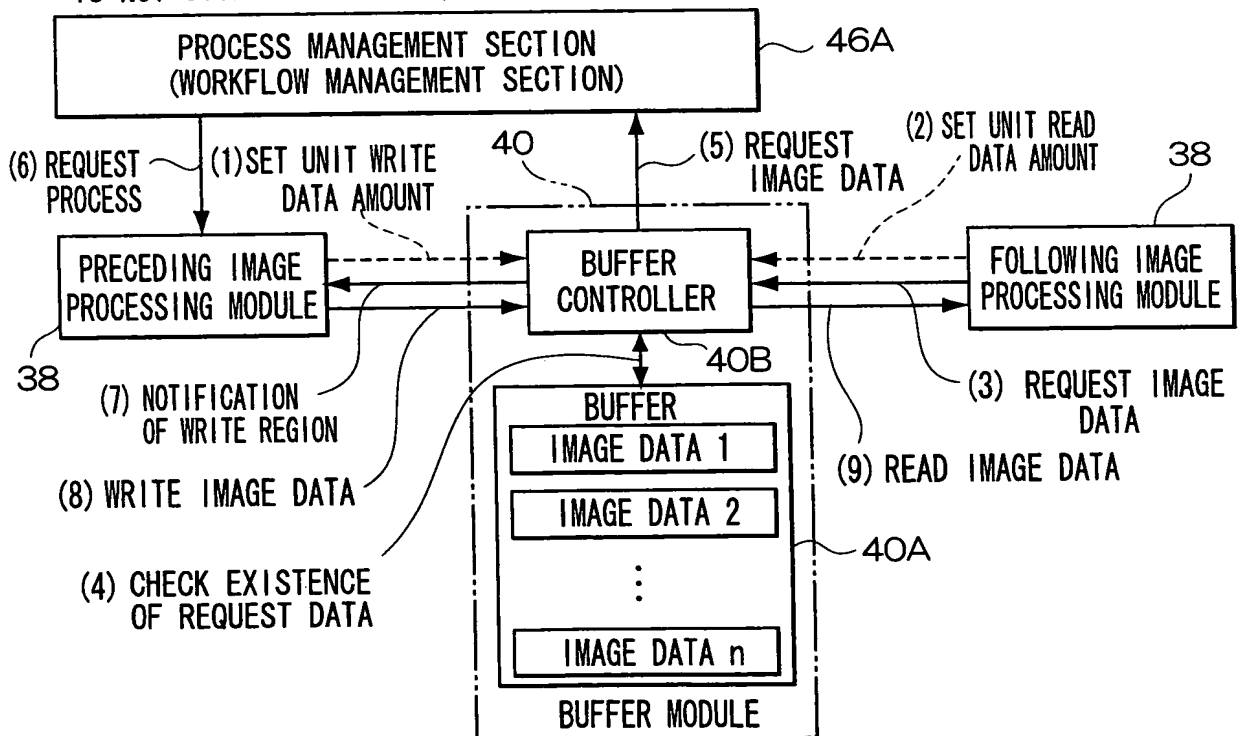
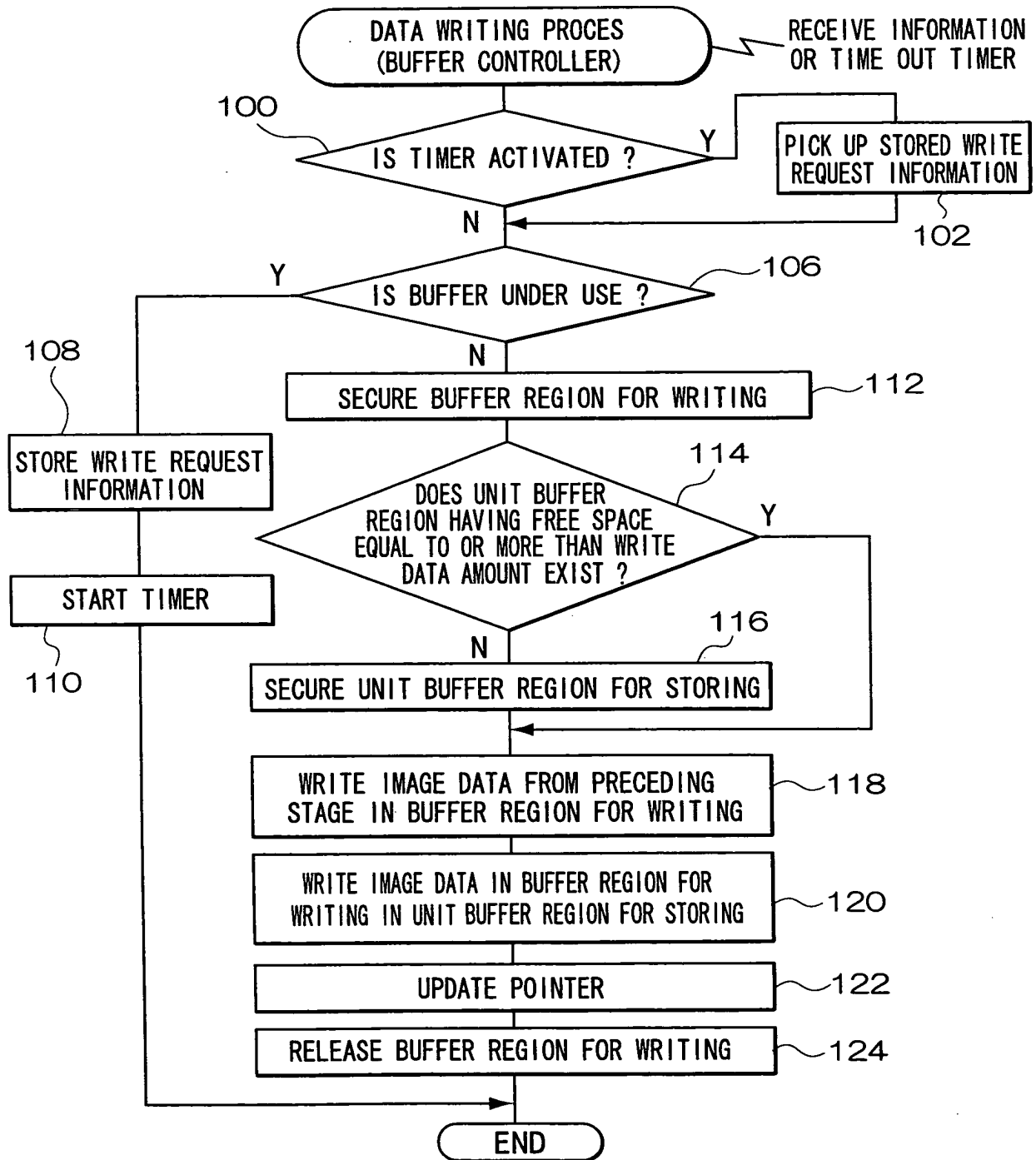


FIG. 4



PROCESS IN CASE THAT IMAGE DATA OF WRITTEN OBJECT STRIDES OVER
PLURAL UNIT BUFFER REGIONS FOR STORING

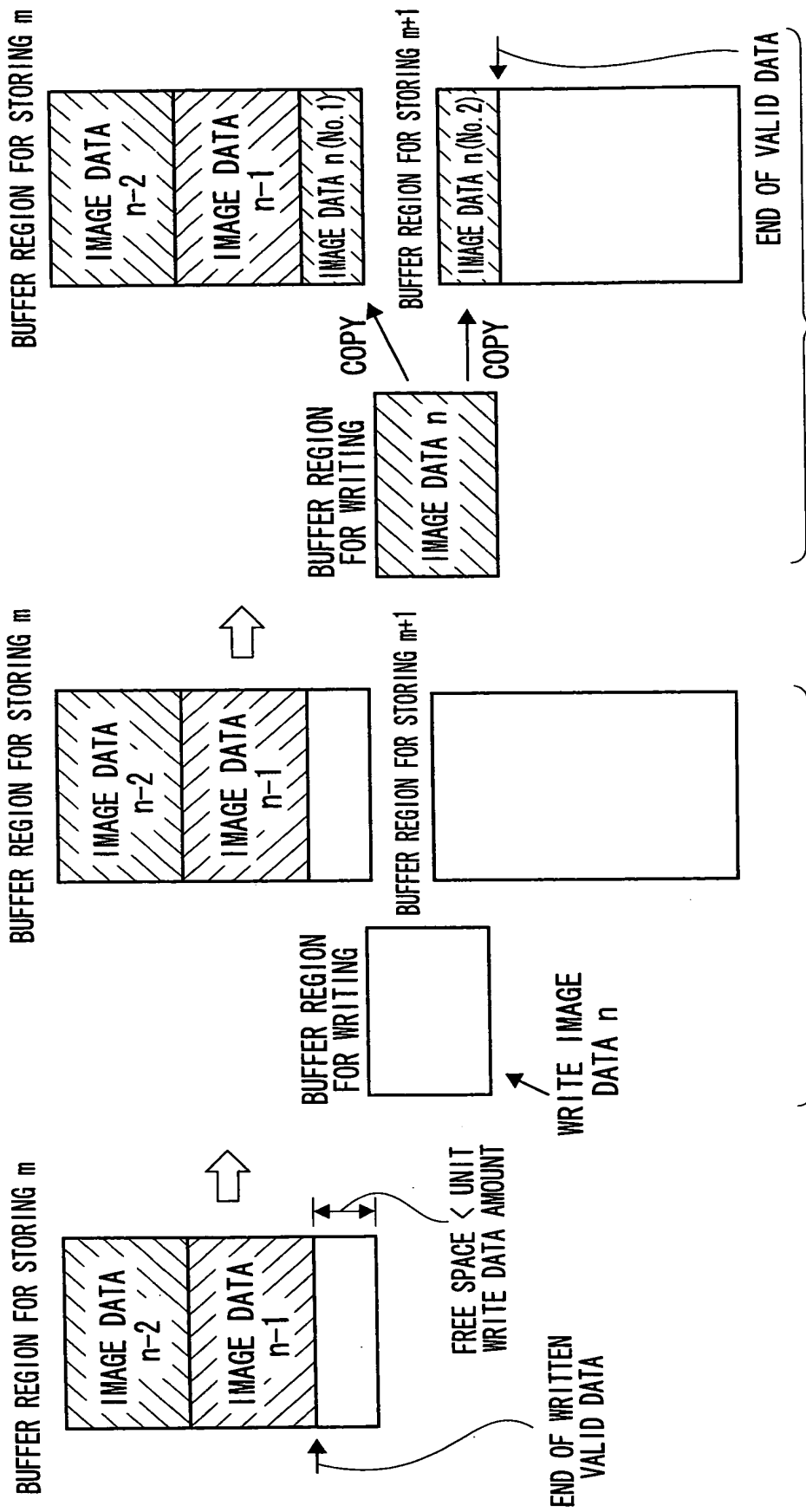
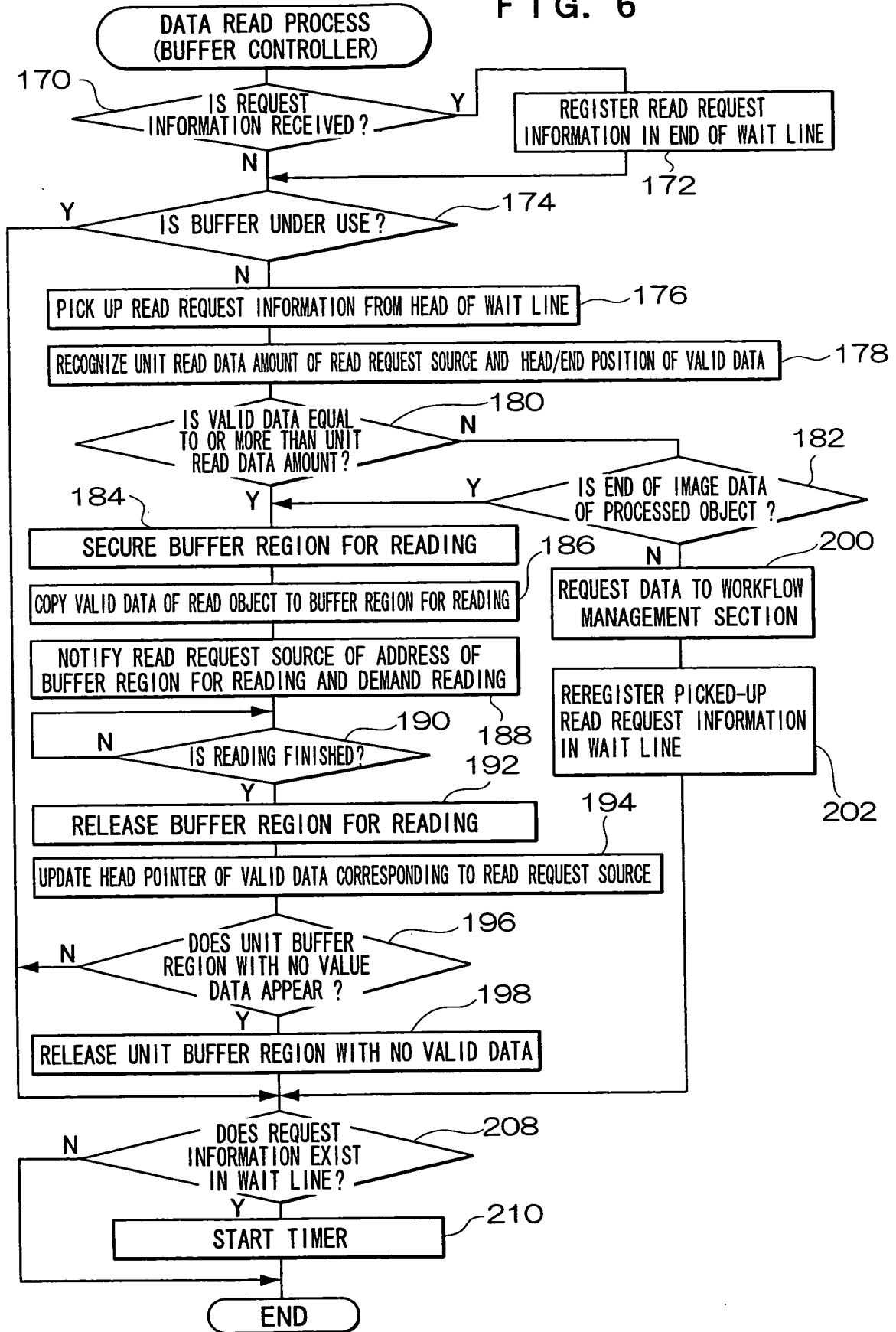


FIG. 5C

FIG. 5B

FIG. 5A

FIG. 6



PROCESS IN CASE THAT IMAGE DATA OF READ OBJECT STRIDES OVER PLURAL UNIT BUFFER REGIONS FOR STORING

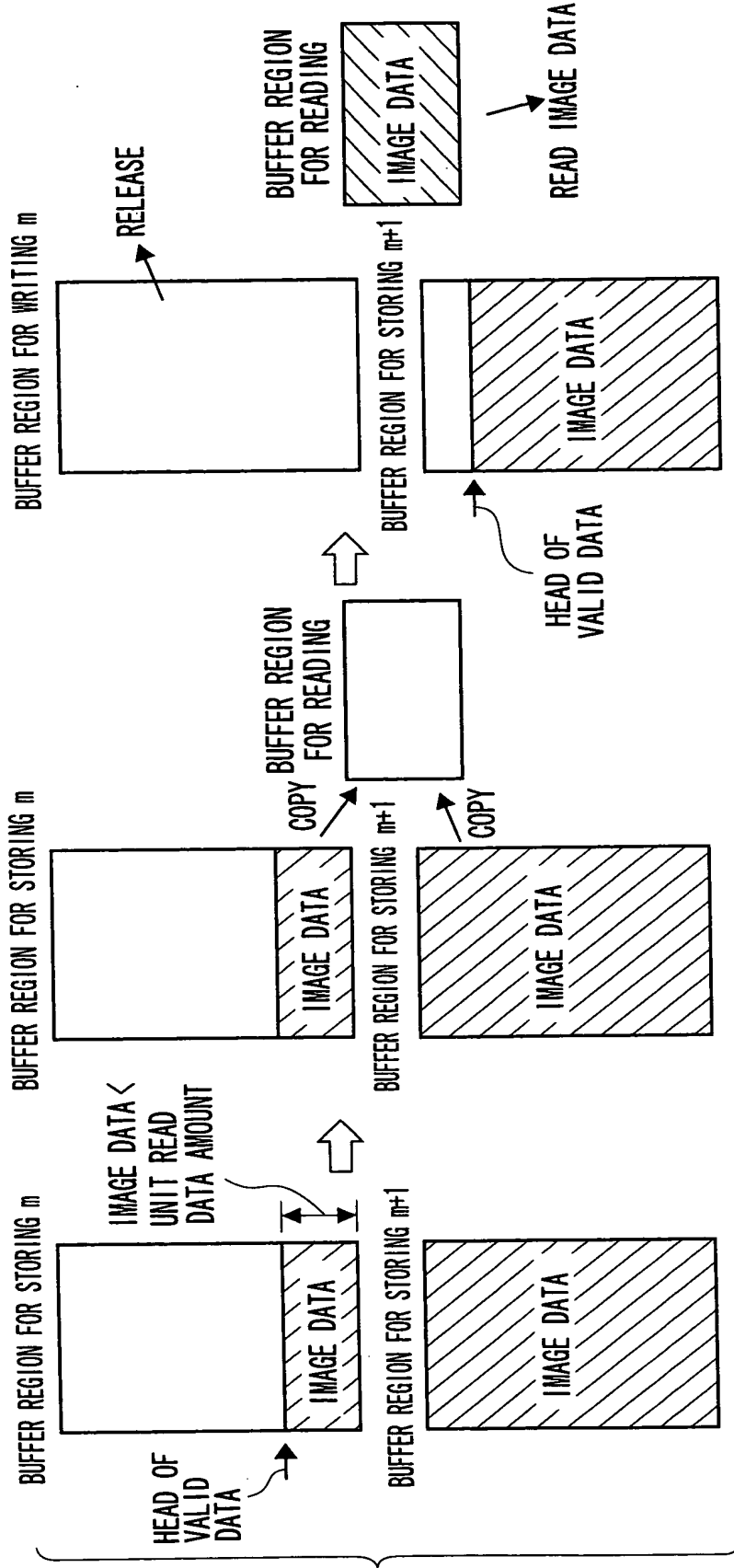


FIG. 7C

FIG. 7B

FIG. 7A

FIG. 8

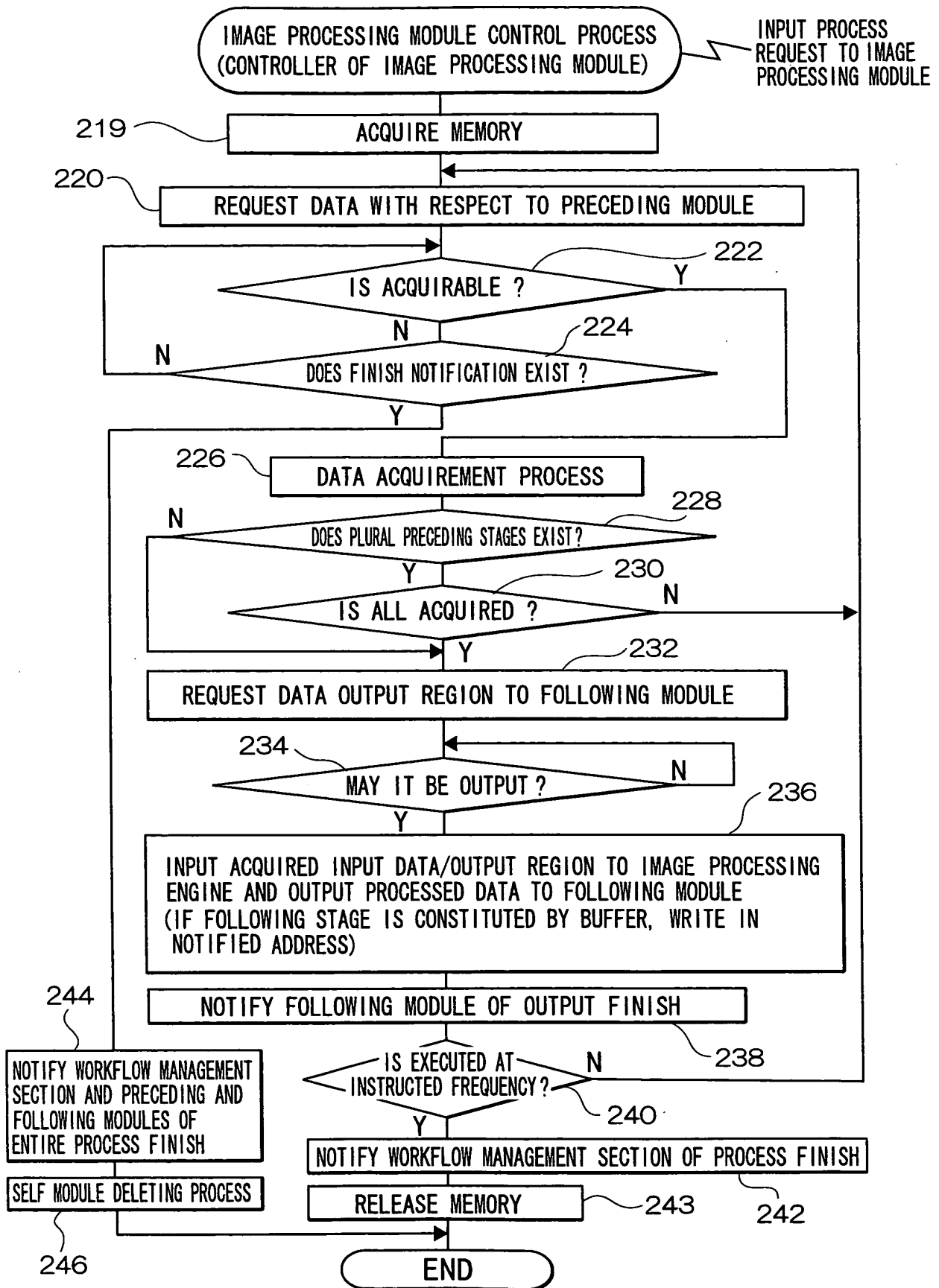


FIG. 9A

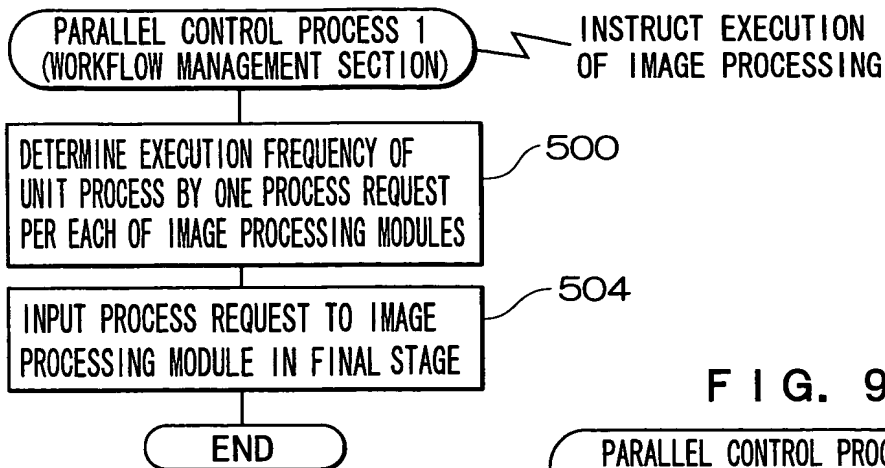


FIG. 9B

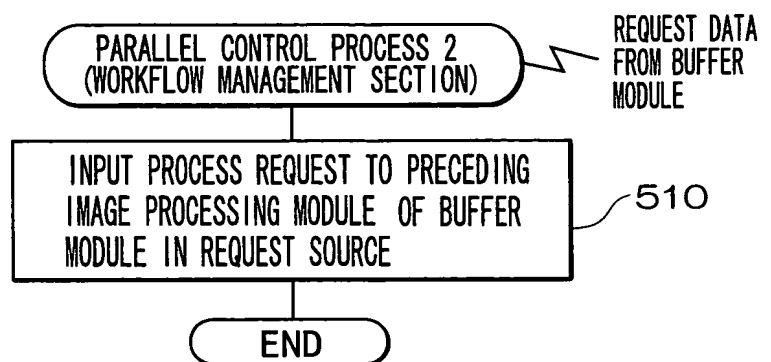


FIG. 9C

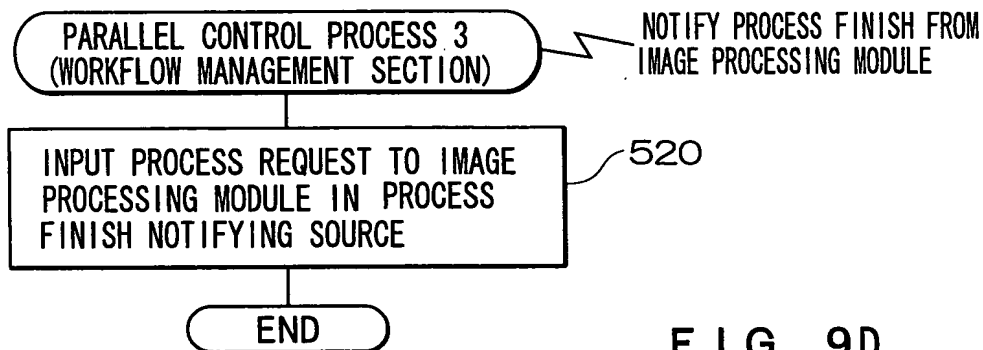


FIG. 9D

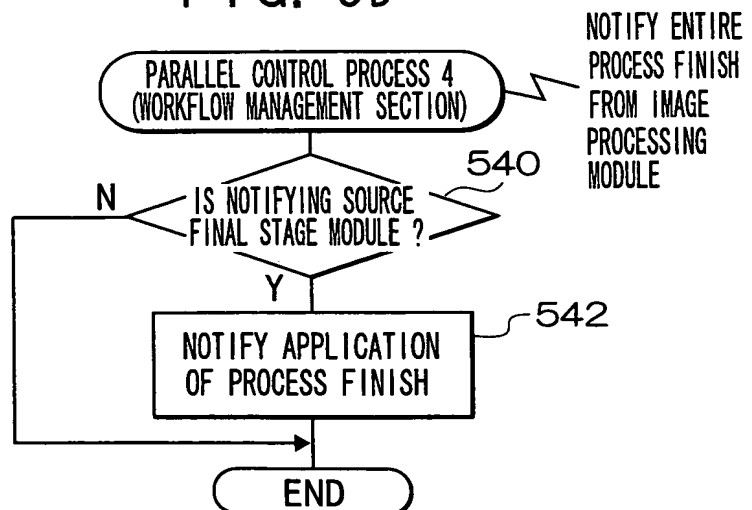


FIG. 10

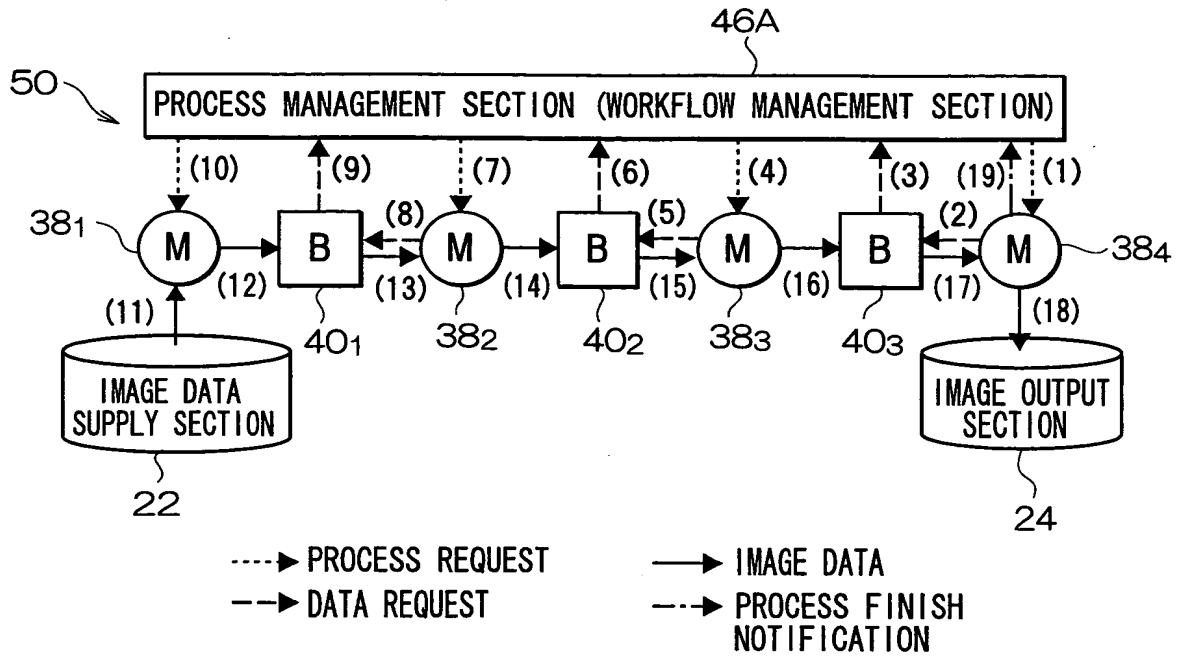


FIG. 11

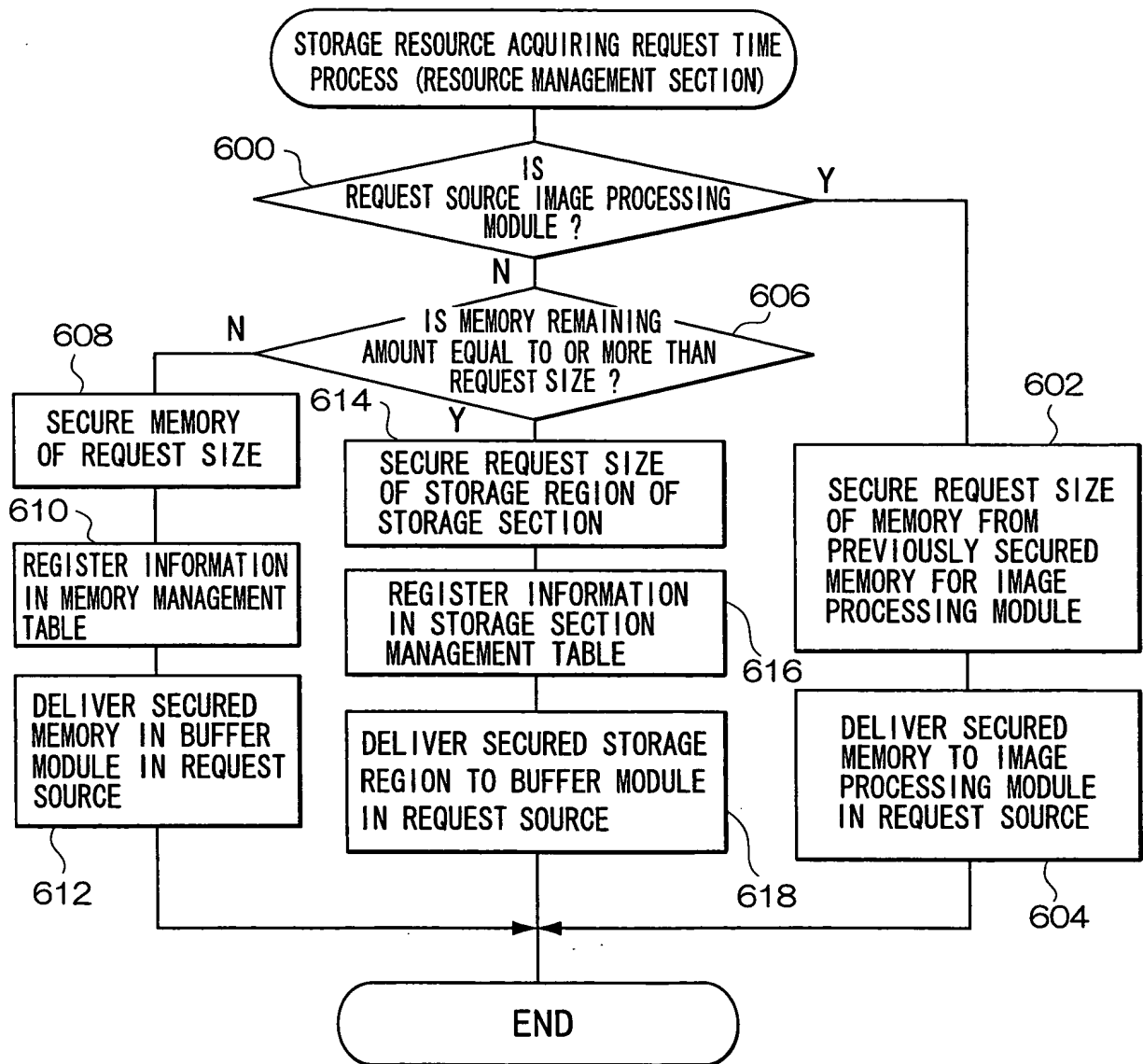


FIG. 12

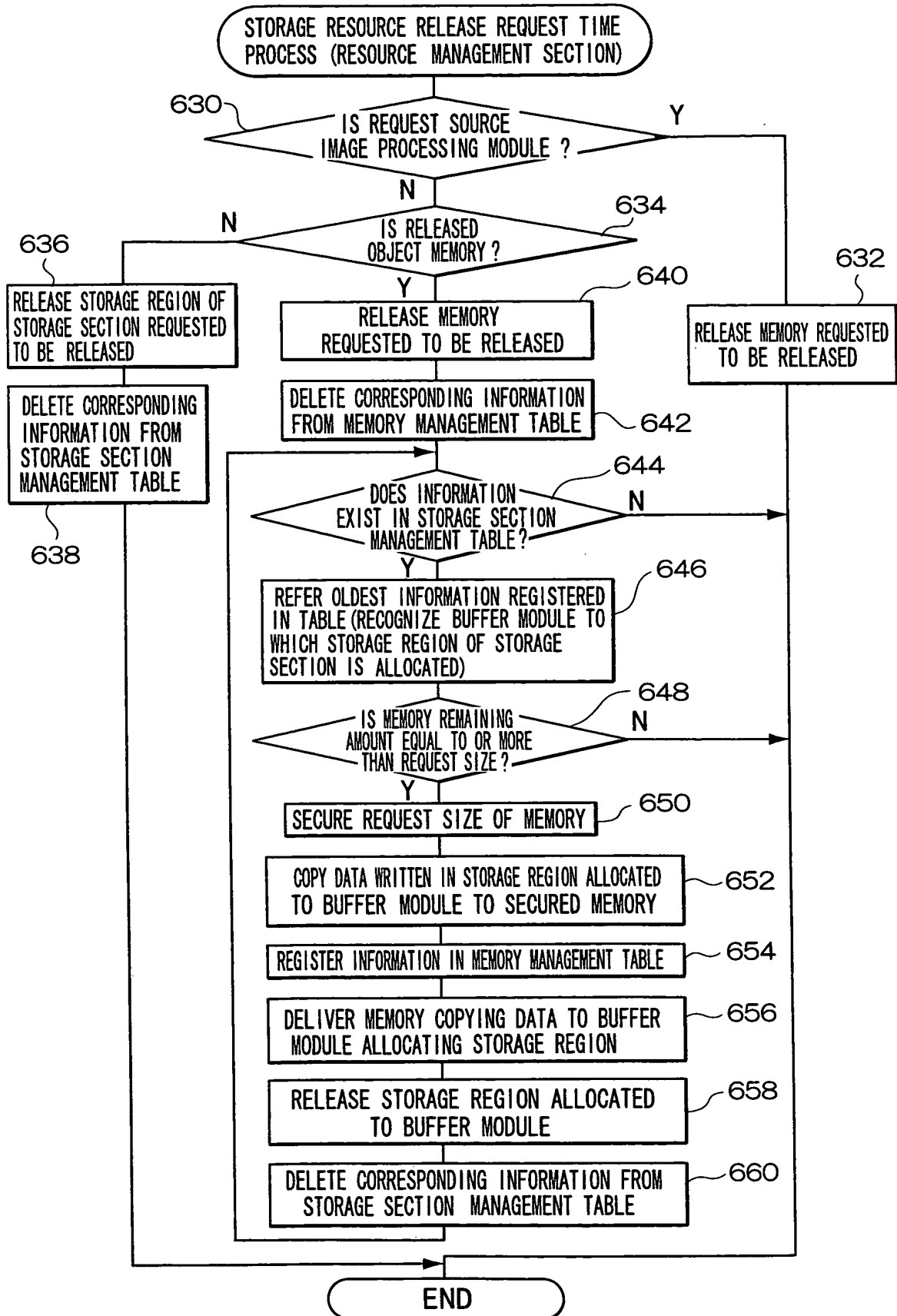


FIG. 13A

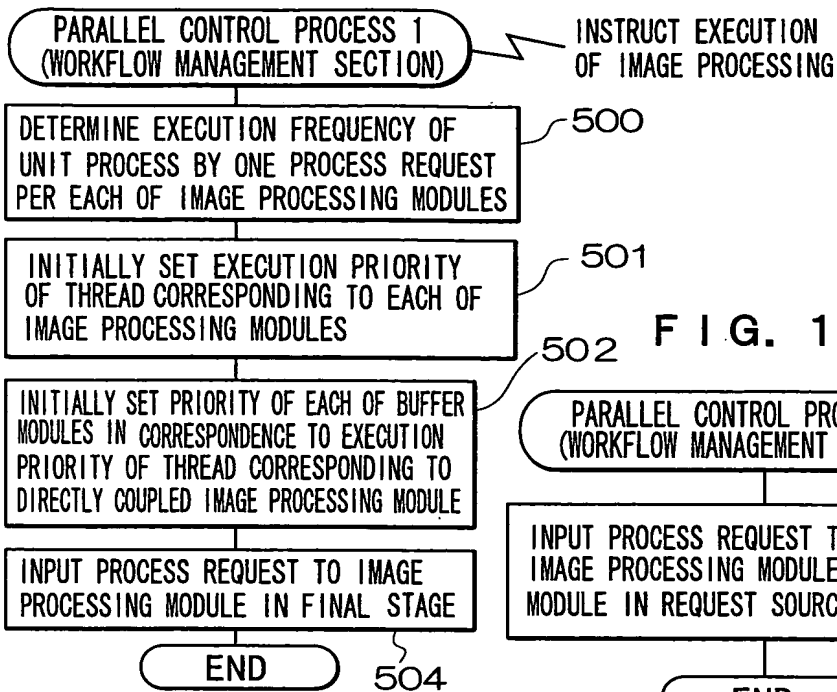


FIG. 13B

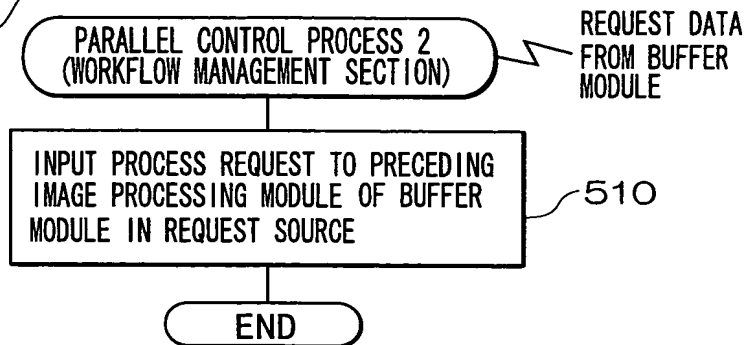


FIG. 13C

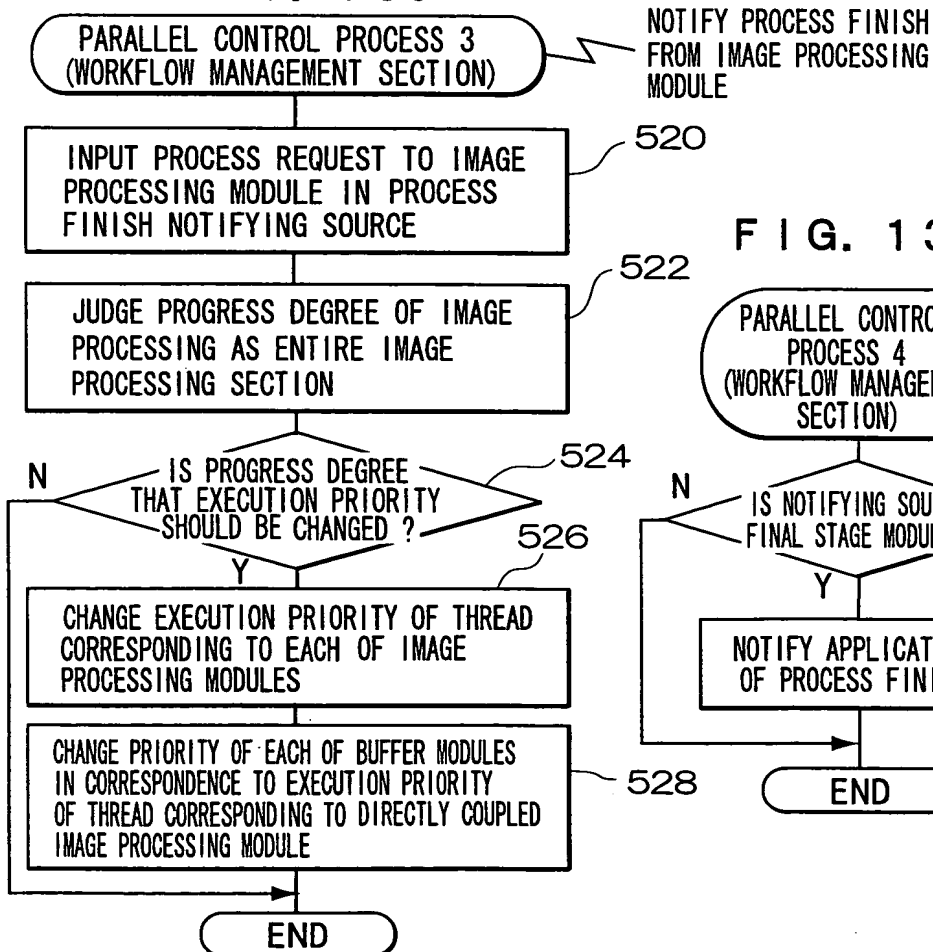


FIG. 13D

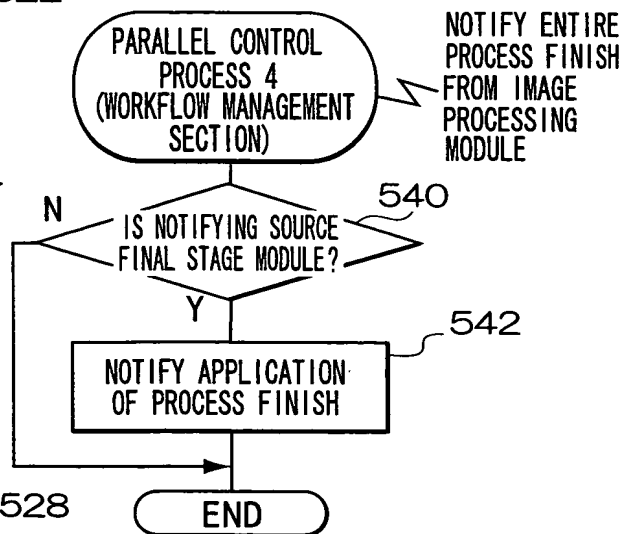


FIG. 14C

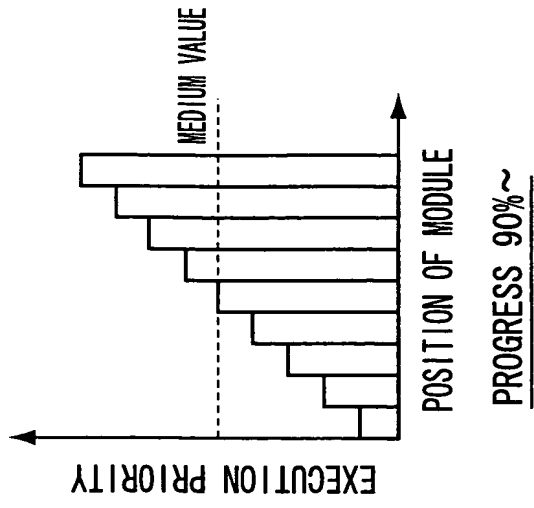


FIG. 14E

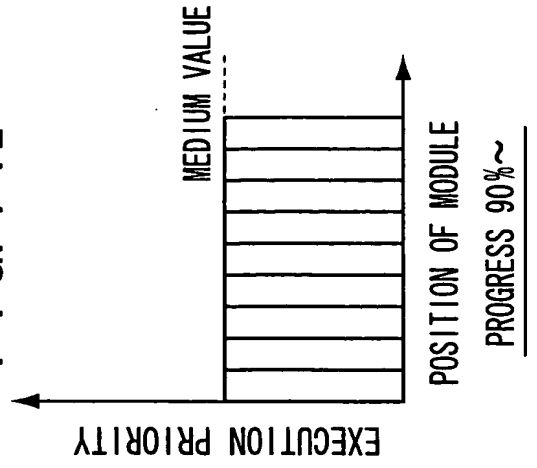


FIG. 14B

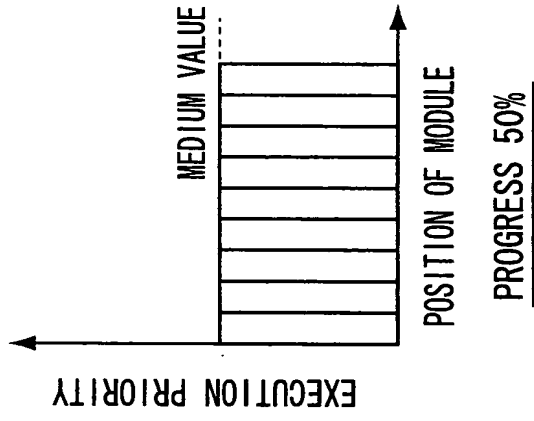


FIG. 14D

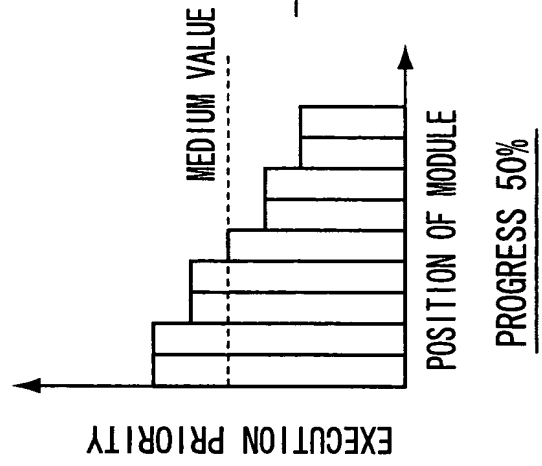


FIG. 14A

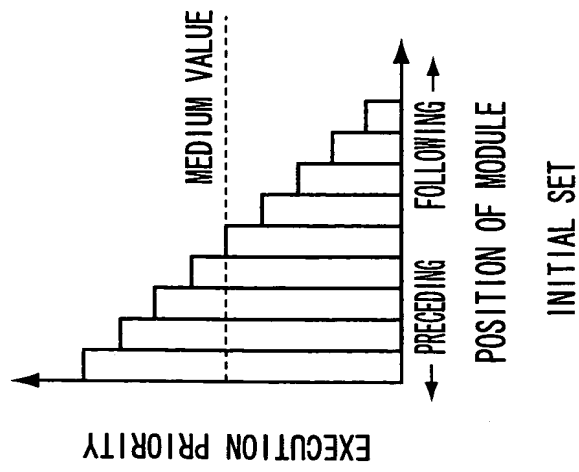


FIG. 15A

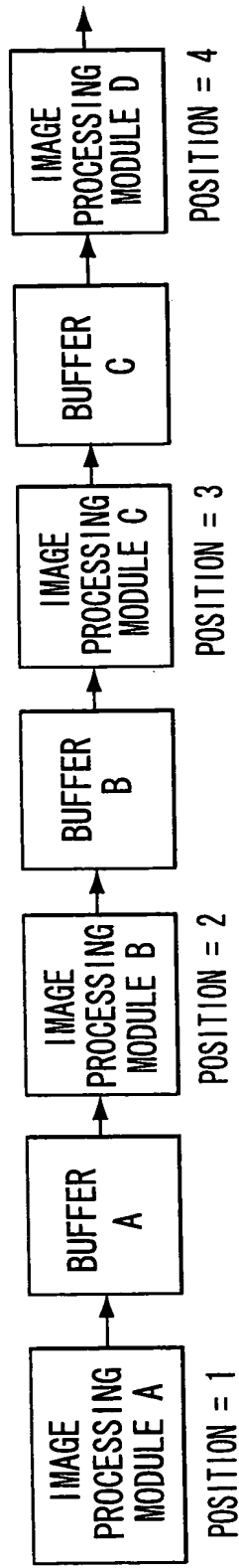


FIG. 15B

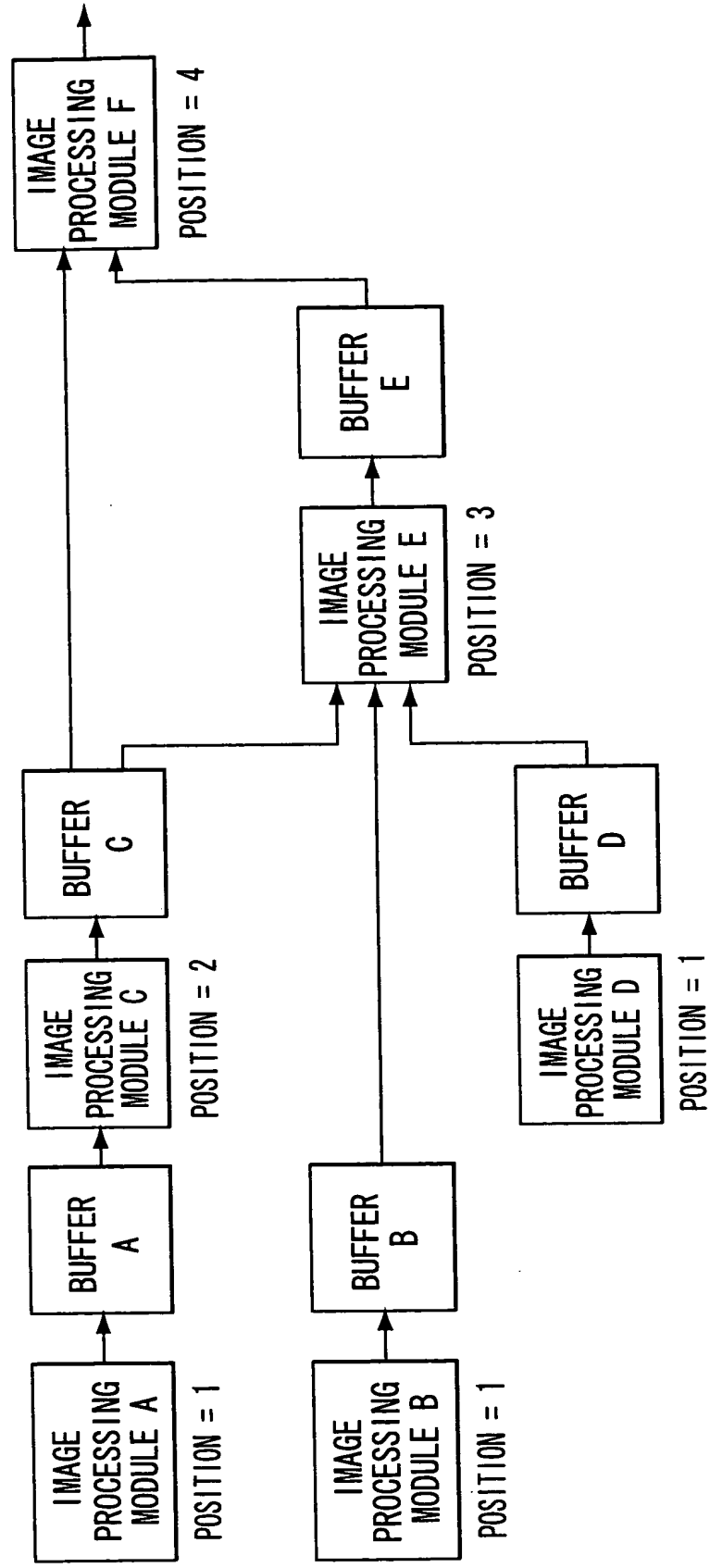


FIG. 16

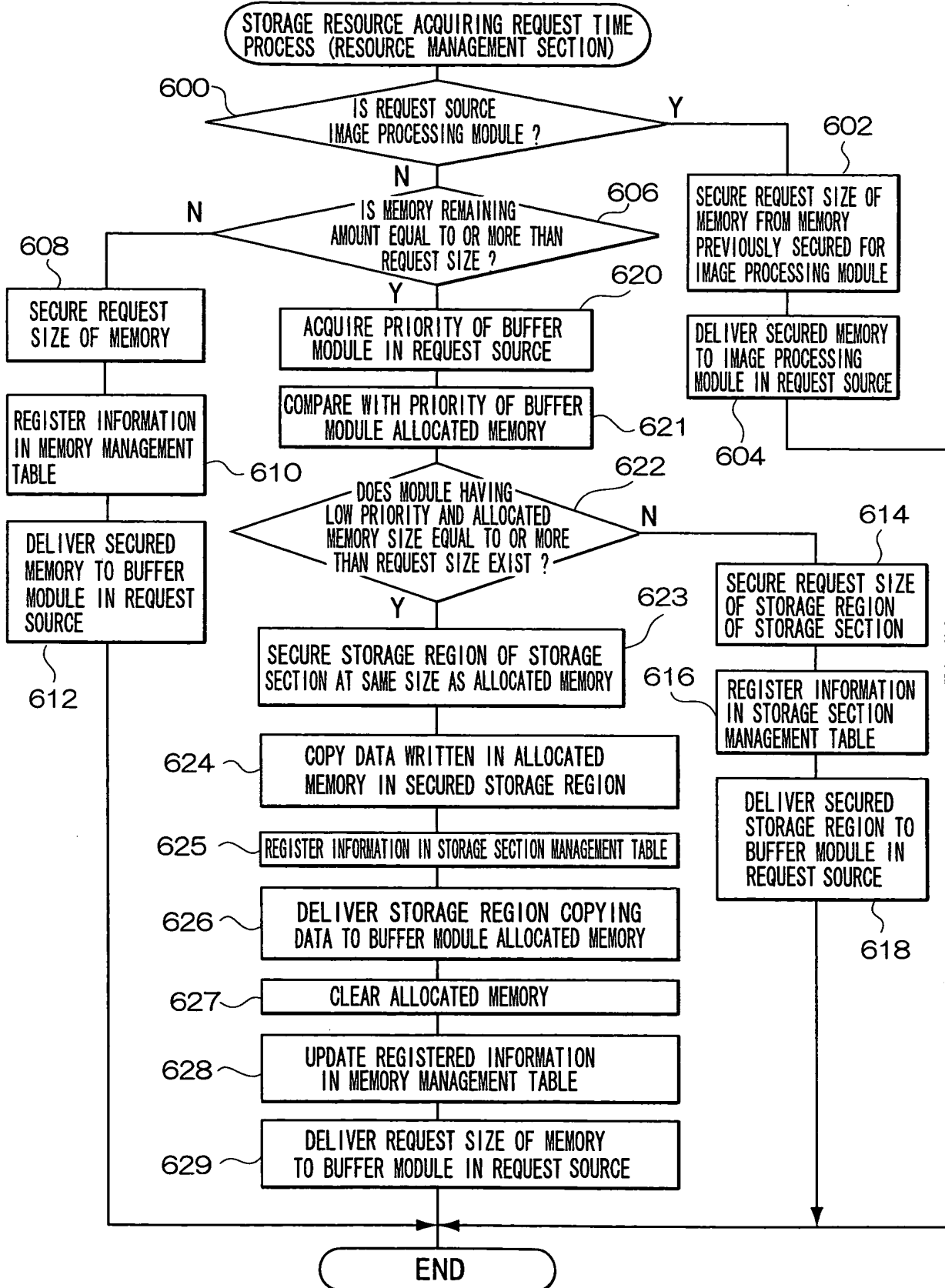


FIG. 17

