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Image processing apparatus, storage medium and data signal

Y Kumazawa, T Nagao, Y Isaka, T Igarashi, Y ... - US Patent App. 11/637,726, 2006 - Google Patents

... SECURED **MEMORY** FOR **IMAGE** PROCESSING MODULE DELIVER SECURED **MEMORY** IN **BUFFER** MODULE

IN REQUEST SOURCE DELIVER SECURED **MEMORY** TO **IMAGE** PROCESSING MODULE IN ...

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A survey of some theoretical aspects of multiprocessing

JL Baer - ACM Computing Surveys (CSUR), 1973 - portal.acm.org

... or more) programs resident simultaneously in primary **memory**. ... language particularly attractive for **pipe-line** machines. ... portion, add portion to **buffer**, V(number ...

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Reconfiguration system and method for high-speed mesh connected local area network

LB Lamport, TL Rodeheffer, KM Chandy - US Patent 5,138,615, 1992 - Google Patents

... 11,1992 sheet 7 of 19 5,138,615 120 HOST Q22 Bus 162 164 166 29116 DES CRC QBus 194 172 ' 192 190 Packet **Buffer** Big FIFO Rev L 186 Link Control Packet **Buffer** ...

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Performance metrics for embedded parallel pipelines- ► essex.ac.uk (PDF)

M Fleury, AC Downton, AF Clark - IEEE Transactions on Parallel and Distributed Systems, 2000 - ieeexplore.ieee.org

... a worker process to work from a **buffer** while a ... Across a series of consecutive **pipe-line** stages, traversal ... of the digitized postcode **image**, classification on a ...

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