#ADC128s022 BASE\_ADDRESS = '00000000'

# lvds\_tx\_9+ TO tp15 DOUT FROM J4 PIN 16

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_0\_interface\_MISO

set\_location\_assignment PIN\_U7 -to spi\_0\_interface\_MISO

# lvds\_tx\_9- TO tp15 DIN FROM J4 PIN 15

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_0\_interface\_MOSI

set\_location\_assignment PIN\_U6 -to spi\_0\_interface\_MOSI

# lvds\_tx\_8+ TO tp15 SCLK FROM J4 PIN 20

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_0\_interface\_SCLK

set\_location\_assignment PIN\_W8 -to spi\_0\_interface\_SCLK

# lvds\_tx\_8- TO tp15 CSB FROM J4 PIN 19

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_0\_interface\_SS\_n

set\_location\_assignment PIN\_W7 -to spi\_0\_interface\_SS\_n

#J4 PINS 1 AND 2 ARE 3.3 V SUPPLY TO THE TERMINAL BLOCK

#J4 PINS 7 AND 8 ARE THE GROUND TO THE TERMINAL BLOCK

#DAC128s085 BASE\_ADDRESS = '00000020'

# LVDS\_TX\_3+ TO ROW C DOUT FROM J4 PIN 32

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_1\_interface\_MISO

set\_location\_assignment PIN\_W14 -to spi\_1\_interface\_MISO

# LVDS\_TX\_3- TO ROW A DIN FROM J4 PIN 31

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_1\_interface\_MOSI

set\_location\_assignment PIN\_V13 -to spi\_1\_interface\_MOSI

# LVDS\_TX\_2+ TO ROW A SCLK FROM J4 PIN 36

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_1\_interface\_SCLK

set\_location\_assignment PIN\_W15 -to spi\_1\_interface\_SCLK

# LVDS\_TX\_2- TO ROW C SYNC FROM J4 PIN 35

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_1\_interface\_SS\_n

set\_location\_assignment PIN\_V14 -to spi\_1\_interface\_SS\_n

#J5 PIN 29 IS 3.3 VOLT SUPPLY TO ROW C PIN 1

#J5 PIN 30 GROUND TO ROW C PIN 2

#LTC2656BCUFD-X16 BASE\_ADDRESS = '00000040'

# DIFF\_RX\_6+ TO SDO FROM J4 PIN 24

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_2\_interface\_MISO

set\_location\_assignment PIN\_V10 -to spi\_2\_interface\_MISO

# DIFF\_RX\_6- TO SDI FROM J4 PIN 23

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_2\_interface\_MOSI

set\_location\_assignment PIN\_V9 -to spi\_2\_interface\_MOSI

# DIFF\_RX\_5+ TO SCK FROM J4 PIN 28

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_2\_interface\_SCLK

set\_location\_assignment PIN\_AA10 -to spi\_2\_interface\_SCLK

# DIFF\_RX\_5- TO CS FROM J4 PIN 27

set\_instance\_assignment -name IO\_STANDARD "3.3-V LVTTL" -to spi\_2\_interface\_SS\_n

set\_location\_assignment PIN\_Y10 -to spi\_2\_interface\_SS\_n

#J5 PIN 11 IS 5 VOLT SUPPLY TO VCC PIN

#J5 PIN 12 GROUND TO J1 LEFT ROW MIDDLE PIN (PIN 8 WHERE PIN 1 IS THE BOTTOM RIGHT PIN 2 IS BOTTOM LEFT ECT)

#NOTE LDAC SHOULD BE JUMPERED HIGH TO VCC (LEFT ROW BOTTOM LEFT PIN)