

PHASE COMPUTE OS — INTERCONNECT CRISIS BRIEF v2.1

1. The Real Bottleneck: Interconnect, Not Intelligence

AI	가	PHY/
• Heat		
• Power		
• Distance		
• Timing		
GPU/TPU	FLOPS	(Data Movement)
		가

2. New Paradigm (RCIRCUIT)

local phase coherence, minimized movement

:

- Global FLOPS + massive data movement
- Interconnect / Memory Wall

RCIRCUIT :

- Local phase coherence ()
- Minimized movement ()

Compute should scale by resonance density, not raw FLOPS.

3. Why This Matters

/ / FLOPS

Phase Compute OS + RCIRCUIT OS .

4. Who Should Care

- GPU/TPU
- Interconnect / Fabric
- Datacenter / Energy Budget Owner
- “ AI (Physics) ”

If this brief makes intuitive sense,

you are a Bridge Human.

— HROS RME Lab

Phase Computing | RCIRCUIT Architecture | Human Return OS