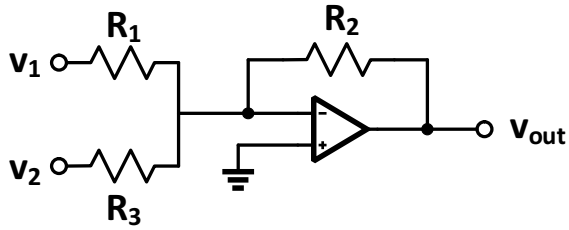


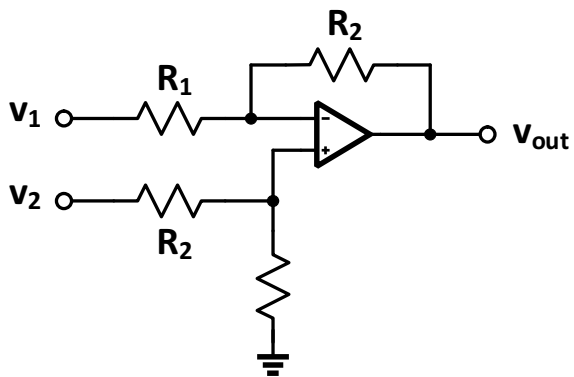
Name:

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1. Compute  $v_{\text{out}}$  in the circuit shown below.



2. Compute  $v_{\text{out}}$  in the circuit shown below.



3. Sketch a op-amp circuit that gives output  $\mathbf{v_{out}} = \mathbf{v_{in1}} + \mathbf{v_{in2}}$ , where  $\mathbf{v_{out}}$  denotes the output voltage, and  $\mathbf{v_{in1}}$  and  $\mathbf{v_{in2}}$  denote the two input voltages.