Name: UIN:

1. Simplify the following Boolean expressions

(a)
$$\mathbf{f} = (\overline{\mathbf{A}} + \mathbf{B})$$
 . $(\overline{\mathbf{A}}$. $(\mathbf{B} + \mathbf{A}))$

(b)
$${\bf f}=\overline{\bf A}$$
 , $\overline{\bf B}$, $\overline{\bf C}+\overline{\bf A}$, ${\bf B}$, $\overline{\bf C}+{\bf A}$, ${\bf B}$, $\overline{\bf C}$

(c)
$$\mathbf{f} = \mathbf{A} \cdot \mathbf{B} + \overline{\mathbf{A}} \cdot \mathbf{C} + \mathbf{B} \cdot \mathbf{C}$$

(d) Sketch the RTL based circuit that implements the following logic functions:

i.
$$\mathbf{f} = \mathbf{A}$$
 . \mathbf{B} . \mathbf{C}

ii.
$$\mathbf{f} = \overline{\mathbf{A} \cdot \mathbf{B} + \mathbf{C}}$$