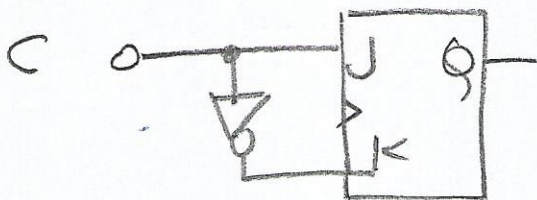


IF they are ON ( $b=1$ ) then the output of first AND gate is OFF whatever a is.



then the simplified circuit is

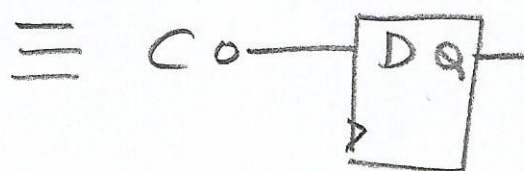


$$Q^+ = JQ' + K'Q$$

here  $K = J'$  then

$$\begin{aligned} Q^+ &= JQ' + JQ \\ &= J(Q' + Q) \\ &= J \end{aligned}$$

This circuit behaves like a D-FF



$b=1$  LOADS  
C and d into  
J-K FF's !