Home work - 1 Yusuf Tekin 0402000 43 Part -1 1. Convert the hexadecimal number 64CD<sub>16</sub> to binary, to octal and decimal.  $64CD_{16} = (0110)(0100)(1100)(1101) = 0110010011001101_{2} binary$  6 4 (= 12 D = 1301100100110011012 = (110) (010) (011) (001) (101) = 62315 g Octu 640D1 = 6 x 163 + 4x 162 + 12x161 + 13x160 = 2580510 decimal 2. Convert the decimal number 431 to binary, hexadecimal and octal. 3. Express the following numbers in decimal: a.  $(10110.0101)_2$ b.  $(16.5)_{16}$ c.  $(26.24)_8$ d. (DADA.B)<sub>16</sub> e.  $(1010.1101)_2$ 

a) 
$$1 \times 2^{\frac{1}{4}} + O_{\times} 2^{\frac{3}{4}} + 1_{\times} 2^{\frac{3}{4}} + 1_{\times} 2^{\frac{3}{4}} + O_{\times} 2^{\frac{3}{4}} + 1_{\times} 2^{\frac{3}{4}} + 0_{\times} 2^{\frac{3}{4}} + 1_{\times} 2^{\frac{3}{4}} + 0_{\times} 2^{\frac{3}{4}} + 1_{\times} 2^{\frac{3}{4}} = 22.3125_{10}$$
b)  $(16.5)_{16} = (1 \times 16^{\frac{1}{4}} + 6 \times 16^{\frac{3}{4}}). (5 \times 16^{\frac{3}{4}}) = 22 + \frac{5}{16} = 22.3125_{10}$ 
c)  $(26.24)_{8} = (2 \times 8^{\frac{3}{4}} + 6 \times 8^{\frac{3}{4}}). (2 \times 8^{\frac{3}{4}} + 4 \times 8^{\frac{3}{4}}) = 22 + \frac{2}{8} + \frac{4}{64} = 22.3125_{10}$ 

d) 
$$(DADA.8)_{16} = (13 \times 16^{3} + 10 \times 16^{2} + 13 \times 16^{1} + 10 \times 16^{0})_{10} = 56026.6875_{10}$$
  
e)  $(1010.1101)_{2} = 10 + \frac{1}{2} + \frac{1}{2} + \frac{1}{16} = 10.8125_{10}$ 

- 4. Convert the following binary numbers to hexadecimal and to decimal:
  - a. 1.10010
  - b. 110.010. Explain why the decimal answer in (b) is 4 times that in (a).

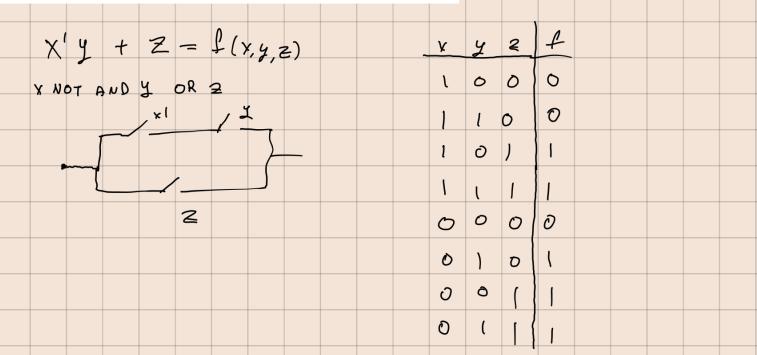
a) 
$$(0001.1001)_{1} = (1.5625)_{10}$$
 $(1+\frac{1}{2}+\frac{1}{16})_{16} = (1.5625)_{10}$ 

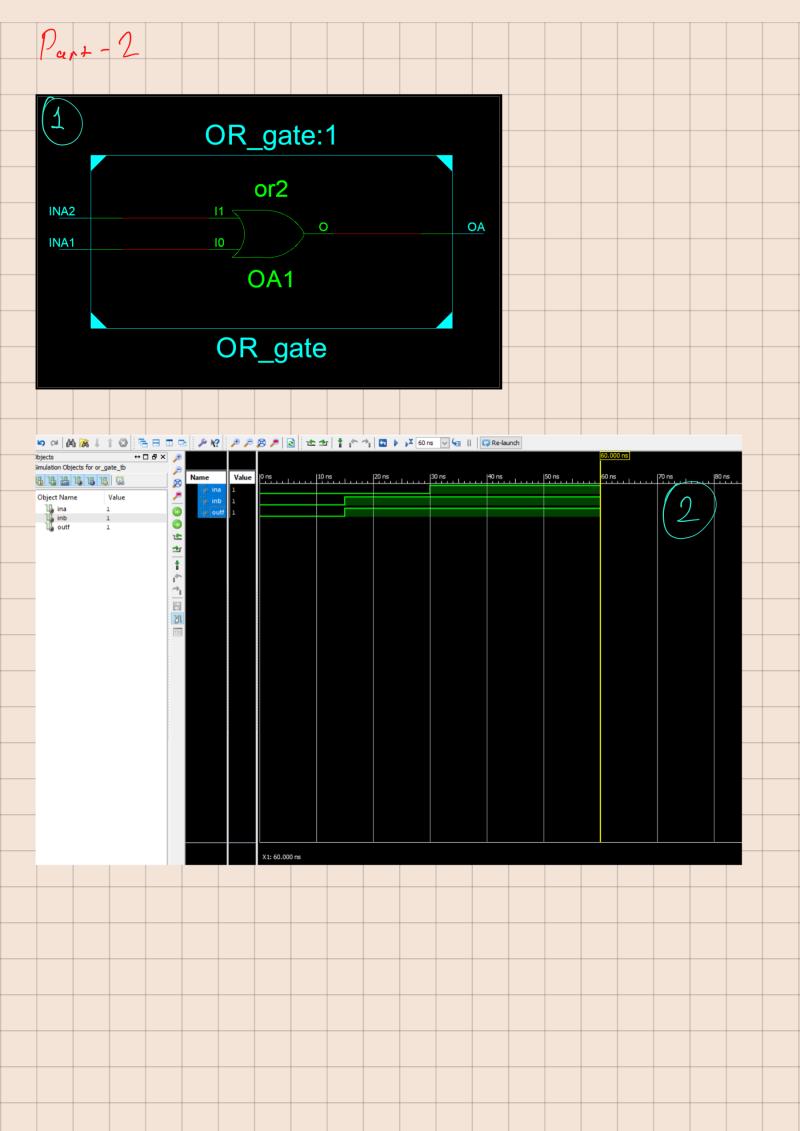
$$\frac{6}{6} (0110.0100)_{2} = (6.4)_{16} = (6.25)_{10}$$

if we mutiply 1.1001 by 
$$h=2^2$$
 moves the numbers to the left by two which is 110.01 (b). (The dot is not

moving)

5. Draw truth table of the Boolean function 
$$f(x,y,z)=x'y+z$$
.





## Report:

For the first part, we simply define inputs and outputs in the syntax. For this homework the source has already been given by lecturer, so all we need is to add it into our project. After that to view schematic we run "View RTL Schematic" under synthesize tab at the "Implementation" view option.

For the second part, we need to switch the view option to "Simulation" and run "Simulate Behavioral Model" function on process window. After a couple of zooming adjustments we can see the graph in the 2nd image. To understand the OR function I double clicked on one of the objectives. As it can be seen in the syntax we are basically define the inputs as zeros at the first 15 ns. There for the output of the OR gate is also zero. For the second test we define input B "1" and input A "0" which makes output 1 between 15 - 30 ns. For the last part we define all the two inputs "1" which gives "1" for OR function again. So the graph of output does not change.