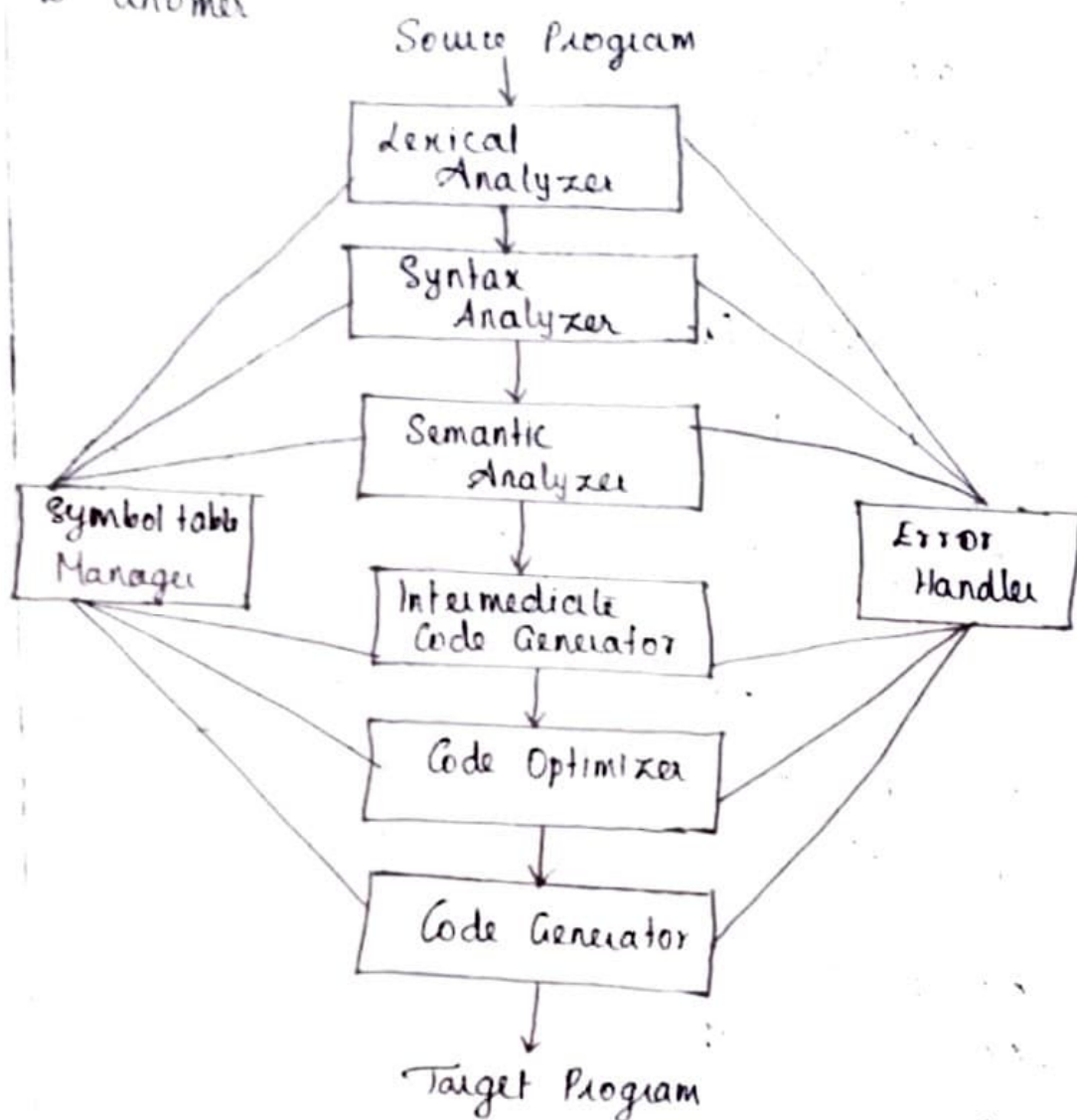


## IV THE PHASES OF COMPILER

- A compiler operates in phases, each of which transforms the source program from one representation to another



- Symbol table Management and error handling interact with the six phases of lexical analysis, syntax Analysis, Semantic Analysis, Intermediate Code Generation, Code optimization and Code Generation

## Analysis of the Source Program.

\* In compiling analysis consists of three phases.

1. Linear Analysis
2. Hierarchical Analysis
3. Semantic Analysis

### 1. Linear Analysis.

\* Linear analysis is called as lexical analysis or scanning.

\* Linear analysis in which the stream of characters making up the source program is read from left-to-right and grouped into tokens that are sequences of characters having a collective meaning.

\* For eg)

In the assignment statement

$pos = initial + rate * 60$

would be grouped into the following tokens.

1. The identifier pos
2. The assignment symbol =
3. The identifier initial
4. The + sign
5. The identifier rate
6. The \* sign
7. The number 60

\* The blanks separating the characters of these tokens would be eliminated during lexical analysis.

\* The hierarchical structure of a program is expressed by recursive rules.

\* The rules of the definitions of expressions

1 Any identifier is an expression

2 Any number is an expression

3 If expression<sub>1</sub> and expression<sub>2</sub> are expressions then

expression<sub>1</sub> + expression<sub>2</sub>

expression<sub>1</sub> \* expression<sub>2</sub>

(expression<sub>1</sub> , )

are expressions.

\* Rules (1) and (2) are basic rules, Rule (3) defines expressions in terms of operators applied to other expressions.

\* The rules of the definition of statement

1 If identifier<sub>1</sub> is an identifier and expression<sub>2</sub> is an expression then

identifier<sub>1</sub> = expression<sub>2</sub>

is a statement

2 If expression<sub>1</sub> is an expression and statement<sub>2</sub> is a statement then

while (expression<sub>1</sub>) do statement<sub>2</sub>

if (expression<sub>1</sub>) then statement<sub>2</sub>

are statements



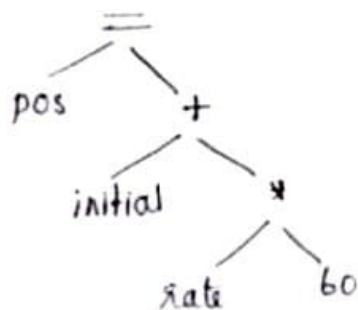
### Error Detection and Reporting:

- \* Each phase can encounter errors. However, after detecting an error, a phase must deal with the error so that compilation can proceed, allowing further errors in the source program to be detected.

### Intermediate Code Generation:

- \* After syntax and semantic analysis phase, some compilers generate an explicit intermediate representation of the source program.
- \* The intermediate representation should have two properties:
  1. It should be easy to produce.
  2. It should be easy to translate into target program.
- \* The intermediate representation can have variety of forms such as, syntax tree, postfix notation, and three address code.
- \* Three address code which is like assembly language for a machine in which every location can act like a register.
- \* The source program might appear in three address code as

- \* The division of lexical and syntax analysis simplifies the overall task of the analysis
- \* The parse tree is a syntactic structure of the input
- \* A more common internal representation of this syntactic structure is given by the syntax tree
- \* A syntax tree is a compressed representation of a parse tree in which the operators appear as the interior nodes, and the leaves are operands of an operator



### Semantic Analysis

- \* This phase checks the source program for semantic errors and gathers type information for the code-generation phase
- \* It uses the hierarchical structure determined by the syntax analysis phase to identify the operators and operands of expressions and statements
- \* An important component of semantic analysis is type-checking.

- \* Here the operands are identical
- \* For eg) require a real
- \* For eg) Typ a real approach
- \* This for integ

### Symbol

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## Code Generation

- \* The final phase of the compiler is the generation of target code, consisting of relocatable machine code or assembly code.
- \* The intermediate instructions are translated into a sequence of machine instructions that perform the same task.
- \* An aspect is the assignment of variables to registers.

MOV F id<sub>3</sub>, R<sub>2</sub>

MUL F \*60.0 R<sub>2</sub>

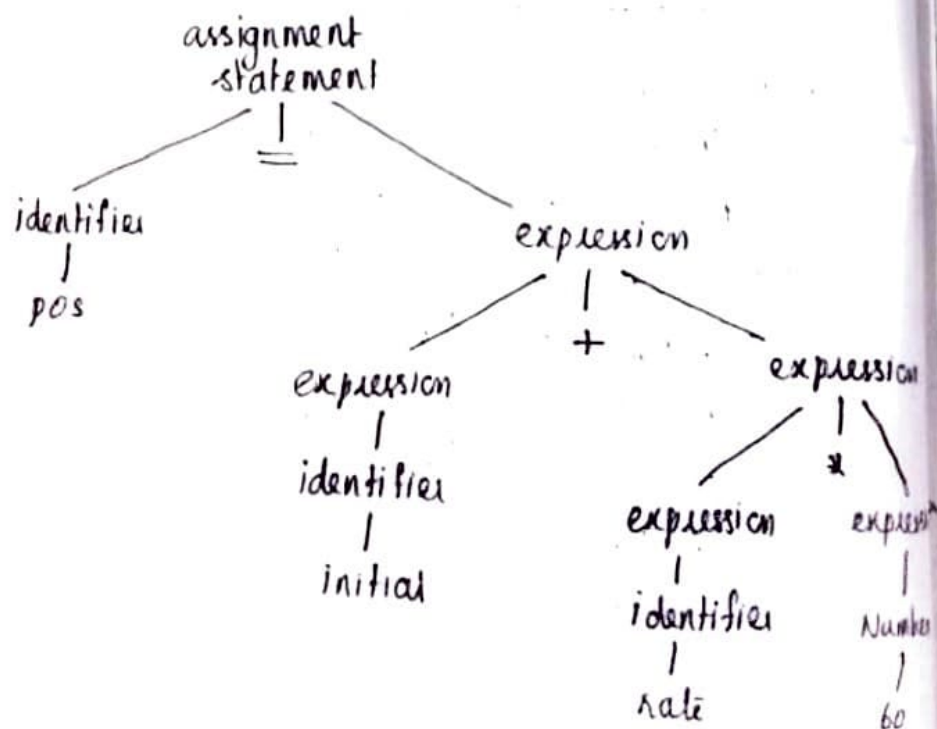
MOV F id<sub>2</sub>, R<sub>1</sub>

ADD F R<sub>2</sub>, R<sub>1</sub>

MOV F R<sub>1</sub>, id

## 2 Hierarchical Analysis:

- \* Hierarchical analysis is called as parsing or syntax analysis
- \* Hierarchical analysis in which characters or tokens are grouped hierarchically into nested collections with collective meaning.
- \* It groups the tokens of the source program into grammatical phrases that are used by the compiler to synthesize output
- \* The grammatical phrases of the source program are represented by a parse tree.



\* These attributes may provide information about the storage allocated for an identifier; its type, its scope in the case of procedure names, things such as the number and type of its arguments, the method of passing each argument and the type returned.

\* A symbol table is a data structure containing a record for each identifier with fields for the attributes of the identifier.

\* The data structure allows us to find the record for each identifier quickly and to store or retrieve data from that record quickly.

\* When an identifier in the source program is detected by the lexical analyzer, the identifier is entered into the symbol table.

\* However, the attributes of an identifier cannot normally be determined during lexical analysis.

For eg)

```
var pos, init; rate : real
```

the type real is not known when pos, init, rate are seen by the lexical analyzer.

\* The remaining phases enter information about identifiers into the symbol table and use this information in various ways.

## Error Detection

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- Here the operands of the specification
- For eg) require a real
- For eg) Type a real approach
- This is for the integer

### Symbol Table

- An important part of the compiler is the collection of each

$temp_1 = \text{intto real}(60)$

$temp_2 = id_3 * temp_1$

$temp_3 = id_2 + temp_2$

$id_1 = temp_3$

\* This intermediate form has several properties

1. Each three address instruction has at most one operator in addition to the assignment operator.

2. The compiler must generate a temporary names to hold the value computed by each instruction

3. Some 'three address' instruction have fewer than three operands eg

first and last instructions

### Code Optimization:

\* The code optimization phase attempts to improve the intermediate code so that faster running machine code will result.

$temp_1 = id_3 * 60.0$

$id_1 = id_2 + temp_1$

\* However, there are simple optimizations that significantly improve the running time of the target program without slowing compilation time too much.

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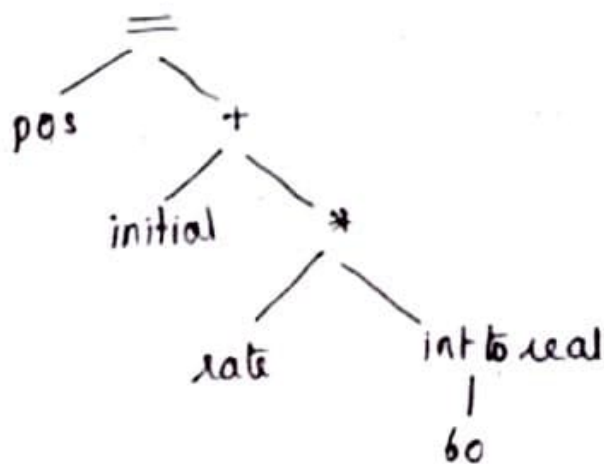
\* Here the compiler checks that each operator has operands that are permitted by the source language specification

\* For eg) many programming language definitions require a compiler to report an error every time a real number is used to index an array.

\* For eg)

Type checking reveals that \* is applied to a real value and integer 60. The general approach is to convert the integer into a real.

\* This is achieved by creating an extra node for the operator `int to real` that converts the integer into a real.



### Symbol Table Management

\* An important function of a compiler is to record the identifiers used in the source program and collect information about the various attributes of each identifier.