

why bother

use to improve efficiency → How?

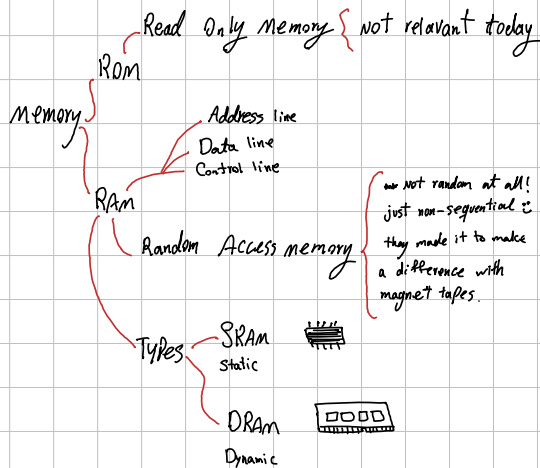
frequent data

Store at higher level

ask for them ahead of time

Cost

Register	0	cycle
Cache	1-10	cycle
RAM	50-100	cycle
Disk	20,000,000	cycle



Serial Access memory

Shift Register

Serial In Parallel Out (SIPO)

Parallel In Serial Out (PISO)

Queues

First In First Out

Last In First Out

Content Address memory