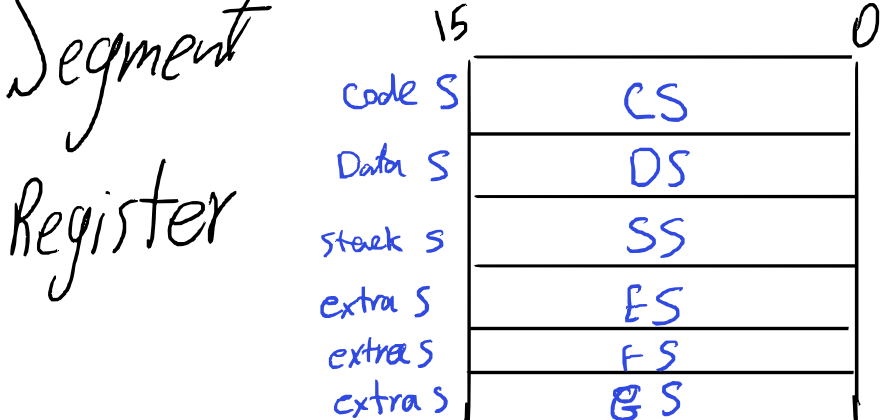
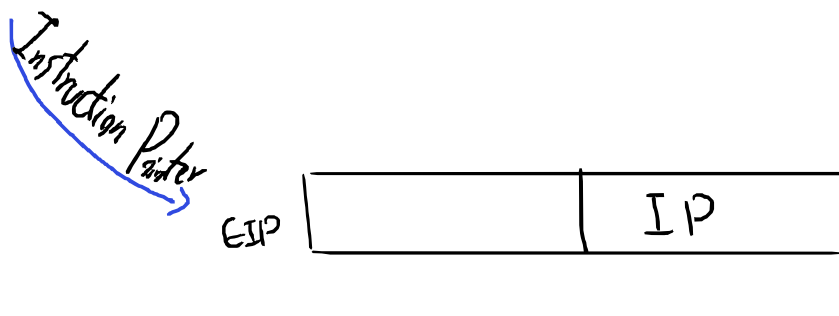
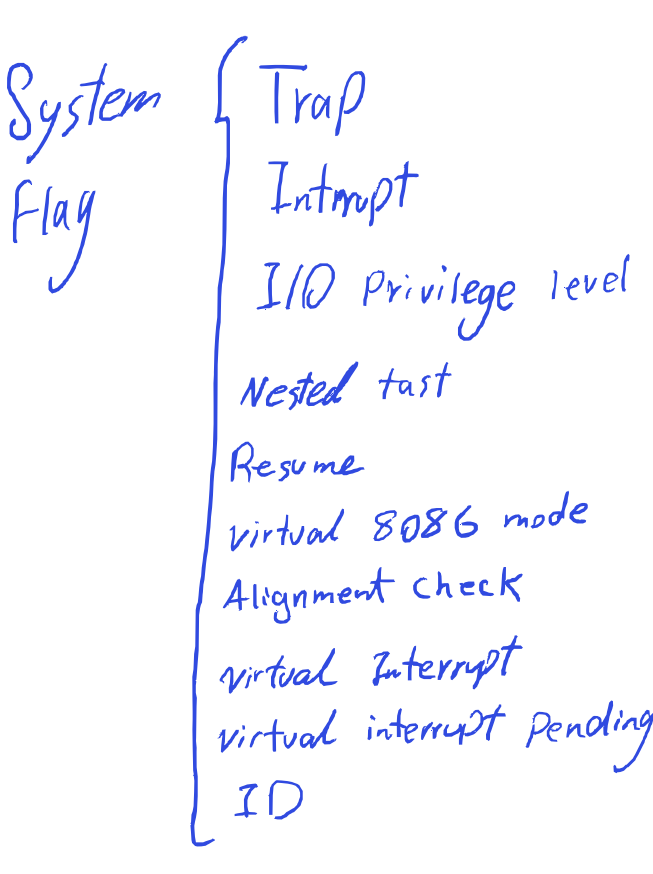
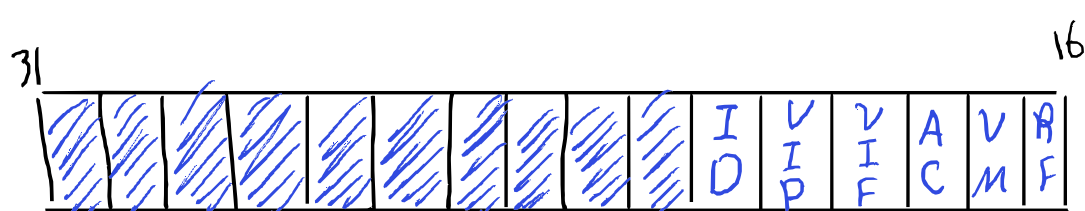
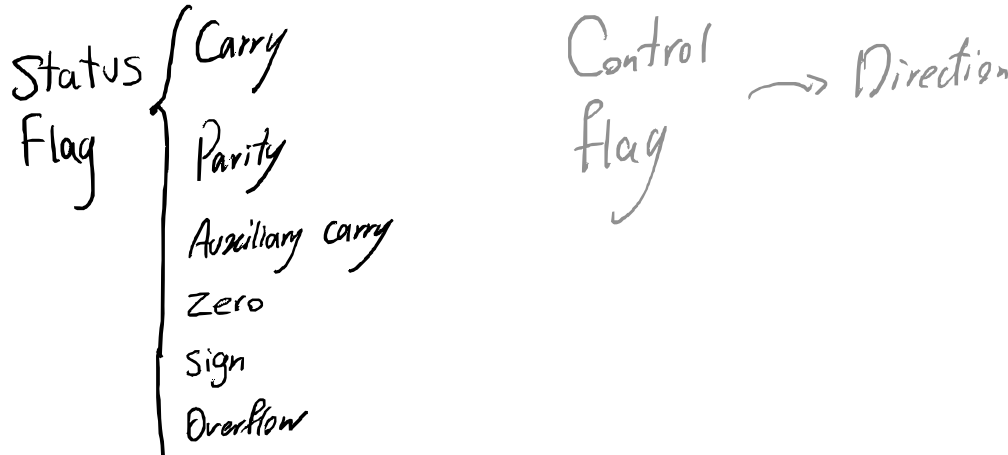
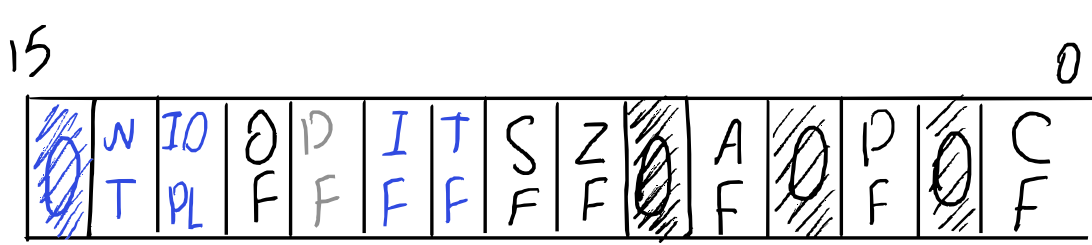


Control Registers

Flag



Protected-mode

logical addr

↓ Segmentation translation

32-bit linear addr

↓ Page translation

32-bit physical addr



TLB
Page table!

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0

CS

SS

DS

ES

FS

GS