```
set_property PACKAGE_PIN W5 [get_ports mclk]
set_property IOSTANDARD LVCMOS33 [get_ports mclk]
create_clock -add -name sys_clk_pin -period 10.00 -waveform {0 5} [get_ports mclk]


# Switches
set_property PACKAGE_PIN V17 [get_ports b]
set_property IOSTANDARD LVCMOS33 [get_ports b]
set_property PACKAGE_PIN V16 [get_ports g]
set_property IOSTANDARD LVCMOS33 [get_ports g]
set_property PACKAGE_PIN W16 [get_ports r]
set_property IOSTANDARD LVCMOS33 [get_ports r]
set_property PACKAGE_PIN W16 [get_ports rst]
set_property IOSTANDARD LVCMOS33 [get_ports rst]




#Pmod Header JA
#    #Sch name = JA1
    set_property PACKAGE_PIN J1 [get_ports AX]
        set_property IOSTANDARD LVCMOS33 [get_ports AX]
#    #Sch name = JA2
    set_property PACKAGE_PIN L2 [get_ports BX]
        set_property IOSTANDARD LVCMOS33 [get_ports BX]
    ##Sch name = JA3
  set_property PACKAGE_PIN J2 [get_ports XA]
        set_property IOSTANDARD LVCMOS33 [get_ports XA]
    ##Sch name = JA4
  set_property PACKAGE_PIN G2 [get_ports XB]
        set_property IOSTANDARD LVCMOS33 [get_ports XB]


#Pmod Header JB
#Sch name = JB1
set_property PACKAGE_PIN A14 [get_ports u]
set_property IOSTANDARD LVCMOS33 [get_ports u]
#Sch name = JB2
set_property PACKAGE_PIN A16 [get_ports d]
set_property IOSTANDARD LVCMOS33 [get_ports d]




#Pmod Header JC
#Sch name = JC1
set_property PACKAGE_PIN K17 [get_ports YA]
set_property IOSTANDARD LVCMOS33 [get_ports YA]
#Sch name = JC2
set_property PACKAGE_PIN M18 [get_ports YB]
set_property IOSTANDARD LVCMOS33 [get_ports YB]
```