Jason Ting

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FDUCATION

UC SANTA CRUZ

BS IN COMPUTER ENGINEERING

Expected graduation: June 2018 | Santa Cruz, CA GPA: 3.34

MILPITAS HIGH SCHOOL

Grad. June 2014 Milpitas, CA

COMPUTER SKILLS

PROGRAMMING

- Java (proficient) Assembly (decent)
- C (proficient) Verilog (decent)

SOURCE CODE CONTROL

• GIT

LANGUAGES

English (Native)
Mandarin (Fluent)

EXTRACURRICULAR

- Society of Asian Scientist and Engineers
- Chinese Student Association
- National Level USA Badminton Umpire

WORK FXPERIENCE

INDIVIDUAL TUTOR | ABSTRACT DATA AND ALGORITHM

January 2017 - present | Santa Cruz, CA

- Create lesson plan to help student further understand the concept
- Keep tutee engaged during tutor hour

MSI LEARNING ASSISTANT ILOGIC DESIGN TUTOR

January 2017 - present | Santa Cruz, CA

- Facilitate interactive group learning sessions for up to 12 students at a time
- Help the students work together to understand the class material
- Create a collaborative learning environment

ID TECH | CAMP INSTRUCTOR

June 2016 - August 2016 | Palo Alto, CA

- Coordinate activites with other staff members
- Be a role model and a leader for students

INCREDIFLIX I CAMP INSTRUCTOR

July 2015 - August 2015 | Milpitas, CA

- Educated children from ages 5-11 on how to create stop motion videos
- Taught me leadership skills and time management

THE TUTORING CENTER | TUTOR

September 2013 - March 2014 | Milipitas, CA

- Interacting with students from elementary school to high school
- Learned how to interact and handle problems students ask

COURSEWORK

INTRODUCTION TO OPERATING SYSTEMS | Spring 2017

• Gain knowledge of process synchronization, deadlocks, memory management, resource allocation, scheduling, and storage systems

INTRO TO ELECTRONIC CIRCUIT | WINTER 2017

- Gained experience in using a digital voltmeter to measure circuit's voltage and current
- Gained understanding of many different voltage theorem

ABSTRACT DATA AND ALGORITHM | FALL 2016

• Gained knowledge in sorting and searching techniques, basic graph algorithms, and algorithm design techniques

LOGIC DESIGN | Spring 2016

- Gained experience creating logics using K-maps and state diagrams
- Learn how to create a state machine using Verilog

COMPUTER NETWORKS | WINTER 2016

- Gained understanding of congestion control mechanisms and routing algorithms
- Designed a simple firewall using OpenFlow protocol
- Gained understanding of the inner workings of the five layers of the internet