Jason Ting

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EDUCATION

UC SANTA CRUZ

MASTER'S OF SCIENCE: COMPUTER ENGINEERING

Expected graduation:

December 2019 | Santa Cruz, CA

BACHELOR'S OF SCIENCE IN COMPUTER ENGINEERING Graduated: June 2018 | Santa Cruz, CA GPA: 3.4

COMPUTER SKILLS

PROGRAMMING

- Python Java Assembly
- C Verilog
- •HTML •CSS JavaScript

TOOLS AND IDE

- Eclipse MPLAB X
- VMs ISE Design Suite
- GIT Unix Vivado
- Oscilloscope
- MATLAB

COURSEWORK

- AI (current Operating Systems
- Logic Design With Verilog
- Logic Design
- Electronic Circuit
- Abstract Data and Algorithm
- Computer Network
- Data Structure
- Game Al
- Microprocessor Design

LANGUAGES

English (Native)
Mandarin (Fluent)

EXTRACURRICULAR

- Society of Asian Scientist and Engineers
- Chinese Student Association
- National Level USA Badminton Umpire

PROJECTS

PUBG FINISH PLACEMENT PREDICTION | CURRENT

- Given a dataset, predict the player's finishing placement
- Compare data with win rate percentage to find useful data

SMART PARKING SYSTEM |JUNE 2018

- Achieved goal of making a smart parking system that allow users to find availability of parking spots via mobile application
- Incorporated TCP/IP and REST API to send information from microcontroller to the cloud
- Used OV7670 with microcontroller to capture car image

AMAZON PRODUCT RECOMMENDATION | MARCH 2018

- Incorporated collaborative filtering to recommend users product
- Implemented cosine similarity to find similar users and K-nearest neighbors for computation

SMASH BRO MELEE BOT | JANUARY 2018

- Created an Al training bot that helps player practice their chain grabs
- Implemented using behavior tree and genetic algorithm

ULTIMATE TIC TAC TOE BOT | OCTOBER 2017

- Successfully created an AI bot that will play Ultimate Tic Tac Toe
- Applied Monte Carlo Tree Search to determine the next move
- Included heuristics to help the AI decide the next move

WORK EXPERIENCE

LAB INSTRUCTOR | Logic Design

January 2018 - March 2018 | Santa Cruz, CA

- Teaching a class of 20 students learning logic design
- Subjects taught include state machine, sequential circuits, system level design

MSI LEARNING ASSISTANT | Logic Design Tutor

January 2017 - June 2018 | Santa Cruz, CA

- Facilitate interactive group learning sessions for up to 12 students at a time
- Help the students work together to understand the class material
- Create a collaborative learning environment
- Taught students how to create a state machine and solve sequential circuit problems

READER | Logic Design

September 2017 - January 2018 | Santa Cruz, CA

- Graded student's homework and tests
- Kept in close communication with professor on any grading issues

INDIVIDUAL TUTOR |ABSTRACT DATA AND ALGORITHM

January 2017 - March 2017 | Santa Cruz, CA

- Create lesson plan to help student further understand the concept
- Keep tutee engaged during tutor hour
- Taught sorting / searching techniques and basic graph algorithm