

Hold Issue Conditions (Path Conflicts) (Page 1 of 2)

This document shows Hold Issue and Hold Advance Queue conditions. It is a comparison of the instruction in CP(-1) of the G Rank (G terms) and what has previously issued (H terms). Example: GDE is the decode for a 02 or 46 instruction in the G Rank. It may issue if there is no HDE term which is a 22 or 23 instruction at CP4. Of course the example above may be such to allow issue but issue may be held off because of register conflicts, functional unit conflicts, multi-parcel instructions issuing previously to this, etc.

GDE - 02,46 instruction (G Rank) CP(-1)
HDE - CP4-22, 3

GDF - 20, 1, 44 (CP(-1))
HDF - CP2-2, 46 or CP7-22, 3

GDG - 24 CP(-1)
HDG - CP3-2, 46 or CP8-22, 3

GDH - 25-7, 40-3 CP(-1)
HDH - CP1-24 or CP2-20, 1, 44 or CP5-2, 46 or CP10-22, 3

GEA - 132,3 CP(-1)
HEA - Go Common Memory - CP1

GEB - 120-7 CP(-1)
HEB - CP2-132, 3 or CP4-Common Memory

GEC - 106,7 CP(-1)
HEC - CP6-12X or CP9-132, 3 or CP11-C.M.

GED - 56, 104, 5, 110-3 CP(-1)
HED - CP7-12X or CP10-132, 3 or CP12-C.M.

GEE - 53, 4, 100-3, 114, 5, 130, 1 CP(-1)
HEE - CP2-56, 104, 5, 110-3 or CP3-106, 7 or CP10-12X or CP13-132, 3 or CP15-C.M.

GEF - 50-2, 116, 7 CP(-1)
HEF - CP1-53, 4, 100-3, 114, 5, 130, 1 or CP4-56, 104, 5, 110-3 or CP5-106, 7 or CP12-12X or CP15-132, 3 or CP17-C.M.
HFG - Hold Issue = CP1-0-17 or Hold Issue from IB

HFH - Multi-Parcel Instruction - CP(2, 3, 4) - 36, 7 or Adv. Q (CP1)

{ GFA - 44, 54 CP(-1)
HFA - Local Memory Busy CP(1-X)-45-7, 55-7, 74, 5