



THE VECTOR WRITE DELAY CHAIN SYNCHRONIZES THE WRITING OF DATA INTO A VECTOR REGISTER WITH RESULTS FROM A FUNCTIONAL UNIT.

ENTRY POINTS INTO THE DELAY CHAIN ARE DETERMINED BY INSTRUCTION AND RESULT REGISTER DECODE.

ENTRY POINT	INSTRUCTION	OPERATION
V-A	166,7	RECIPROCAL OR SQUARE ROOT APPROXIMATION
V-E	154-7, 170-5	FLOATING MULTIPLY, SQUARE ROOT/RECIPROCAL ITERATION, OR FLOATING ADD
V-K	150-3, 160-5, 176,7	VECTOR SHIFT, INTERGER SUM, POP/LEADING ZERO COUNT, OR COMPRESS IOTA
V-M	74	LOCAL MEMORY READ
V-N	140-7	VECTOR LOGICAL OR VECTOR MASK