

Multiplication of the two 48 bit coefficients yields in a 96-bit result. Our 64-bit result register will receive a number of floating-point format. This result requires a 48-bit coefficient. Since the binary point is to the left of the coefficient we truncate the right hand 48 bits of our result coefficient. These are the least significant 48 bits.

Instruction Summary For Multiplication

124ijk Enter Si with floating product $S_j * S_k$
125ijk (same as above)

154ijk Enter Vi with floating product $V_j * V_k$
155ijk Enter Vi with floating product $V_j * V_k$