

Length Register

This is a 10 bit register used to count the number of 64 bit words transferred to or from common memory. Only a read or write may be active at a time.

Status Register

This 12 bit register retains the EB status. The status contains syndrome, single and double bit error flags, and single and double bit interrupt enable flags. See hardware reference manual for bit assignments.

Common Memory Error Address Register

This is a 32 bit register which contains a common memory erroring address. A double bit memory error will over write a single bit erroring address regardless of the status of the interrupt enable flags.

I/O Memory Control

The EB communicates with the IB module to do I/O memory references to common memory.

Common Memory Address Register

This is a 32 bit register used for holding the address for common memory references.