

# Test Point Descriptions

14.1000

## Memory

- 01) External Reference (EB IB) - A one clock pulse at the start of an external R/W reference.
- 02) End Sequence (IB EB) - A one clock pulse that occurs when the IB has completed the last external reference to memory.
- 03) Arrival Go Bit (TD EB) - There will be VL pulses that occur on an external read reference. They indicate a valid word was sent to the EB from memory.
- 04) Go Write Data (IB TE) - There will be VL pulses that occur on an external write reference. They indicate a valid word was sent from the EB to memory.
- 05) Valid Data (IB TE) - There will be VL pulses that occur on any memory reference. They indicate a valid reference was sent from the IB to memory.
- 06) Retry (IB TE) - A five to eleven clock pulse that occurs when memory is in a backup condition. They indicate that the references in backup should be retried.
- 07) Go Quad 0 (TE TC) - A one clock pulse that occurs any time the memory reference is for Quad 0. It indicates that the TC module should capture address/data and send it to memory Quadra 0.
- 08) Bit 22 Quad 0 (TC SB) - This is bit 22 of the memory reference packets for Quad 0. If this bit is set in the 1st of the 4 packets, it indicates that the memory reference is a read. The remaining 3 packets are N/A.
- 09) Bit 23 Quad 0 (TC SB) - This is bit 23 of the memory reference packets for Quad 0. If this bit is set in the 1st of the 4 packets, it indicates that the memory reference is a write. (This bit is also set when there are no references active.) The remaining 3 packets indicate data.
- 10) Go Bank 0 (QB SB) - This is a one clock pulse that activates memory Bank 0 for the reference. It indicates that there is a valid reference for Bank 0.