6) A Register Readout Code

0GA-0GC

Two consecutive clock periods of A register readout code are sent out to the AR module. The first clock period determines the Ak designator and the second clock period relates to the Aj designator. These two A registers will then be readout to the correct place.

7) A Register Entry Code

The A registers (AR module) can be entered by six different paths. The A register entry code selects the correct path and A register to be used. Below are the Boolean terms used.

OEA - OEC = Ai designator
OED - OEF = Entry code (see below)

Entry code	Description	Instructions
0	No Entry	
1	Vector Length Data	025
2	Local Memory Data	044, 046
3	S Reg. Data	024
4	Constant Data	026, 027, 040-043
5	AM module Data	002, 022, 023
6	Adder Data	020, 021
7	N/U	

8) A Register Tag Delay Chain

This is a 12 clock period delay chain that controls the A register entry code described above. An A register type instruction will enter the Ai designator and Entry Code in at the appropriate place within the delay chain. See A/S Register Tag Delay Chain diagram.

9) VR Module Source Code

The VR module source code is sent to the VR module to select the correct paths and vector or scalar registers. Following are the Boolean terms used.