

BACKGROUND PORT

The background port provides the interface (node) between the foreground processor and the background processor. It controls the background operation by loading and monitoring the background P register and being capable of starting and stopping the background. It also monitors background operation and generates interrupts for the channel loop. The background port is found on the EA module.

The main areas on the EA are:

- Channel Function Decode
- Background Port Status Register
- Interrupt Address Register
- Background local memory parity storage and check
- Master Clock Fanout

Channel Function Decode

The EA module excepts channel loop functions from the foreground and can respond with status back to the foreground. Functions 10-17 can be accepted by the EA module. See following diagram and the hardware reference manual for further description.