127 INSTRUCTION CP 3
157 INSTRUCTION CP 3

127 INSTRUCTION CP 4
157 INSTRUCTION CP 4

CP 5
CP 6/IIOLD

FUNCTIONAL UNIT ENABLE
VECTOR LENGTH CONTROL
157 INSTRUCTION

CP 6/IIOLD

SQUARE ROOT ITERATION
ROUND CONTROL
(127,157)

(1)
QJC
MA

IIOLD FOR VL

QJR

JGE

QGB CP 5

QGA CP 4
IUA
JB

124-127 INSTRUCTIONS
SCALAR-SCALAR MULTIPLY,
RECIPROCAL ITERATION,
OR SQUARE ROOT
ITERATION

127-CP 4
QKB

QAA CP 3
IVA
JA VIA WA

MODE BIT j
f FIELD 2^0

QEB CP 4

QEA CP 3
IVE
JA VIA WA

MODE BIT k
f FIELD 2^1

157-CP 5
QKD

QFB CP 4

QFA CP 3
IVF
JA VIA WA

154-157 INSTRUCTIONS
SCALAR-VECTOR OR VECTOR-
VECTOR MULTIPLY, RECIPROCAL
ITERATION, OR SQUARE ROOT
ITERATION

QCB CP 4

QCA CP 3
IVC
JA VIA WA

SQUARE ROOT ITERATION ROUND CONTROL IS
USED TO GATE THE SQUARE ROOT ITERATION
ROUND BITS INTO THE PYRAMID ADD
PROCESS. THESE ROUND BITS, AS FAR AS
THE MA'S PORTION IS CONCERNED, ARE
SPECIFIC TO THE RECIPROCAL ITERATION
INSTRUCTIONS.

B-2379A

ME MODULE SQUARE ROOT ITERATION ROUND CONTROL