

C. PADDING:

- 1) All padding is to be within 0 to +30 grids of the specified length .
- 2) The longer of an I or O - term compliment pair is to be drawn as short as possible.
- 3) Compliment I - terms must be drawn to within ten grids in length. Any padddding that is required shall only be placed on the shorter of the compliment pair.
- 4) Compliment O - terms that are more then ten (10) grids apart in length are to be brought to the engineers attention. DO NOT PAD UNLESS DIRECTED TO BY THE ENGINEER. Those O - terms that the engineer choses not to pad are to be drawn as short as possible.

D. OTHER DRAWING RULES:

- 1) The .ZZO circuits must be drawn with ten (10) loads or less per transistor. Each segment must be 540 grids or less counted from the transistor to the last load. The center pin of the transistor must be a jumper location and the transistor must be placed in a legal resistor location.
- 2) When drawing to several loads always place a vertex in each pad.
- 3) Terminate F, R and S chip outputs to a 180/120 resistor.
- 4) Final board test is to be run with a newly edited artwork listing.
- 5) While in process board backup disks contain the current .PCB file and the current edited artwork listing. After board check out the board backup disks contain the final edited artwork listing and 0, 1, 2, 3, and if needed 5 Cray 2 format files. A third disk contains the .PCB file. At the end of any shift both board backup disks will match.