TD MODULE COMMON MEMORY READ DATA DESTINATION TAGS

There are four destinations for data read from Common Memory. The read data can be sent to a Scalar Register, a Vector Register, a Instruction Buffer, or to the Foreground I/O loop. Every word read from Common Memory has a destination tag associated with it. This tag contains information about where the data came from and where the data is going to.

The destination tag comes over six lines from the Q series module of the data's associated memory quadrant and is sent to the TD module associated with the requesting processor. The TD module decodes the upper three bits of this tag and determines the intended destination of the data word. This decode puts the tag into one of the ranges listed in the following chart.

DESTINATION TAGS FROM Q? MODULE	
00-07	NO DESTINATION SELECTED
10-13	EXTERNAL DATA TO EB MODULE
20-23	INSTRUCTION DATA TO IA MODULE
30-37	SCALAR REGISTER DATA
40-57	LOWER VECTOR (0-3) REGISTER DATA
60-77	UPPER VECTOR (4-7) REGISTER DATA

10000