RBA
RBF
RBG
qca
QOH
CP3 OIA
CP10 OIA
BRANCH PREAMBLE (IA)

RBC
CP5 OIB
BARREL TEST (IA)

QQC
QOH
CP10 OID
BRANCH OUT (IA)

QLP
CP9 OJC
MEMORY BUSY (JA)
(STAY SET UNTIL QLP CLEARS)

CP2-BOUNDRY RBA
CP7-BOUNDRY RBF
FETCH REQUEST QOA
FETCH SEQUENCE QQC
GO BRANCH REB
CP8-BOUNDRY RBG
OUT OF STACK qcc

CP3 OJD
HOLD ISSUE (STAY SET UNTIL QQC CLEARS) (JA)

HOLD OH~
WORD ADDRESS 2^3-2^0 (IA)

(SEE NOTE #1)
(IA) STACK BOUNDRY IHB
NO CP2-BOUNDRY rba
NO CP3-BOUNDRY rbb
NO CP4-BOUNDRY rbc
NO CP5-BOUNDRY rbd
NO CP6-BOUNDRY rbe
NO CP7-BOUNDRY rbf
NO CP2-02 INSTRUCTION rja
NO CP3-02 INSTRUCTION rjb
NO CP4-02 INSTRUCTION rjc
NO CP2-0,1,3-17 INSTR. raa
NO CP3-0,1,3-17 INSTR. rab
NO FETCH REQUEST qqa
NO FETCH IN PROGRESS qqb
NO ABORT FETCH qqn
NO INTERRUPT qta
NO PROCESSOR IDLE qtx

HOLD OG~
FIELD ADDRESS 2^{27}-2^4 (IA)

BARREL RESPONSE (IA) iha
CP8 qca qcc
OUT OF STACK

CP2 RBA
CP3 RBB
CP4 RBC
CP5 RBD
CP6 RBE
CP7 RBF
CP8 RBG
CP9 QWA
CP10 QWB
CP11 QWC

NO BRANCH raa
rja
(SEE NOTE #2)

qcc
CP9 QOH
CONTINUE PREAMBLE

(SEE NOTE #3)
HOLD
RBE
NO BRANCH CP2 raa
NO 002 INSTR.CP2 rja
CP7 QOA
FETCH REQUEST

HOLD (SEE NOTE #4)
SAME
CP8 QQB QQW
FETCH IN PROGRESS

HOLD (SEE NOTE #5)
FETCH REQUEST QQA
NO FETCH IN PROGRESS qqb
NO ABORT FETCH qql
NO PORT BUSY qlp
CP8 QQC
FETCH SEQUENCE

HOLD (SEE NOTE #6)
SAME
CP8 QLP QLQ
PORT BUSY

C-2915

NOTE #1
    STACK BOUNDRY SIGNAL IS SENT FROM THE IA TO THE IB MODULE WHEN THE LAST WORD IS READ OUT OF AN INSTRUCTION FIELD ON THE IA MODULE.

NOTE #2
    A BRANCH SEQUENCE (RAA,RJA) WILL CLEAR THE BOUNDRY SEQUENCE (RBB-RBG).

NOTE #3
    HOLD FETCH REQUEST (QQA) UNTIL FETCH SEQUENCE (QQC) OR UNTIL ABORT FETCH (QQL) IS INDICATED.

NOTE #4
    HOLD FETCH IN PROGRESS (QQB,QQW) UNTIL FETCH COMPLETE (IHC) OR UNTIL ABORT FETCH (QQL AND QQC) IS INDICATED.

NOTE #5
    HOLD FETCH SEQUENCE (QQC) UNTIL END FETCH (QQK) OR UNTIL ABORT FETCH (QQL AND QQC).

NOTE #6
    HOLD PORT BUSY (QLP, QLQ) UNTIL END FETCH (QQK) OR UNTIL CLEAR PORT (QQM).

IB MODULE - OUT-OF-STACK BOUNDRY SEQUENCE