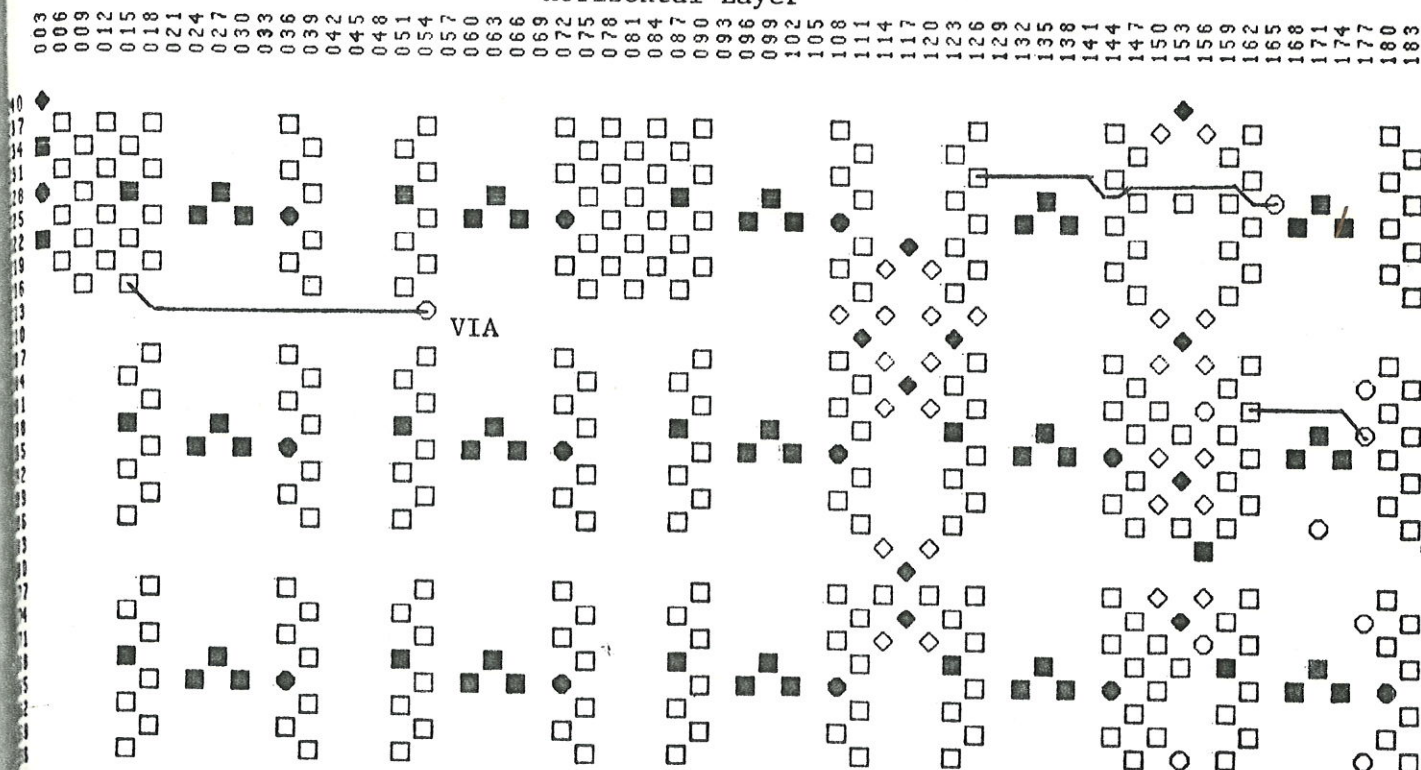


On every board of the 8 boards that make up a module there is a horizontal layer and a vertical layer. Foil lines are drawn on each layer to get from one hole location to another. In order to connect these two layers we put VIA's in the board. On the drawing these VIA's are shown as circles. They maybe drawn at any hole location that is not all ready being used by a jumper or chip location holes. They also may not be drawn to the right of the Z connector field. When drawing the two layers on mylar you will be given two sheets of mylar one for the horizontal and the other for the vertical.

Horizontal Layer



Vertical Layer

