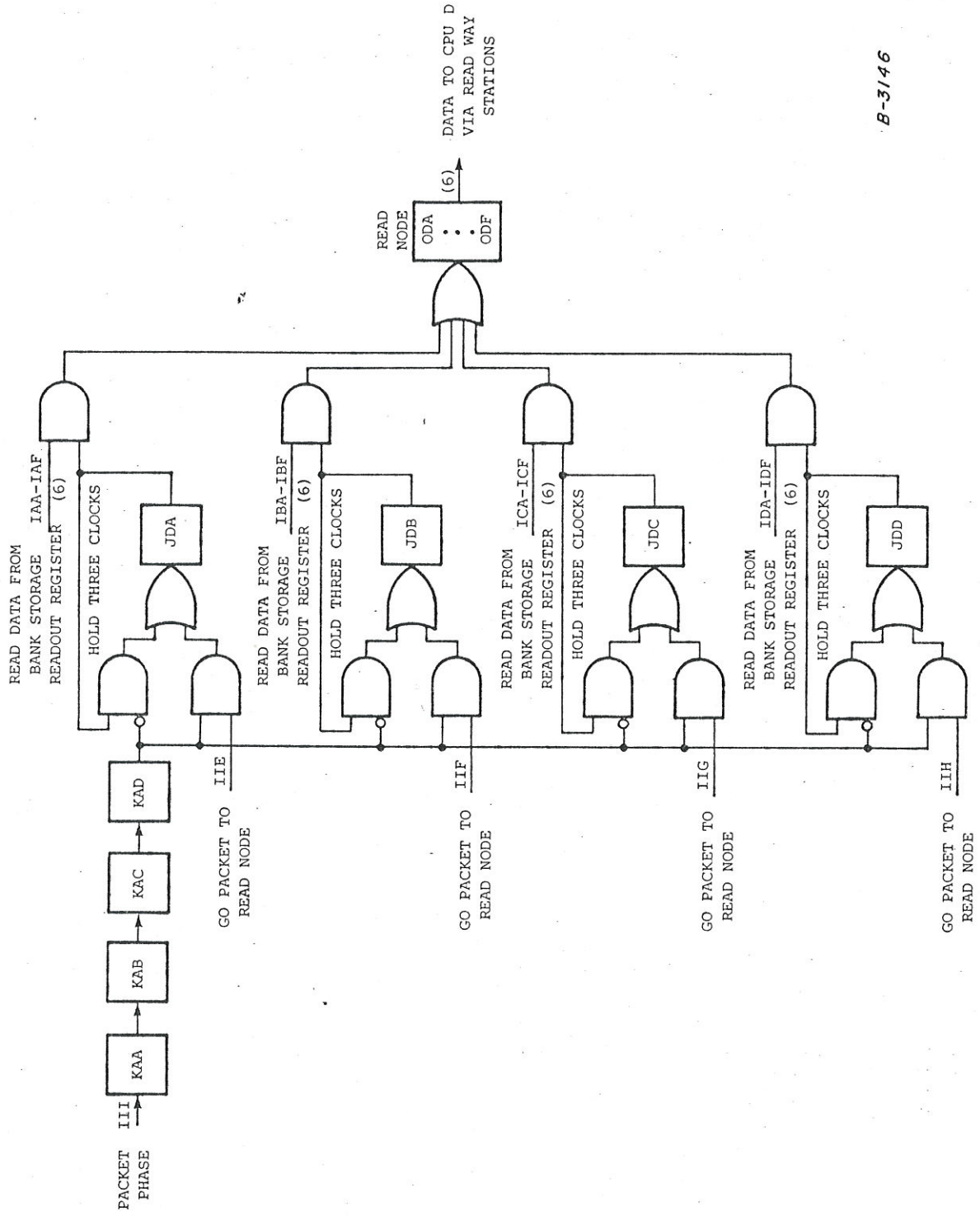


PHASE 0,1,2,3

PHASE 3,0,1,2

PHASE 3 PHASE 0 PHASE 1 PHASE 2



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SE MODULE READ NODE DATA TO CPU D

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