

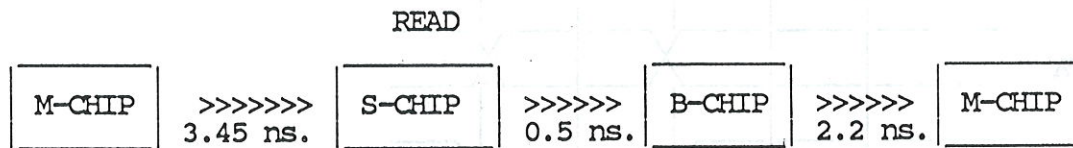
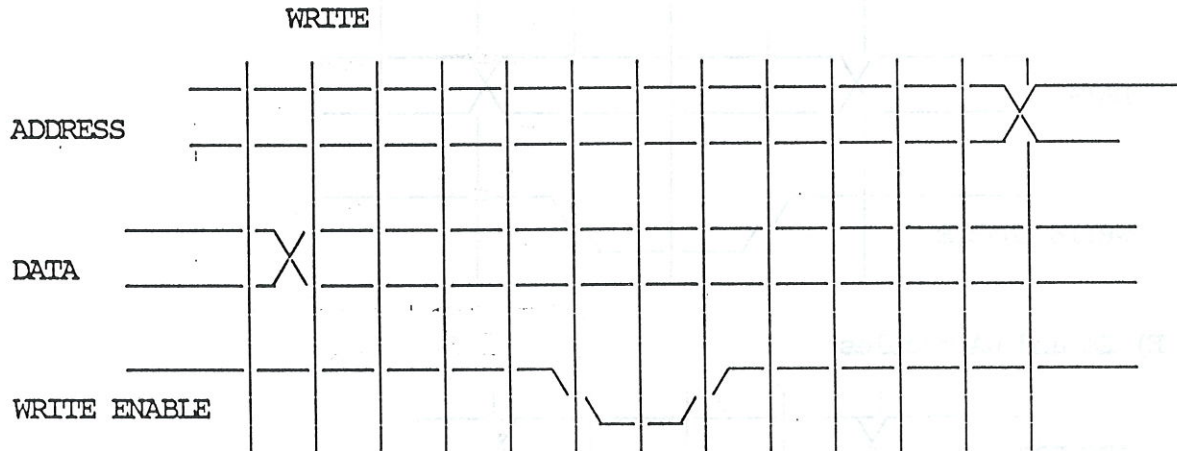
Artwork Groundrules - 05.07.87

APPENDIX B

B2

KB module - Access time during a read is about 12 n.s. ( prop delay of unlatched chip and foil delay )

C) RA module



Address = 16 ns.

16.00
- 3.45
- 2.20
- 0.50
9.85 ns access