

003 (Branch Out)

T00     Instruction issues in JA module.  
         First Parcel of Constant moves JA to IB.

T01     Translate Instruction in IB - G Rank.  
         First Parcel of Constant in IB - E Rank.

T02     Begin Stack limit tests on IB - RAA to RAG.  
         Second Parcel of Constant moves JA to IB.

T03     Constant assembled in IB.  
         New address pointer moves IB to IA.  
         Branch preamble moves IB to IA.  
         Hold issue moves IB to JA.

T04     Translate new pointer in IA.

T05     Barrel test moves IB to IA.

T06     Coincidence tests in IA.  
         Memory Busy moves IB to JA.

T07     Coincidence test results moves IA to IB.  
         Base address to 3rd adder moves IB to TA.  
         IB data increment moves IB to TA.

T08     IB translates tests results.  
         Digit to 2nd adder moves IB to TA.  
         Increment to 3rd adder moves IB to TA.  
         Branch address moves IB to TA.

T09     Branch out moves IB to IA.  
         Go Branch moves IB to JA.  
         Hold data 2nd adder moves IB to TA.  
         Destination code moves IB to TA.  
         Base address and Branch address assembled in TA.

T10     Parcel pointers move IB to JA.  
         New P loaded in IB.  
         JA is waiting for Ready Pulse.  
         Valid data moves IB to TA.  
         Reference address in TA - A Rank.

T11     Address in TA - D Rank.  
         Enable Range test moves IB to TA.

T12     Address in TA - F Rank.

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