

BIT POSITION	CHIP LOCATION	BOOLEAN TERM
2 ⁰	E/R	WED
2 ¹	K/R	WFD
2 ²	Q/R	WGD
2 ³	W/R	WHD
2 ⁴	E/N	WEC
2 ⁵	K/N	WFC
2 ⁶	Q/N	WGC
2 ⁷	W/N	WHC
2 ⁸	B/R	WEB
2 ⁹	H/R	WFB
2 ¹⁰	N/R	WGB
2 ¹¹	T/R	WHB
2 ¹²	B/N	WEA
2 ¹³	H/N	WFA
2 ¹⁴	N/N	WGA
2 ¹⁵	T/N	WHA

Example: Bit 2⁸ is failing on all parcels
Chip at B/R, boolean term WEB is suspect.

DA MODULE SECONDARY BUFFER
4K x 1 CHIP LOCATOR CHART

C0005S0501