

003 (Branch In)

- T00 Instruction issues in JA module.
 First Parcel of Constant moves JA to IB module.
- T01 First Parcel of Constant in IB - E Rank.
 Translate instruction in IB - G Rank.
- T02 Second Parcel of Constant moves JA to IB module.
 Begin Stack limit tests on IB - RAA-RAG.
- T03 Constant assembled in IB module.
 New address pointer moves IB to IA.
 Branch preamble moves IB to IA.
 Hold issue moves IB to JA.
- T04 Translate new stack pointer in IA.
- T05 Barrel test moves IB to IA.
- T06 Coincidence tests in IA module.
 Memory Busy moves IB to JA.
- T07 Coincidence tests Results move IA to IB.
- T08 IB translates test Results.
- T09 Branch in signal moves IB to IA.
 Go Branch signal moves IB to JA.
 Stack limit tests done on IB.
- T10 Parcel pointers move IB to JA.
 New P loaded in IB module.
 JA module is waiting for Ready.
- T11 Hold issue drops IB to JA.
 Data Ready moves IA to JA.
- T12 Memory Busy drops IB to JA.
 Enable issue ques.
- T13 New Parcel captured in JA.
 Resume moves JA to IA.
- T14 New Parcel in Rank C of JA.
 Advance Que moves JA to IB.
- T15 New Parcel in Rank E of JA.
- T16 New Parcel in Rank G of JA.
 Data Ready moves IA-JA.
- T17 Instruction in Issue position.