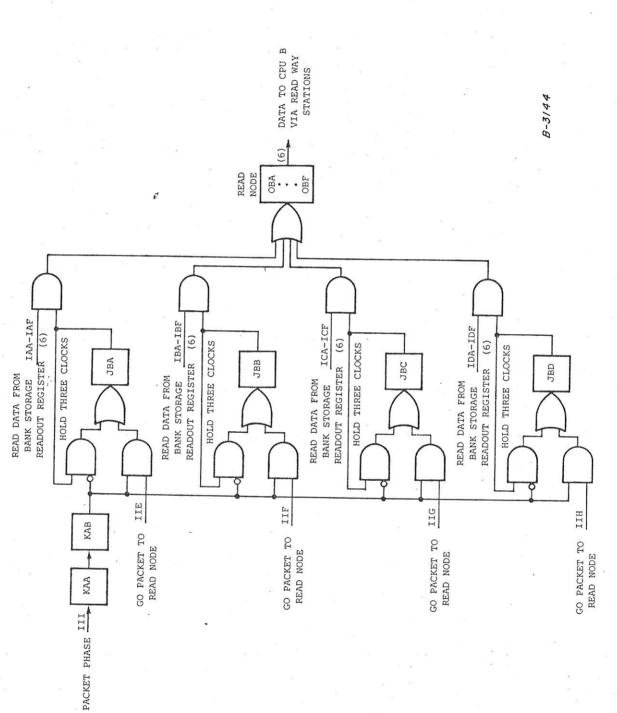
PHASE 3 PHASE 0



SE MODULE READ NODE DATA TO CPU B