176,177 INSTRUCTION   CP 2   CP 3   CP 4   CP 5   CP 6   CP 7   CP 8   CP 9

FIRST POSSIBLE RELEASE

GO IOTA 176,177

IRD / JA

176,177

QAD — DELAY QEA — DELAY QEB — DELAY QEC — DELAY QED

176,177   QGB

HOLD UNTIL ALL ZEROS DETECTED

QHA

OEB (2) OEC   RELEASE JC's (VECTOR REGISTER)

JBB   RELEASE

QJA ... QJE

JAA ... JAP

IOTA MASK BITS 0-62

UAA   UPC

IEA-IHO VR's(63)

Sj DATA FOR IOTA MASK BITS 0-62

JBB IS SET IF ANY OF THE LOWER 63 BITS OF THE IOTA MASK ARE SET. THE MASK IS SHIFTED LEFT ONE PLACE EACH CLOCK AND THESE BITS ARE SAMPLED. WHEN JBB CLEARS A ONE CLOCK PERIOD RELEASE SIGNAL IS SENT TO EACH OF THE JC MODULES.

B-2273

VA MODULE RELEASE SIGNAL TO JC's