JA Module Functions (only one)

    1.   Instruction Issue/Decode Control

    2.   A/S/V register reservation flags

    3.   Functional Unit Busy Flags

JB Module Functions (only one)

    1.   Assists the JA vector instruction timing

    2.   Coordinates vector write sequences with JC modules.

    3.   Contains Vector Length counters for each set of Functional Units.

    4.   Assembles instruction constants and outputs them to the A/S/V registers.

    5.   Relays data from the A to S registers and provides sign extention.

    6.   Generates CPU and Common Memory Phase signals

JC Module Functions (two used)

    1.   One JC module controls four vector registers
        JC @ DE controls vector registers 0-3.
        JC @ DD controls vector registers 4-7.
        Provides addressing, write and stop control to the VR modules.

    2.   Contains a vector length counter for each vector register.