

The SB_ - SD_ terms are the servo counter. The servo counter is incremented by one for every positive servo pulse which occurs once every 1.24 micro seconds. A index pulse from the DSU or a servo count of 1351₈ clears the Servo counter. This counter is used by the DSU read and write circuitry (via SE_, SF_ terms) to activate the DSU read or write commands to the disk drive, (see sector profile). The sector counter is incremented by one for every Servo count of 1351₈. The index pulse also clears the sector counter.

8. DSU Tag and Buss line control

The terms MA_ - MB_ directly active the DSU Tag and Buss lines. The terms are in sync with the DSU clock via the servo clock generation and control circuits.

9. Main Buffer and Control

The main buffer (WA_ - WD_ terms) holds 2056₁₀ - 16 bit parcels (2048 parcels of data and 8 parcels of CRC). The main buffer transfers data to and from the channel, and to and from the secondary buffer.

The control (see control diagram) controls the loading and unloading of this buffer.

10. Secondary Buffer and Control

The secondary buffer holds 2056 - 16 bit words (2048 parcels of data and 8 parcels of CRC). The secondary buffer transfers data to and from the DSU and to and from the main buffer.

11. DSU Write Control

Boolean terms QI_ - QK_ control the writing of data and CRC to the DSU.

12. DSU Read Control

Boolean terms QL_ - QR_ control the reading of data and CRC from the DD29. The types of reads are; normal read, auto read, read any I.D. (after a cylinder select), and read early (start reading 512 x 64 bit words at the I.D. field).