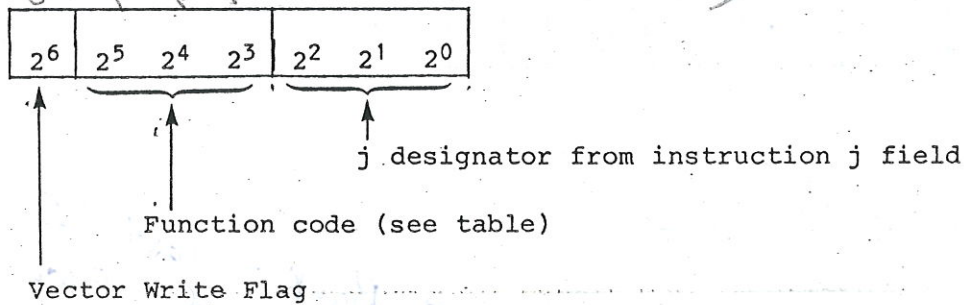


VR MODULE SOURCE CODES

The VR modules receive two sequential clock periods of a seven bit source code from the JA module via a one clock relay on the AM module. The first seven bit code is latched up on the VR modules clock period three of an instruction, and is called the Primary Source Code. The second seven bit code is latched up on the VR's clock period four and is called the Secondary Source Code. The code arrives on the VR's through ISA-ISG.

PRIMARY SOURCE CODE



Code	Function
0	No Function
1	Vector Integer Unit
2	Local Memory
3	Common Memory
4	Floating Add Unit
5	Floating Multiply Unit
6	Vector Logical Unit
7	Scalar Logical Unit

SECONDARY SOURCE CODE

