

#### BOUNDARY CONDITION

The IA module sends the signal stack boundary to the IB when it has read the last word out of a instruction field. The IB module will treat the boundary condition just like a branch sequence. The next field address is generated internally on the IB. This is used to check for in-stack conditions.

#### DEADSTART SEQUENCE

The deadstart sequence is very similar to a branch sequence. The foreground channel load the P register with a value. The IA modules barrel tag ranks are forced to an all ones value. The IB then does a branch sequence. Note: A deadstart to the upper 16 words of common memory will not work. This would cause an in-stack condition.

#### START

The background port can be functioned by the foreground channel to restart the background processor. The IB will use the current P register value and perform a branch sequence.

#### EXIT INSTRUCTION (000, 001)

These two instructions idle the background processor. They also supply the J, K fields of the instruction to the status register on the background port. The idle and exit flags will also be set the register.