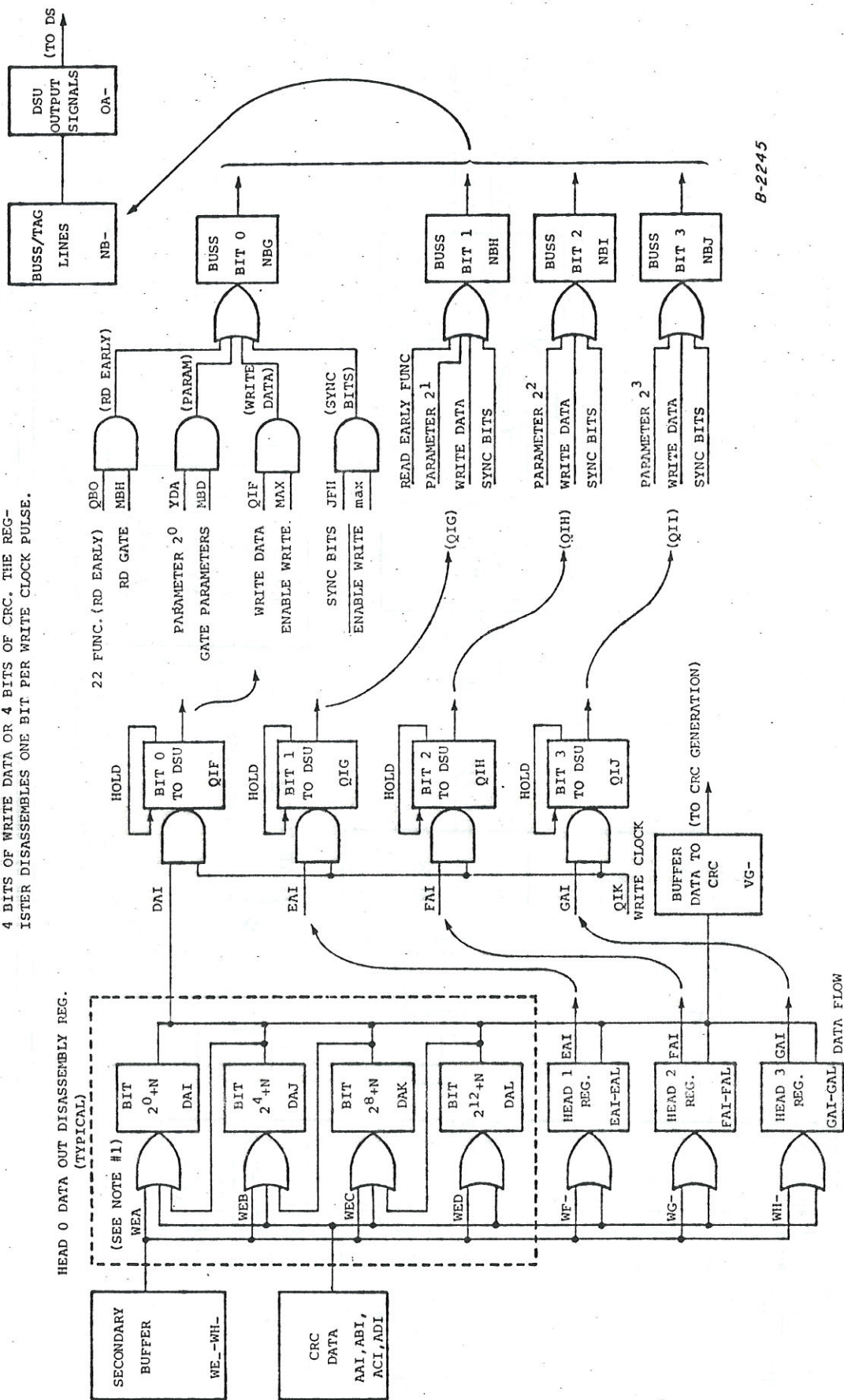


NOTE #1; THE DISASSEMBLY REGISTERS ARE LOADED WITH 4 BITS OF WRITE DATA OR 4 BITS OF CRC. THE REGISTER DISASSEMBLES ONE BIT PER WRITE CLOCK PULSE.



B-2245

SECONDARY BUFFER TO DSU