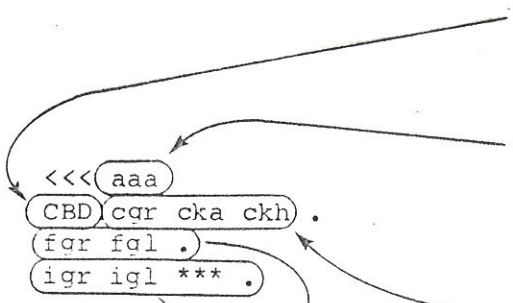
COMPOSITE ARTWORK LISTING

The composite artwork listing is used to draw the modules. It is very useful for troubleshooting on a module level. The listing tells you where each Boolean term feeds to as well as the jumpers it feeds through on the way. The <<< indicates a new term in the listing. The term next to the <<< is the name of the Boolean term involved. The listing is in alphabetical order in terms of the Boolean terms. The inverted and upright Boolean terms are listed separately and are indicated by lower and upper case letter names. The upper case term in the listings below the Boolean term line is the driving chip. The three letter indicators below the Boolean terms describe a map location and a pin. The first two letters indicate the map location and the third letter indicates a pin.




The driving chip is located at the CB- location of the ABC board. The driving pin is pin D of the chip at CB-.

The Boolean term made at location CB- is AAA and the aaa indicates that this listing is for the inverted term. The inverted term comes off of pin D, as indicated above.

The driving chip at CB-, pin D, hits two loads on the ABC board. It is connected to the chip at CK- at pin A. It is also connected to the chip at location CK- pin H. Location CG-, pin R is a chip field jumper. This jumper brings the signal down to the DEF board at location FG-, pin R.

The DEF board receives the signal from the ABC board via the jumper at FG-, pin R. The original signal hits one load on the DEF board. It is connected to the chip at location FG-, at pin L. The signal is then fed to the GHI board via the jumper at FG-, pin R. This jumper connects to the GHI board at the jumper at IG-, pin R.

The signal hits one load on the GHI board. It is connected to the chip at location IG-, at pin L. The *** indicates that the signal is terminated, through 60 ohms, to -2V next to the chip at location IG-.

C0001C0621