

Loaded into the foregrounds  
channel data register

Function	P-0 2 <sup>15</sup> --- 2 <sup>0</sup>	P-1 2 <sup>15</sup> --- 2 <sup>0</sup>	Description
10	0	0	Immediate response
11	0	0	Delay response until after a data sequence to store the P & S register
12	0	0	Delay response until after the background clears the idle flag in the status register.
13	0	0	Delay response until after the background sets the idle flag in the status register.
14	P register 2 <sup>31</sup> --- 2 <sup>16</sup>	P register 2 <sup>15</sup> --- 2 <sup>0</sup>	Immediate response
15	Status reg. 2 <sup>31</sup> --- 2 <sup>16</sup>	Status reg. 2 <sup>15</sup> --- 2 <sup>0</sup>	Immediate response
16	0	0	Immediate response
17	0	0	Immediate response

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Function responses for the background port functions