SE MODULE WRITE DATA FLOW TO MEMORY

ADDRESS AND DATA COMES
TO THE STORAGE MODULE
IN 4-24 BIT PACKETS.
THE PACKETS COME HERE FROM
THE FOUR MODULES OF THE
WRITE NODE, SIX BITS FROM
EACH MODULE IN THE MODE.

IPA-IPX
TC VIA SE (24)

MEMORY
ADDRESS
REGISTERS

BAA ••• BAX   FIRST PACKET   (24)

HOLD UNTIL NEXT REFERENCE

DATA
WRITE
REGISTER

WAA ••• WAX   SECOND PACKET   (24)

HOLD UNTIL NEXT REFERENCE

DATA
WRITE
REGISTER

WBA ••• WBX   THIRD PACKET   (24)

HOLD UNTIL NEXT REFERENCE

DATA
WRITE
REGISTER

WCA ••• WCX   FOURTH PACKET   (24)

TRANSLATORS
/DRIVERS

800 ••• 871

(24) (24) (24) (24)

DATA (72)

STORAGE

256K STORAGE CHIPS
DYNAMIC MOS
2 MILLION 72 BIT WORDS

MAA-MCX

A COPY OF THE GO BANK SIGNAL IS SENT
TO EACH OF THE FOUR MODULES IN THE
WRITE NODE. IIA IS ONE OF THE FOUR
COPIES. IIA IS ALSO USED TO GATE THE
DATA INTO THE PROPER BANK MODULE AND
SET THE WRITE ENABLE ON THAT MODULE.

GO PACKET TO
WRITE NODE
(GO BANK)

IIA
QF VIA SF

TAB   TAC   TAD   TAE   TAF

EVENT TIME   T01   T02   T03   T04   T05   T06

B-3091