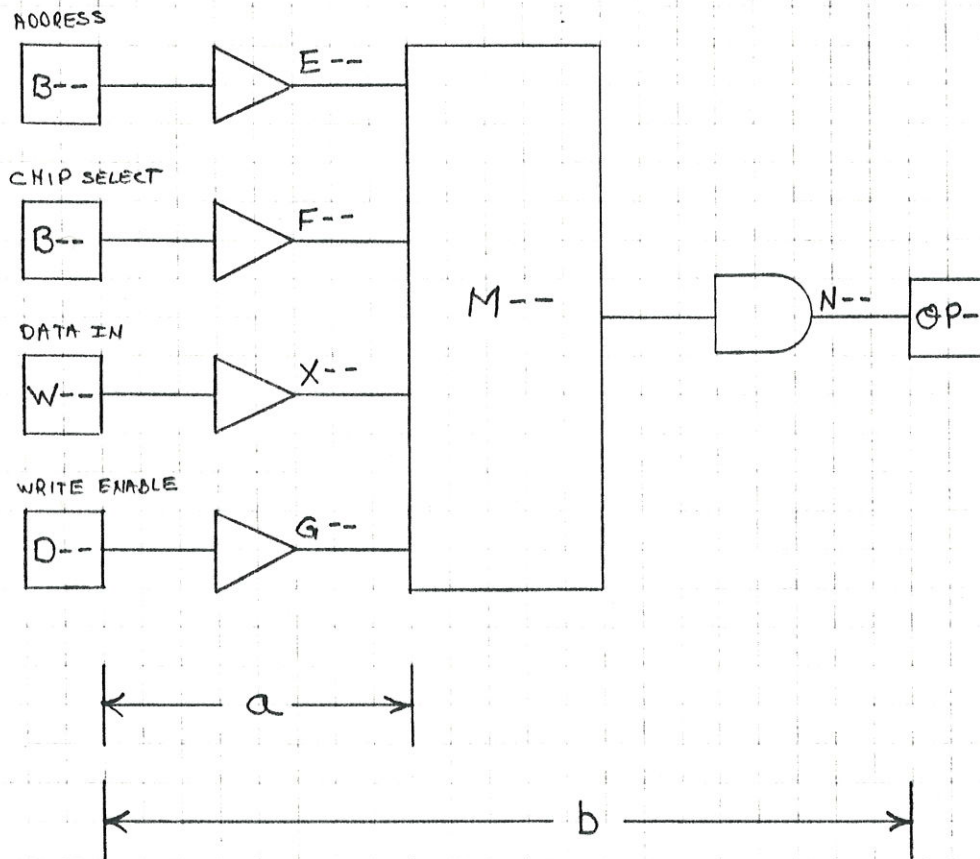


SF MODULE NOTES

9/9/86 JWS

PG 6 OF 6

• LINE BALANCE



- FOR A GIVEN MEMORY CHIP ; DISTANCE "a" IS TO BE BALANCED
- FOR ALL MEMORY CHIPS ; DISTANCE "b" IS A CONSTANT