

Indicator Light Descriptions

11.1000

**** Note:** The signals have to be on at least 50% of the time to light the LED.

Foreground (Column A)

- 1) KA (OJA - OJC) - Indicates which octant is selected. Octal count 0-7.
- 2) KA (OJD - OJO) - Indicates octant 4-7 chip addresses.

CPU (Column B)

- 1) JA (OWA - OWP) - Indicates the instruction that will issue next. (G Rank)
- 2) JA (OWQ) Advance Que - Indicates that the JA, IB, and JB have advanced the next instruction through their decode ranks.
- 3) JA (OWR) Issue - Indicates a valid instruction has issued.
- 4) JA (OWS) Rank C Valid - Indicates there is a valid instruction in Rank C decode rank.
- 5) JA (OWT) Rank E Valid - Indicates there is a valid instruction in Rank E decode rank.
- 6) JA (OWU - OWX) Parcel Pointer - Indicates the next instruction parcel that will enter Rank C decode rank.

CPU (Column C)

- 1) IA (OKA) Enable By-Pass - Indicates that there is a branch out sequence without a boundary flag. This enables the first instruction word to bypass the instruction stack and go directly to the JA.
- 2) IA (OKB - OKD) Early Readout - Indicates at least one instruction word has been received from all four memory quads after a branch out.
 - OKB-D - 3 words valid for JA
 - OKB-C - 2 words valid for JA
 - OKB - 1 words valid for JA
- 03) IA (OKE) Enable Barrel Entry - Indicates a branch out has occurred and the new address hasn't been entered into the barrel.