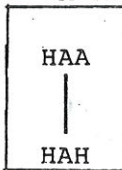


# CP1

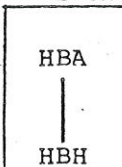
## A Register Reservations (0-7)



The A Register Reservation flag is set for the designated Ai register at CP1 of these instructions 02, 20-4, 44, 46. The flag will be cleared at the following clock period for the given instruction - CP3-24, CP4-20, 11, 44, CP7-2, 46, CP12-22, 3.

Example: if a 44 instruction is executed and the Ai is 3 then HAH would be set for 3 CPs starting at CP1.

## S Register Reservations (0-7)



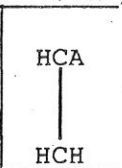
The S Register Reservation flag is set at CP1 for the designated Si register for the following instructions - 53, 54, 56, 60, 62, 64, 66, 100-15, 120-33. The flag will be cleared at the following clock period for the given instruction - CP3-53, 4, 100-3, 114, 5, 130, 1, CP6-56, 104, 5, 110-3, CP7-106, 7, CP14-120-7, CP17-132, 3, CP19-C.M. (minimum time)

Example: if a 115 instruction is executed the designated Si register would set at CP1 time. If would clear at CP3 time. The register was resumed for 2 CPs.

*60-66 are for common memory*

*As soon as C.M. ref. complete*

## V Register Reservations (0-7)



The V Register Reservation flag is set at CP1 time for the designated Vi, Vj, Vk registers for the following instructions - 30-3, 70-5, 140-77. The JC module controls the clearing of the flags.