Pin labels (left side): A, B, C, VCC, D, E, F, G
Pin numbers: 1, 2, 3, 4, 5, 6, 7, 8 (left), 16, 15, 14, 13, 12, 11, 10, 9 (right)
Center label: GATE ARRAY
Pin labels (right side): N, M, L, K, VEE, J, I, H

Logic diagram inputs/outputs:
E, D, J → f, G, i, H
L, K, C → m, N, b, A

M 20 l

fad fan fae faf

Gate Swap

Boolean equation: G,f = DE
H,i = DJ
N,m = KL
A,b = KC

Type: J
Name: Dual 2 Input Buffer with Enable

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