The AM module also contains fanouts for various information. The AM fans out a copy of the 7-bit vector register source code and a copy of the 6-bit vector register destination code from the JA module to each of the eight VR modules. The AM also fans out a copy of the 6-bit vector length register to each of four control modules. The L register data is fanned out from the AR module to the IB, JB and the two JC modules. All of these fanouts are one clock stopovers on the AM module.

Address Multiply Instruction Summary

022ijk   Enter Ai with integer product Aj * Ak
023ijk   (same as above)