Each background processor sends a 14 bit request package to the QF module in the proper places this for that CPU. The QF module translates this package, and reserves the appropriate memory bank, of the bank is busy the request is recycled every four clock periods until the bank is free. Data organization of the 14 bit request package is as follows:

```
IAA -> 2°
IAB
         DESTINATION TAG
IAC
TAD
IAE
IAF
IAG
                                        REQUEST PACKAGE
IAH
                                         (14 BITS)
IAI
        BANK POINTER
IAJ
     24 -> 24 = 1 AND VALID REQUEST
IAK
         -> 24 = O AND VALIE REQUEST
IAL
                      Short Reservation on Even Addresseres
          PSUEDO BANK POINTER
IAN
                       Long Reservation on Odd Holdresses
                        Reserve,
                              for refresh.
```