

BOOLEAN EQUATIONS

This section contains a summary of the Boolean equations for the CRAY-2 gate arrays and detailed descriptions of each chip.

<u>Gate Array Type</u>	<u>Boolean Equation</u>	<u>Power Dissipation</u>
A	$D, e, G, f = KLMN + HIJ + ABC$	265mw
B	$I, h, F, g, E, d = JK + LM + NA + BC$	342mw
C	$F, g = DE + HI + JK + LM + NA + BC$	357mw
D	$F, g = HIMJLK + NMJLK + AJLK + DLK + EK + BC$	357mw
E	$D, e, G, f, H, i, L, k, M, n, A, b = CJ$	321mw
F	$L = bcdJK$ $M = bcdJK$ $N = bCdJK$ $A = bCDJK$ $F = BcdJK$ $G = BcDJK$ $H = BCdJK$ $I = BCDJK$ $E = j + k$	566mw
H	$G, f = EDCBAN$ $H, i = JKLM$	138mw
I	$I, h = JKL$ $E, d = CBA$ $G, f = MN$	173mw
J	$G, f = DE$ $H, i = DJ$ $N, m = KL$ $A, b = KC$	208mw
K	$H, i = KLJ + MNC$ $F, g = ABJ + DEC$	307mw