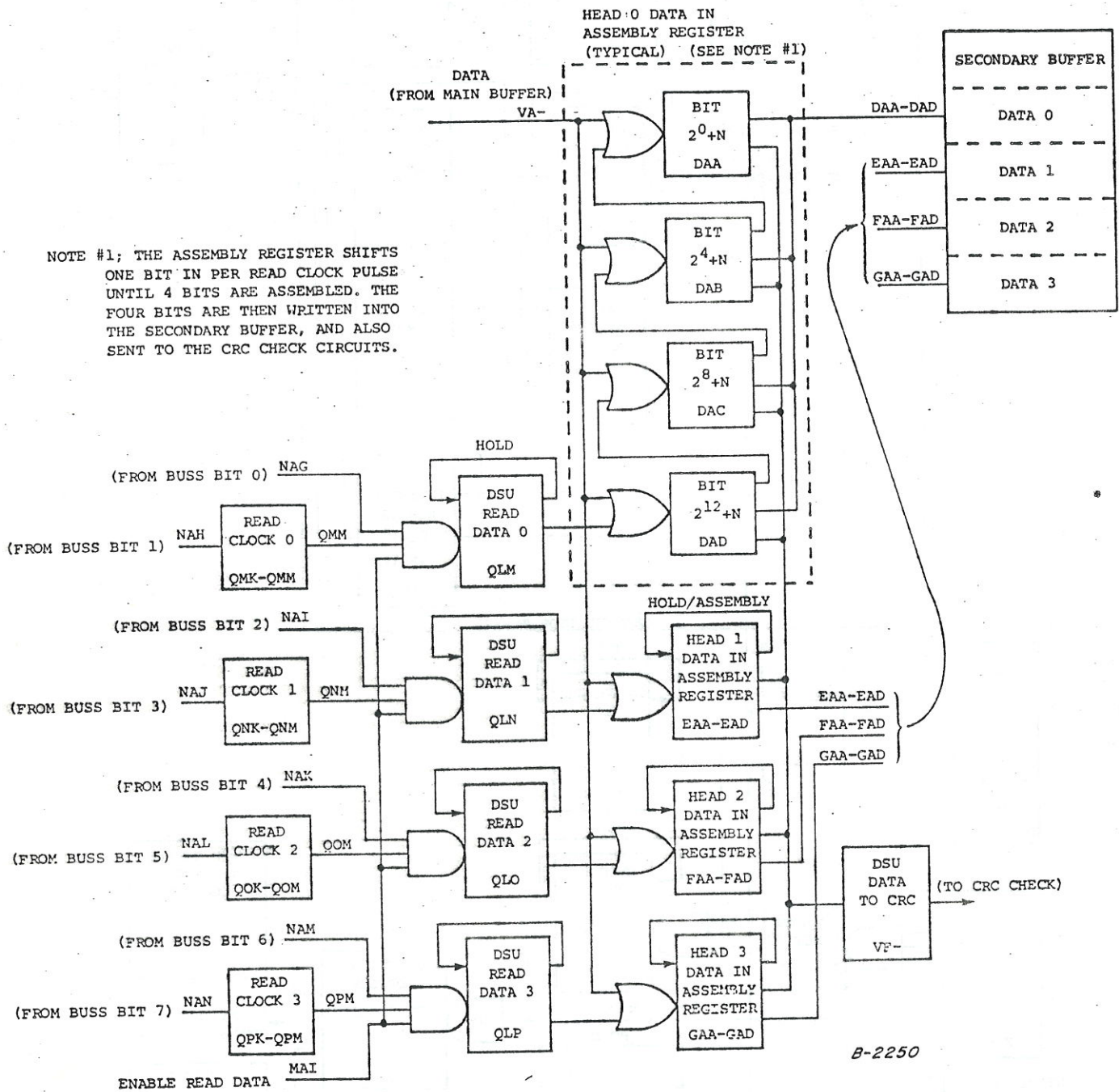


NOTE #1; THE ASSEMBLY REGISTER SHIFTS ONE BIT IN PER READ CLOCK PULSE UNTIL 4 BITS ARE ASSEMBLED. THE FOUR BITS ARE THEN WRITTEN INTO THE SECONDARY BUFFER, AND ALSO SENT TO THE CRC CHECK CIRCUITS.



B-2250

DSU TO SECONDARY BUFFER