

QOB, QOW HOLD UNTIL A FETCH COMPLETE SIGNAL IS RECEIVED FROM THE IA MODULE (INDICATING ALL WORDS INPUTTED INTO THE INSTRUCTION STACK OR UNTIL ABORT FETCH OCCURS).

HOLD QQC, QLP, QLQ UNTIL LENGTH REGISTER EQUALS ZERO OR UNTIL ABORT FETCH IS INDICATED.

HOLD QQJ (ENABLE FETCH LATCH) UNTIL LENGTH REGISTER GOES TO ZERO.

LENGTH ENTRY REGISTER IS LOADED WITH A 20₈
DURING A FETCH SEQUENCE.

ADD THREE CLOCK PERIODS TO OVERALL SEQUENCE
IF A 002 INSTRUCTION (RETURN JUMP). 0025

SEQUENCE WILL BE STOPPED AT CP5 IF THE MEMORY
PORT IS BUSY.

ABORT FETCH (QQI) AND ENABLE FETCH (QQJ) WILL
BE INDICATED AT CP9.

FETCH WILL BE ABORTED IF THE BRANCH CONDITION IS NOT SATISFIED, OR IF IN-STACK BRANCH CONDITION OCCURS. ABORT FETCH WILL INHIBIT VALID REFERENCE SIGNAL TO TA MODULE.

