I/O Memory References

A I/O memory reference will come from the EB module to the IB. The IB will immediately send a signal called common memory busy to the JA module. This stops the JA from issuing any memory reference type instructions. The I/O reference will set the port busy if the port is available and there is no fetch request. A length value will be loaded from the EB module into the IB. The IB module will control the port sequencing. The port will stay busy until the length equals zero. Note: I/O memory reference addressing is always absolute and no limit checks will be performed.

Background Memory Reference

When the JA module issues a 60-67, 70-73 instruction the background processor will get the port. A fetch or I/O reference will prevent the issuing of these instructions.

The common memory port busy flag is not set for the Scalar type memory instruction (60-67), but is set for V.L. time for the Vector type memory instructions (70-73).