III. Stub lines

    A. Lines should be drawn as short as possible and under 36 grids .

    B. No stub lines are to be drawn on a single board level .

    C. Resistor program places terminators on the furthest board from the transmitter with a load which could cause stubs . Design Group should note these improper terminators so artwork can place them accordingly .

IV. Short's and shortj's are padded to 144 grids . These are signals latch to latch either on the same board (short's) or through a jumper (shortj's) . If a latch feeds another latch on the same chip these conditions are padded 72 grids .

V. Shortl's are padded to 72 grids only by the request of the Design Eng. Group . Shortl's are signals feeding themselves . Only false sides of chips feeding the true side without a controling factor need padding . Some examples are shown below ,

    QAA = haa eaa + qaa .   (Needs padding)

    QAA = haa eaa tja + qaa TJA . (Doesn't need padding)

    qaa = haa eaa + QAA .   (Needs padding)

    QAA = haa eaa + QAA .   (Doesn't need padding)