

Common Memory Options

| Storage Module | Bank Timing Module | Storage Chip | Resulting Four Quadrant Memory Size |
|----------------|--------------------|------------------|-------------------------------------|
| SA | QA TA TE | 4k ECL | 4 Million 64-bit words |
| SB | QB | 64k Dynamic MOS | 64 Million 64-bit words |
| SC | QC | 16k Static MOS | 16 Million 64-bit words |
| SD | QC | 64k Static MOS | 64 Million 64-bit words |
| SE | QE TA TE | 256k Dynamic MOS | 256 Million 64-bit words |

SE QF + F 256 K Dynamic (20 nsec) Cycle (23 n sec)

→ Pseudo Banking
Same as in SSD

32 Banks per Quad
4 Quads per Machine

| | | | |
|----|----|--------------|--------------------------|
| SF | QF | 256 K Static | 128 Meg 64-bit |
| SG | QH | | |
| SH | QI | 1 M Dyno | 512 Million 64 bit words |