The memory chips that are used in some of the modules are much larger than the normal gate-array chips which are used most often. Because of this the jumper field where these memory chips are located are used to accomodate the larger chip thus eliminating the jumper field. These chips also have a larger foot print with more chip holes so make sure you take this into account 2 when drawing foil paths to or from memory chips. Q A В P 33 C R GRIDS 0 0 N E M 117 <u>-</u>-114 F 111 L 108 X 0 G K 192 X п 119 H I 193 190 187 1 CRIDS A В ם P 0 958 957 954 951 C 0 □ [ ' D N E M F О L صٍ G K Н J 0 I A 17 В P C 0 D N E M F L G K Н J

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