

PARCEL 0 CHIP LOCATION (BOOLEAN TERM)	PARCEL 1 CHIP LOCATION (BOOLEAN TERM)	PARCEL 2 CHIP LOCATION (BOOLEAN TERM)	PARCEL 3 CHIP LOCATION (BOOLEAN TERM)
248 - A/A (WAA)	232 - G/A (WBA)	216 - M/A (WCA)	20 - S/A (WDA)
249 - A/E (WAB)	233 - G/E (WBB)	217 - M/E (WCB)	21 - S/E (WDB)
250 - A/I (WAC)	234 - G/I (WBC)	218 - M/I (WCC)	22 - S/I (WDC)
251 - A/M (WAD)	235 - G/M (WBD)	219 - M/M (WCD)	23 - S/M (WDD)
252 - A/Q (WAE)	236 - G/Q (WBE)	220 - M/Q (WCE)	24 - S/Q (WDE)
253 - A/U (WAF)	237 - G/U (WBF)	221 - M/U (WCF)	25 - S/U (WDF)
254 - A/1 (WAG)	238 - G/1 (WBG)	222 - M/1 (WCG)	26 - S/1 (WDG)
255 - A/5 (WAH)	239 - G/5 (WBH)	223 - M/5 (WCH)	27 - S/5 (WDH)
256 - D/A (WAI)	240 - J/A (WBI)	224 - P/A (WCI)	28 - V/A (WDI)
257 - D/E (WAJ)	241 - J/E (WBJ)	225 - P/E (WCJ)	29 - V/E (WDJ)
258 - D/I (WAK)	242 - J/I (WBK)	226 - P/I (WCK)	210 - V/I (WDK)
259 - D/M (WAL)	243 - J/M (WBL)	227 - P/M (WCL)	211 - V/M (WDL)
260 - D/Q (WAM)	244 - J/Q (WBM)	228 - P/Q (WCM)	212 - V/Q (WDM)
261 - D/U (WAN)	245 - J/U (WBN)	229 - P/U (WCN)	213 - V/U (WDN)
262 - D/1 (WAO)	246 - J/1 (WBO)	230 - P/1 (WCO)	214 - V/1 (WDO)
263 - D/5 (WAP)	247 - J/5 (WBP)	231 - P/5 (WCP)	215 - V/5 (WDP)

Example: Bit 2<sup>41</sup> is failing chip at J/E, boolean term WBJ is suspect.

DA MODULE MAIN BUFFER 4K x 1 CHIP LOCATOR CHART

C0005S0502