

If in the artwork listings you see only an alpha-numerical jumper with a *** after it hook it directly to a resistor. If the foil paths you are drawing from the jumper to the resistor is excessive you may be putting in too much foil and that will cause you to have problems when drawing other foil paths in at a later time. This rule goes with all lines that you draw to resistors if you keep them short you will have an easier time later on with other foil paths.

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 * FC Module - ABC Board - Artwork Listing
 * Revision A - 02.24.84
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aas ***	.ZZZ
abs	.ZZZ
aby *g*	.ZAE
acs ***	.ZZZ
ads	.ZZZ
ady *g*	.ZAF
aes	.ZZZ
aey *g*	.ZAG
afs ***	.ZZZ
ags	.ZZZ
agy *g*	.ZAH
ahs ***	.ZZZ
ais ***	.ZZZ
ajs	.ZZZ
ajy *g*	.ZAI
aks ***	.ZZZ
als	.ZZZ
aly *g*	.ZAJ
ams	.ZZZ
amy *g*	.ZAK
ans ***	.ZZZ
aos	.ZZZ
aoy *g*	.ZAL
APB cmk ***	.fcg
APC cmm ***	.FCG
APD cma ***	.fch
APE cmd ***	.FCH
aps ***	.ZZZ
ags ***	.ZZZ
ars	.ZZZ
ary *g*	.ZAM
ass ***	.ZZZ
ATH btm ***	.FCI
ATI btk ***	.fci
ats	.ZZZ
aty *g*	.ZAN
aus	.ZZZ
auy *g*	.ZAO
avs ***	.ZZZ
aws	.ZZZ
awy *g*	.ZAP
AXH btd ***	.FCJ
AXI bta ***	.fcj
axs ***	.ZZZ
als ***	.ZZZ
o2s	.ZZZ
o2y *g*	.ZAQ

In your artwork listing you will have several boolean terms (ZZA,ZZB,ZZC,ZZD,ZZE,ZZF). These boolean terms have six hole locations that are all on the same chip. These holes are connected to a special resistor which is designated in the artwork listing as *h*. On a standard board these special resistors are always located in the same position. Unlike the normal resistors these have two solid diamonds and one open diamond below. The foil paths should connect holes B,E and F to the center hole of the resistor and I,K and N should also be hooked to the center of the resistor.

