	QUAD 0			CPU A			CPU B			QUAD 1						
e: •		A	В	С	D	Е	F	G	Н	I	J	K	L	М	N	
	A	37	17	36	16	F4	AR	EA	EA	AR	FA	16	36	17	37	
	В	35	15	34	14	FB	AM	EB	EB	AR	FB	14	34	15	35	
•	С	33	13	32	12	FC	VR	JA	JA	VR	FC	12	32	13	33	
	D	31	11	30	10	GB	VR	JC	JC	VR	GB	10	30	11	31	
	E	27	7	26	6	GA	VR	JC	JC	VR	GA	6	26	7	27	
	F	25	5	24	4	VA	VR	JВ	JВ	VR	VA	4	24	5	25	
	G	23	3	22	2	VB	WA	IA	IA	WA	VB	2	22	3	23	
	н	21	1	20	0	VL	VR	IB	IB	VR	VL	0	20	1	21	
	I		DA	DA	DA	ME	VR	TB	TB	VR	ME	DA	DA			
CH 0	J		DB	KA	DB	RA	VR	TC	TC	VR	RA	DB	DB			CH
	K		DA	КВ	MD	MA	VR	TA	TA	VR	MA <sup>.</sup>	MD	DA			
	L		DA	. KC	MC	MB	QВ	TD	TD	QB	МВ	MC	DA			
	М			DA	MC	MB	QВ	TD	TD	QВ	МВ	MC	DA			
	N			DA	MD	MA	VR.	TA	TA	VR	MA	MD	DA			
сн 3	0			DB	DB	RA	VR	TC	TC	VR	RA	DB	DB			CH :
	P			DA	DA	ME	VR	ТВ	TB	VR	ME	DA	DA			
	Q	1	21	0	20	VL	VR	IB	IB	VR	VL	20	0	21	1	
	R	3	23	2	22	VB	WA	IA	IA	WA	VB	22	2	23	3	
27	s	5	25	4	24	VA	VR	JВ	JВ	VR	VA	24	4	25	5	
*	т	7	27	6	26	GA	VR	JC	JC	VR	GA	26	6	27	7	
	U	11	31	10	30	GB	VR	JC	JC	· VR	GB	30	10	31	11	
	v	13	33	12	32	FC	VR	JA	JA	VR	FC	32	12	33	13	
	W	15	35	14	34	FB	AM	EB	EB	AM	FB	34	14	35	15	
	х	17	37	16	36	FA	AR	EA	EA	AR	FA	36	16	37	17	
		28	QU	IAD 3		CPU_D				CPU C			QUAD 2			

FOUR PROCESSOR BANK NUMBERING WITHIN QUADRANT (SERIAL NUMBER 2001)