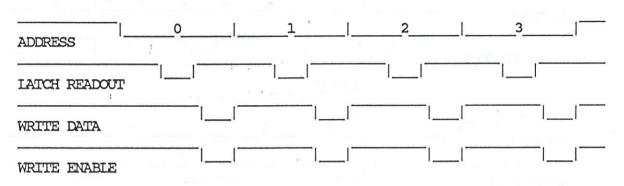
	APPENDIX A	Al
	16 x 4 Usage by Modules (register chips)	
()	DA module (12 per)	



B) IA module (128 per)

ADDRESS

LATCH READOUT

WRITE DATA

WRITE ENABLE