

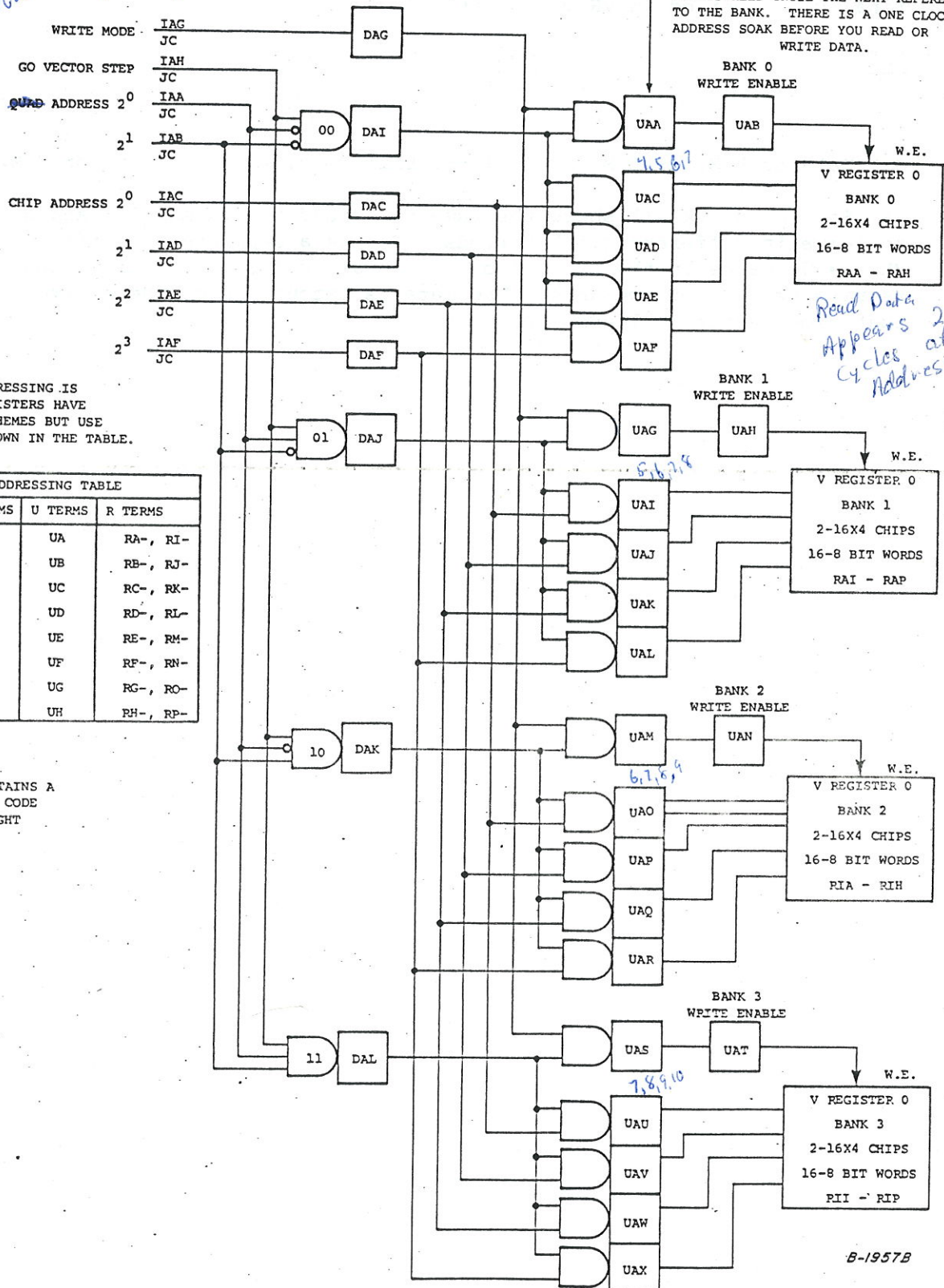
Down for Length of Vector

READ CP 3
WRITE (DEPENDS ON DATA ARRIVAL TIME)

GP 4

THE CHIP ADDRESS IS SET UP AND IS ALWAYS HELD UNTIL THE NEXT REFERENCE TO THE BANK. THERE IS A ONE CLOCK ADDRESS SOAK BEFORE YOU READ OR WRITE DATA.

BANK



VECTOR REGISTER 0 ADDRESSING IS SHOWN. ALL OTHER REGISTERS HAVE SIMILAR ADDRESSING SCHEMES BUT USE DIFFERENT TERMS AS SHOWN IN THE TABLE.

REG	I TERMS	D TERMS	U TERMS	R TERMS
V0	IA	DA	UA	RA-, RI-
V1	IB	DB	UB	RB-, RJ-
V2	IC	DC	UC	RC-, RK-
V3	ID	DD	UD	RD-, RL-
V4	IE	DE	UE	RE-, RM-
V5	IF	DF	UF	RF-, RN-
V6	IG	DG	UG	RG-, RO-
V7	IH	DH	UH	RH-, RP-

THE JC MODULE MAINTAINS A SEPARATE EIGHT BIT CODE FOR EACH OF THE EIGHT VECTOR REGISTERS.

B-1957B