BOOLEAN RECONSTRUCTION

The Boolean reconstruction contains all the Boolean equations for a module. The equations are alpabetized according to the Boolean term that is the output signal. The Boolean reconstruction contains two columns. The first column is the map location and chip type of the output signal of the equation; column two is the equation. In the equations uppercase terms are true outputs and lowercase terms are inverted outputs. Three asterisks (***) mean a forced one, and three dashes (---) mean a forced zero.

Examples