



NOTE #1:

SEE MEMORY REFERENCE DESTINATION TAG CHART FOR DECODE.

NOTE #2:

THE LOWER TWO BITS OF THE DESTINATION TAG, ARE INCREMENTED SEQUENTIALLY BY VALID DATA FOR VECTOR, I/O, AND FETCH REFERENCES.

NOTE #3:

THE DESTINATION TAG IS CLEARED FOR COMMON MEMORY WRITE REFERENCES.

A-2826

IB MODULE MEMORY REFERENCE DESTINATION TAG GENERATION