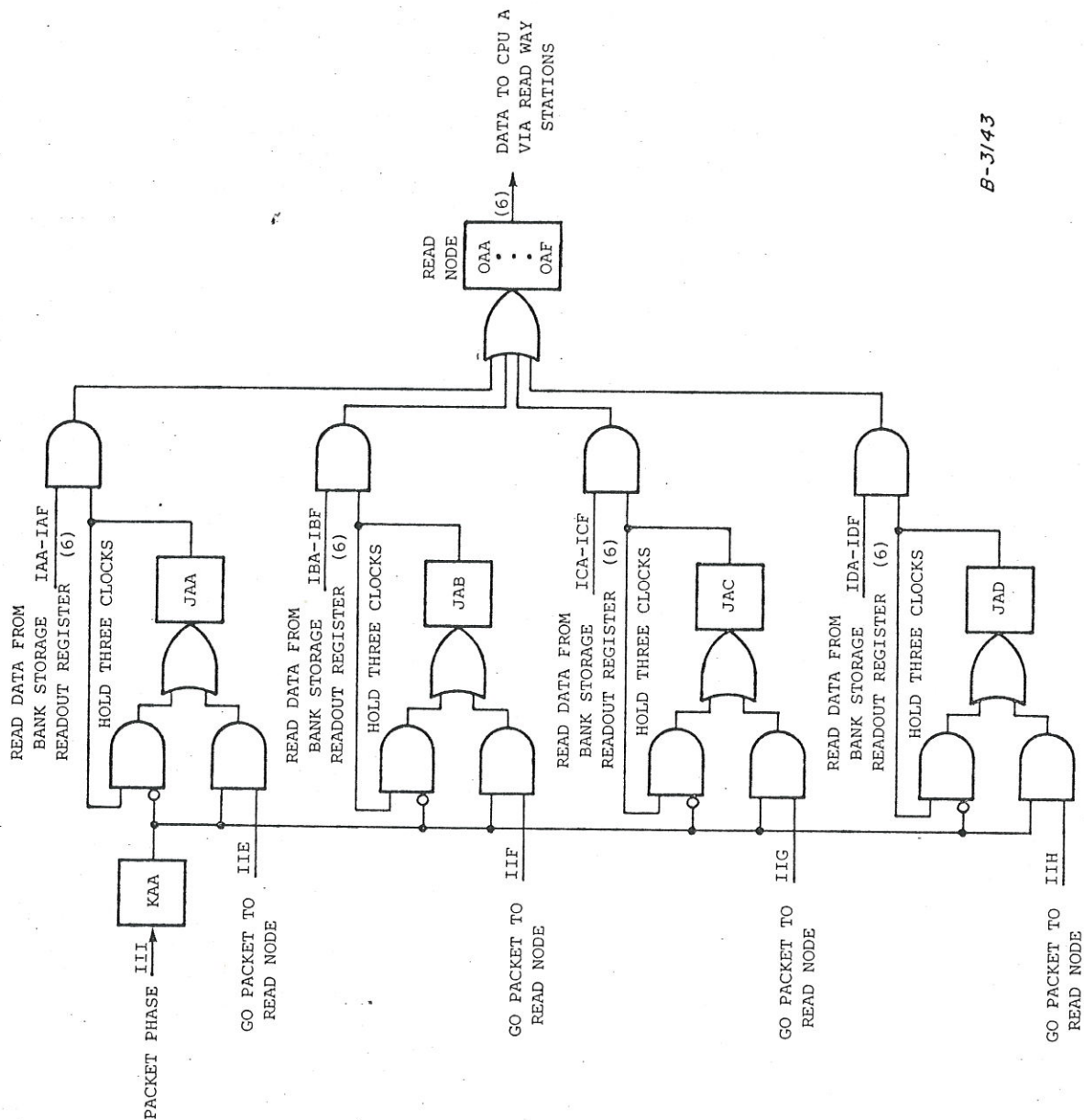


PHASE 3

PHASE 0,1,2,3

PHASE 1,2,3,0



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SE MODULE READ NODE DATA TO CPU A

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