

100-137	SR	data_5	;Shift B right	3
140-177	SL	data_5	;Shift B left	3
200-217	LA4	data_4	;Enter A with 4 bit constant	2
220-237	LA12	data_12	;Enter A with 12 bit constant	4
240-257	LAM	data_12	;Enter A from local memory, immediate address	6
260-277	SAM	data_12	;Store A into local memory, immediate address	6
300-317	LB4	data_4	;Enter B with 4 bit constant	2
320-337	LB12	data_12	;Enter B with 12 bit constant	4
340-357	LBM	data_12	;Enter B from local memory, immediate address	6
360-377	SBM	data_12	;Store B into local memory, immediate address	6

Note: For a left shift use a 2 complement value for the shift count.

0111111

70 → SR
80 → SL
90 → LAY
A0 → LAI2
B0 → LAM
C0 → SAM
D0 → LB + 001
E0 → CB12 -
F0 → BM
G0 → SBR

1006 000
1075 777
1001 1001
10011111
14 1 51

1700 060
17122

0111111
1000000
17122