Cray-2 TE Module Common Memory Address Manipulation

The TE module replaces the TA module in a single processor Cray-2 system. The TE module allows the same Common Memory control modules to be used as are used in a four processor system. The TE module alters the memory address to make a single quadrant of memory work as a four quadrant system. The address is altered in the following manner.

Original Address (lower portion shown)

New chip address that is sent to storage module. The upper two bits of the original bank address become the lower two bits of the chip address.

New bank number that is sent to Q series bank timing module. This allows us to reference sequential columns in memory instead of sequential modules in the column.

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