

## CP1

## CP2

OHA	2 <sup>0</sup> -2 <sup>2</sup>	2 <sup>0</sup> -2 <sup>2</sup> of I design. (for instr.- 57, 61, 3, 5, 7, 71, 3, 5, 110-3)
OHB	of	<u>or</u>
OHC	J Design.	2 <sup>0</sup> -2 <sup>2</sup> of K design. (for instr.- 30-4, 45-7, 55, 6, 60, 2, 4, 6, 70, 2, 4, 100-3, 114, 5, 120-7, 132, 3, 140-77)
OHD	VR Function	2 <sup>0</sup> -2 <sup>2</sup> of I design. (for instr.- 30-4, 45-7, 55-75, 120-7, 132, 140-77)
OHE	Code	<u>or</u>
OHF	See Below	Scalar Code (for instr. - 100-3) (See below)
OHG	Go Vector Wr. See Below	N/U

VR Function CodeScalar Code

0 - No Function	0 - N/U
1 - Vector Integer Unit (150-3, 160-5, 176, 7)	1 - 100 instr.
2 - Local Memory (45-7, 55-7, 74, 5)	2 - 101 instr.
3 - Common Memory (60-73)	3 - N/U
4 - Floating Add (120-3, 170-5)	4 - N/U
5 - Floating Multiply (124-7, 132, 3, 154-7, 166, 7)	5 - N/U
6 - Vector Logical (30-4, 114, 5, 140-7)	6 - 102 instr.
7 - Scalar Logical (100-3)	7 - 103 instr.

Go Vector Write

Instructions - 70, 2, 4, 140-175

## 10) S Register Entry Code

The S register (VR modules) can be entered by seven different paths. The S register entry code is supplied to the VR modules via the AM module. Boolean terms are listed below.

OFA - OFC = S register designator  
OFD - OFF = S register source path code

Code	Path	Instructions
0	No scalar entry	
1	Constant	50-3, 116, 7, 130, 1
2	Local Memory	54, 6
3	Common Memory	60, 2, 4, 6
4	Floating Add	120-3
5	Floating Multiply	124-7, 132, 3
6	Vector Logical	114, 5
7	Scalar Logical/Integer	100-13