VECTOR LENGTH REGISTER (L)

The vector length register is a six bit special purpose register which
is explicitly referenced in the background processor instructions. The
vector length register is used to hold the vector segment length during
a portion of the background computation. All vector streaming
operations capture the segment length at the time of instruction issue
from the L register.

The L register content is interpreted modulo 64. The allowed values
are one through 64. A zero value is therefore interpreted as a 64.
The instructions which communicate explicitly with the L register are
the following.

025i--   Enter Ai from L
036--k   Enter L from Ak

C0001C0205A