

XXI. PACKAGE DELAYS

PACKAGE	DELAY	
A	1.0 ns	
B	1.0	
C	1.0	
D	1.0	
E	1.0	
F	1.0	
G	1.0	
H	0.5	
I	0.5	
J	0.5	
K	1.0	
L	1.0	
M	1.0	
N	1.0	
O	1.0	
P	1.0	D pin is an extra 0.5 ns.
Q	1.0	A , B , C , L , M , and N pins
R	0.5	J and K pins
S	1.0	
T	1.0	
U	1.0	
V	1.5	A , D , K , and M pins
W	0.5	B , C , E , J , L , and M pins
X	1.0	
Y	1.0	
TYPE 8	1.5	

XXII. PATH TIMES BETWEEN MODULES

- A) Wire delay is 120 ps. per inch
- B) Connector delay is 120 ps per pair ( female/male )

