

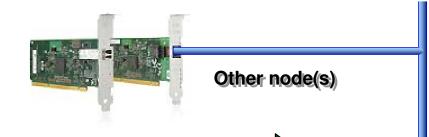
AT90CAN32/64/128



## Demo. program

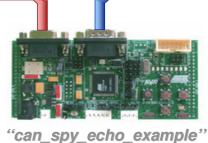
### Spy:

All frames of the CAN bus (echo frames included) are sent on the RS232 serial links of the DVK90CAB1 board (UART 0 & UART 1). «HyperTerminal» Windows can display them.









**DVK90CAN1** 

"Echo"

**Terminal** 

### Echo:

Each frame of the CAN bus (echo frames excepted) is re-sent with the following protocol:

- ID<sub>echo</sub> = ID<sub>received</sub> +1 8-byte data frame
- 2 last bytes = CAN-STAMP register





## Demo. environment

### ■ IDE:

AVR Studio 4.13.528 (or higher)
AVRGCC plug-in
CAN plug-in

**■** C Compiler:

WinAVR-20070122

**■** Default target:

**DVK90CAN1 Atmel development board** 

- **■** Configuration:
  - Software:

Described in «config.h» in main root

Hardware:

Described in «...\libraries\lib\_board\dvk90can\_board.h» (called by «config.h»)

- Default setting:
  - Microcontroller: AT90CAN128 at 8 MHz (external crystal no internal RC)
  - Both **UART\_0** & **\_1** available baudrate: **38400** bds (8-bit, no parity, 1 stop-bit)
  - CAN bitrate: automatic detection (both CAN 2.0A & 2.0B)
  - Both Spy & Echo modes available



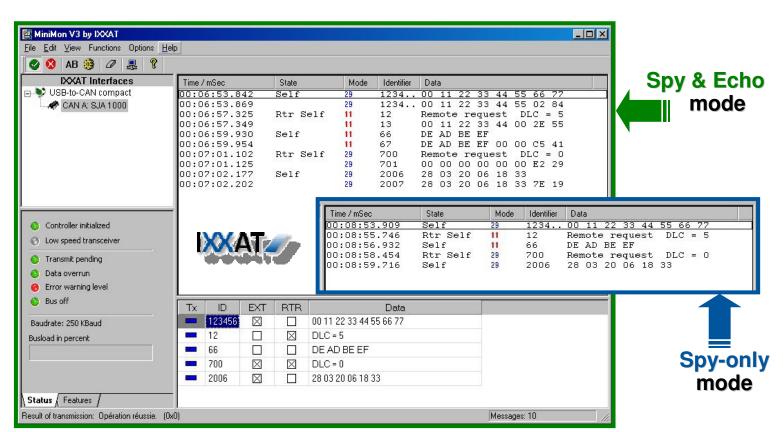








- ☐ If no key is pushed during RESET → Spy & Echo mode
- ☐ If some key is pushed during RESET → Spy-only mode
- ☐ CAN ID = 0 simulates a CAN Error







# "Spy" screen shot example

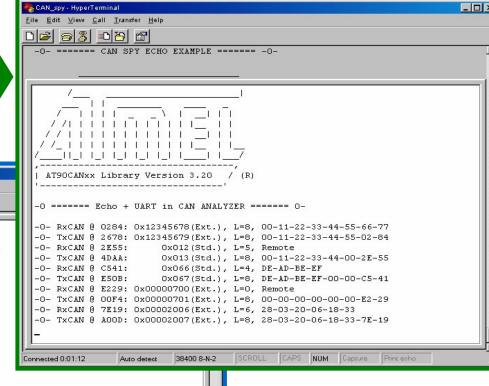


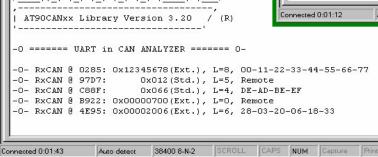
CAN\_spy - HyperTerminal

File Edit View Call Transfer Help 

Spy & Echo mode

-O- ====== CAN SPY ECHO EXAMPLE ====== -O-











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