

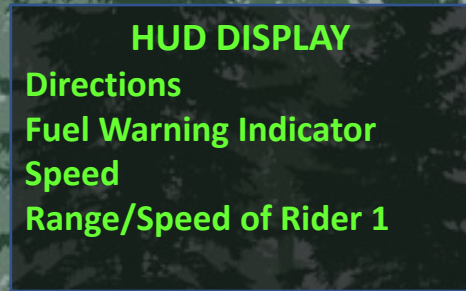


V2X Motorcycle HUD Weekly Updates

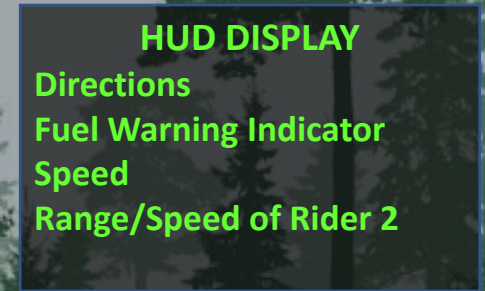
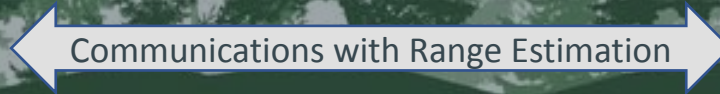
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Ryan Hiser, Jacob Nguyen, Jorge Pacheco
UCSD MAS WES
1 April 2022

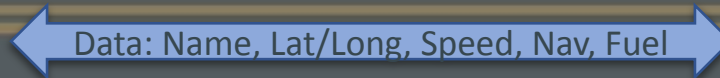
V2X Motorcycle HUD



Rider 2



Rider 1



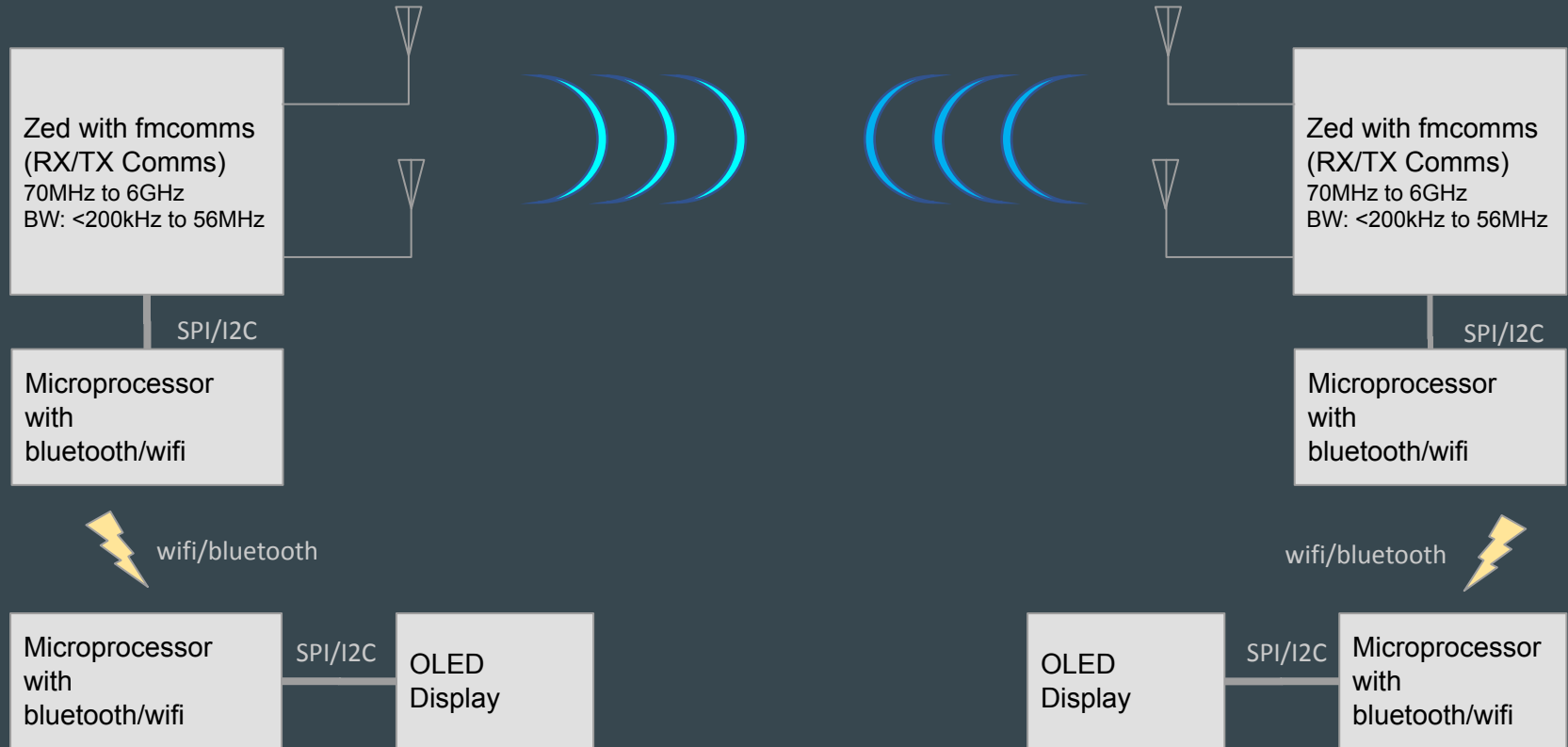


Motivation

- Motorcyclists often ride in groups. To effectively and safely ride it is often necessary to communicate. This takes many forms:
 - Hand gestures (hands off of handlebars)
 - High speed maneuvers
 - Intercoms (difficult with multiple riders)
 - Refuel periods (gas tanks size/consumption speed vary)
 - Modern cruise control is handled as a throttle lock
 - Riders at rear cannot easily keep consistent following distance
- A HUD system would allow for fewer distractions from the driving experience by displaying this information in a digestible format:
 - Driving Directions
 - Speed
 - Simple Messages
 - Others need to refuel
 - Following distance



Apparatus / System Diagram





Previous Quarter Accomplishments (Current Status)

- Waveform Description defined
- Simulink Model of Waveform (compatible with code generation)
- OLED and wireless link using arduino
- FMComms4 + Zedboard integration with PYNQ



Milestones (this quarter)

- TX Modulator
- RX Demodulator
- Complete Heads-up-display (HUD)
- TDMA Channel
- Two-way link
- Comms system integrated with HUD



Deliverables

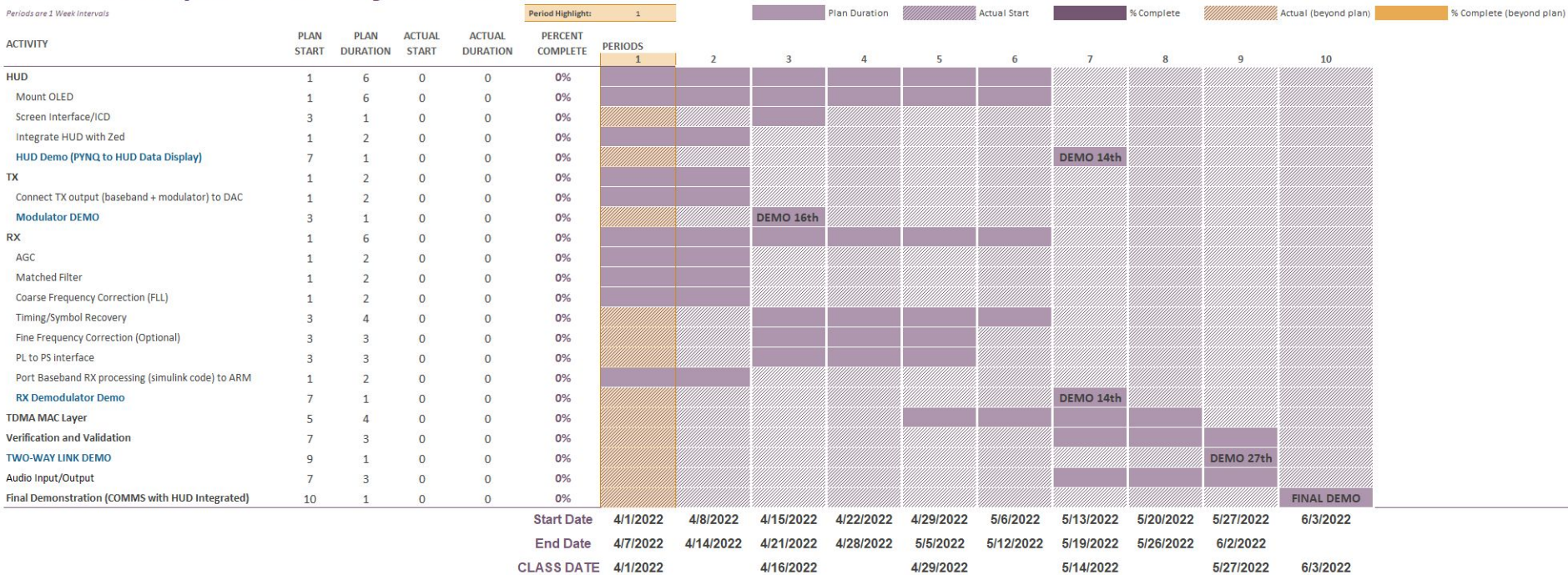
- Git Repo: <https://github.com/jtn017/capstone>
- Final demonstration/video
 - HUD Demonstration
 - Data-link demonstration
- Project Report
 - Progress
 - Problems we faced
 - Solutions to problems
 - Future work



Gantt (timeline)

V2X Motorcycle HUD Project Plan

Periods are 1 Week Intervals





Next Sprint

- TX
 - Generate data and send from PS to DAC
- RX
 - Modulator
 - Implement AGC (digital and/or fmcomms method)
 - Implement Matched Filter
 - Implement Coarse Frequency Correction
 - Test PL to PS using dummy data
 - Baseband
 - Create test vectors (IO)
 - Setup Simulink model with actual data
 - Process RX Baseband data and display on HUD
- HUD
 - Integrate HUD with PYNQ
 - Start prototype mounts

ACTIVITY	PERIODS	
	1	2
HUD		
Mount OLED		
Screen Interface/ICD		
Integrate HUD with Zed		
HUD Demo (PYNQ to HUD Data Display)		
TX		
Connect TX output (baseband + modulator) to DAC		
Modulator DEMO		
RX		
AGC		
Matched Filter		
Coarse Frequency Correction (FLL)		
Timing/Symbol Recovery		
Fine Frequency Correction (Optional)		
PL to PS interface		
Port Baseband RX processing (simulink code) to ARM		
RX Demodulator Demo		
TDMA MAC Layer		
Verification and Validation		
TWO-WAY LINK DEMO		
Audio Input/Output		
Final Demonstration (COMMS with HUD Integrated)		
	4/1/2022	4/8/2022
	4/7/2022	4/14/2022

Additional Slides

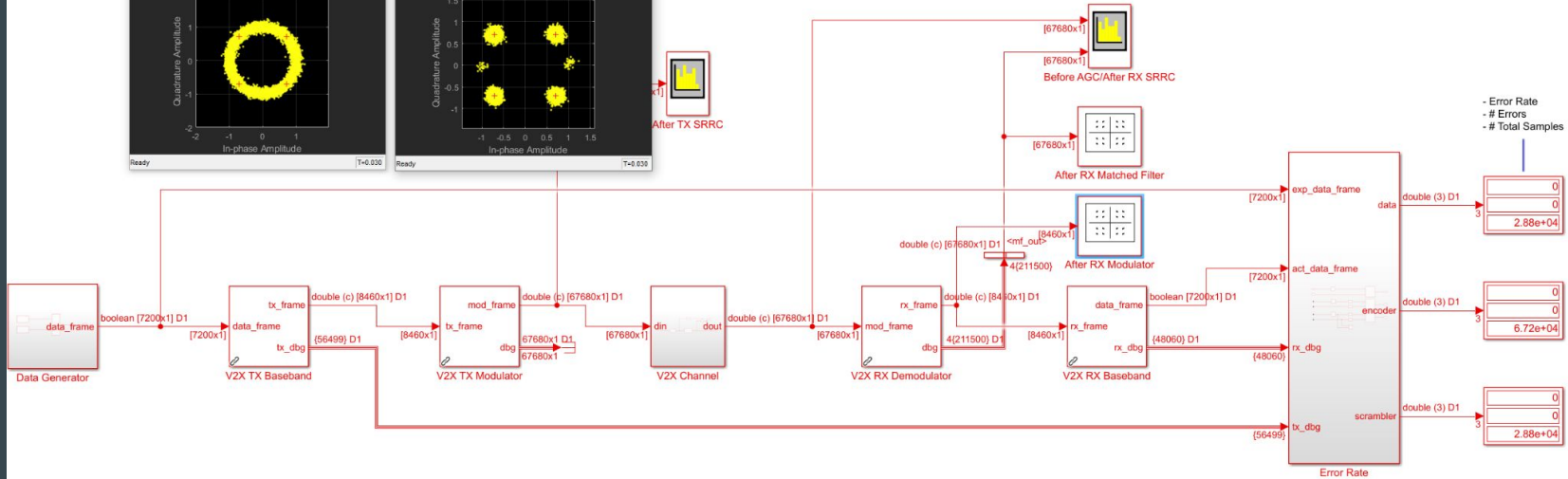


Current state of affairs

Waveform Description

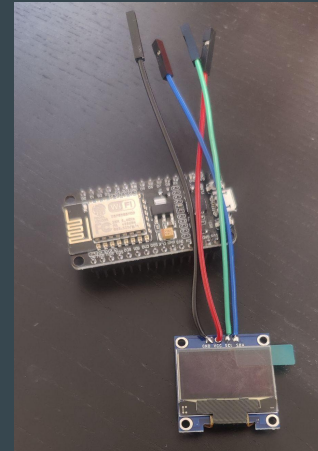
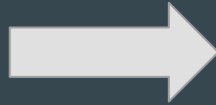
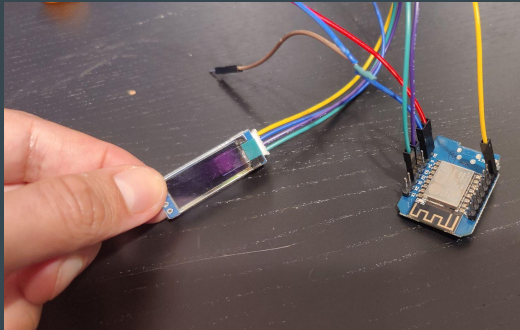
- Datalength: 7200 bits (Data + 0.01s audio).
 - Throughput: $7200 \text{ bits} * (1/0.01\text{s}) = 720 \text{ kbps}$
- Minimum Required Sampling Frequency (before carrier mixer)
 - After TX processing/modulation: 67200 sym
 - $\text{Min } F_s = 67200 \text{ sym} * (1/0.01\text{s}) * 2 = 13.44 \text{ Msps}$
- Max Doppler Frequency:
 - $F_c = 2.4 \text{ GHz}$ (WiFi)
 - Max speed (x2): 200 MPH = 89.41m/s
 - $F_d = 2,400,715 \text{ Hz}$ (offset = 715 Hz)
- $\text{Max } f_e < 1/2T$
 - $1/2T = f_s/2L$
 - $f_s/2L = 100 \text{ MHz}/(2*64) = 781.25 \text{ KHz}$

RX Demod



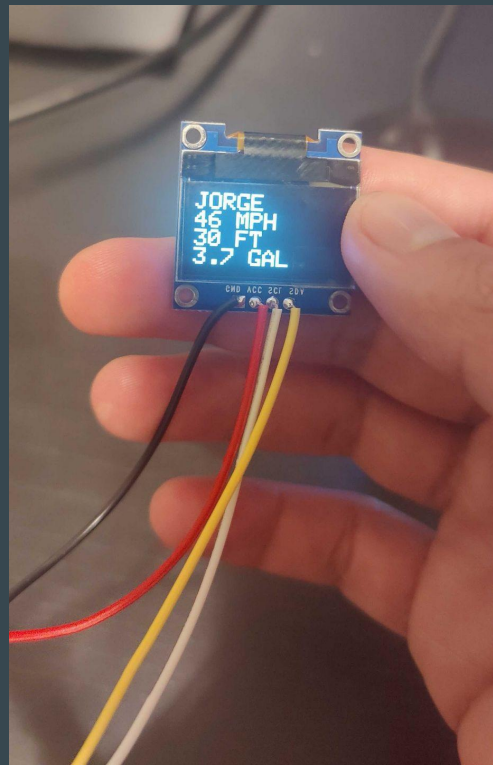
HUD Progress

- Encountered issues with first batch of components
 - Assembled all parts and soldered components
 - Created test programs on Arduino IDE to configure ESP8266 (WiFi) and SSD1306 (OLED)
 - Couldn't control OLED with SPI or I2C
 - Ordered a new set of parts with same controllers



HUD Progress (cont)

- Configured OLED to receive subset of data we plan to transmit
 - Created test programs to fiddle with WiFi server functionality
 - Connected WiFi module to local network and was able to toggle individual GPIO pins through a client connected to the network
- Next steps
 - Create a server to listen to our incoming traffic based on our requirements
 - Display text in real time on OLED
 - Work on enclosure



FMComms+Zed Board Integration

- Working SD Card PYNQ image
 - ADI provided base HDL project for FPGA.
 - Used Petalinux to generate boot files with desired FMComms4 device tree.
 - Integrated Petalinux and PYNQ Root Filesystem
 - Installed LibIIO library
 - Created a Transmit and Receive Demonstration.
 - Sends to Tx DMA → DAC
 - Reads from Tx DMA ← ADC and stores to file.
 - On our github.

```
In [25]: import numpy as np
import matplotlib.pyplot as plt
import os

In [38]: fid = open("./iio_stream/build/fmcomms_data.dat", "rb")
data = np.fromfile(fid, dtype='int16')

In [39]: inphase = data[1:40000:2]
quadrature = data[0:40000:2]

In [40]: plt.plot(inphase)
plt.plot(quadrature)
plt.show()
```

