

PCM-058
phyCORE-i.MX 6

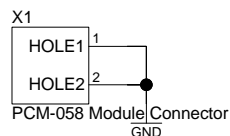
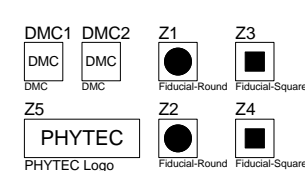
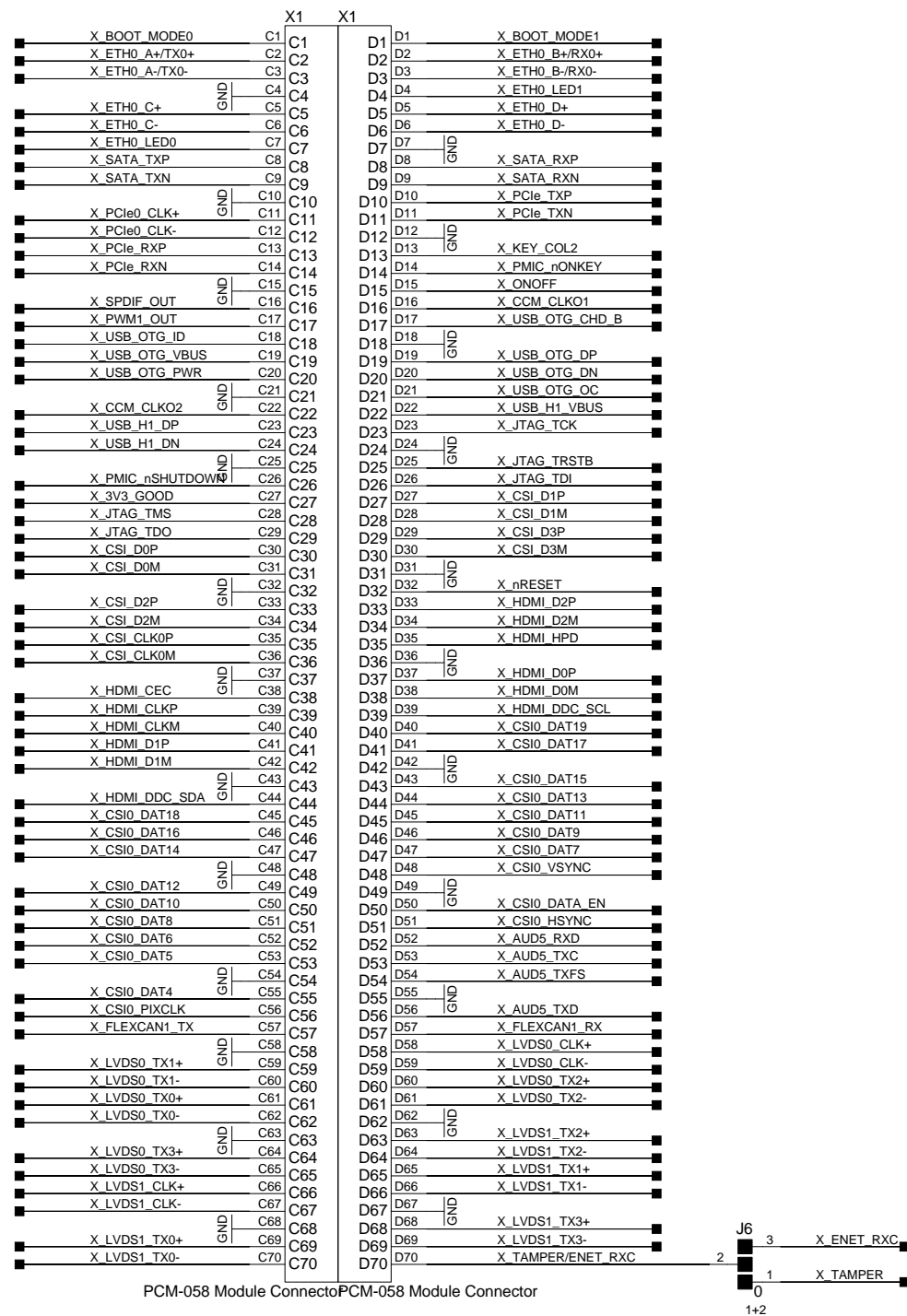
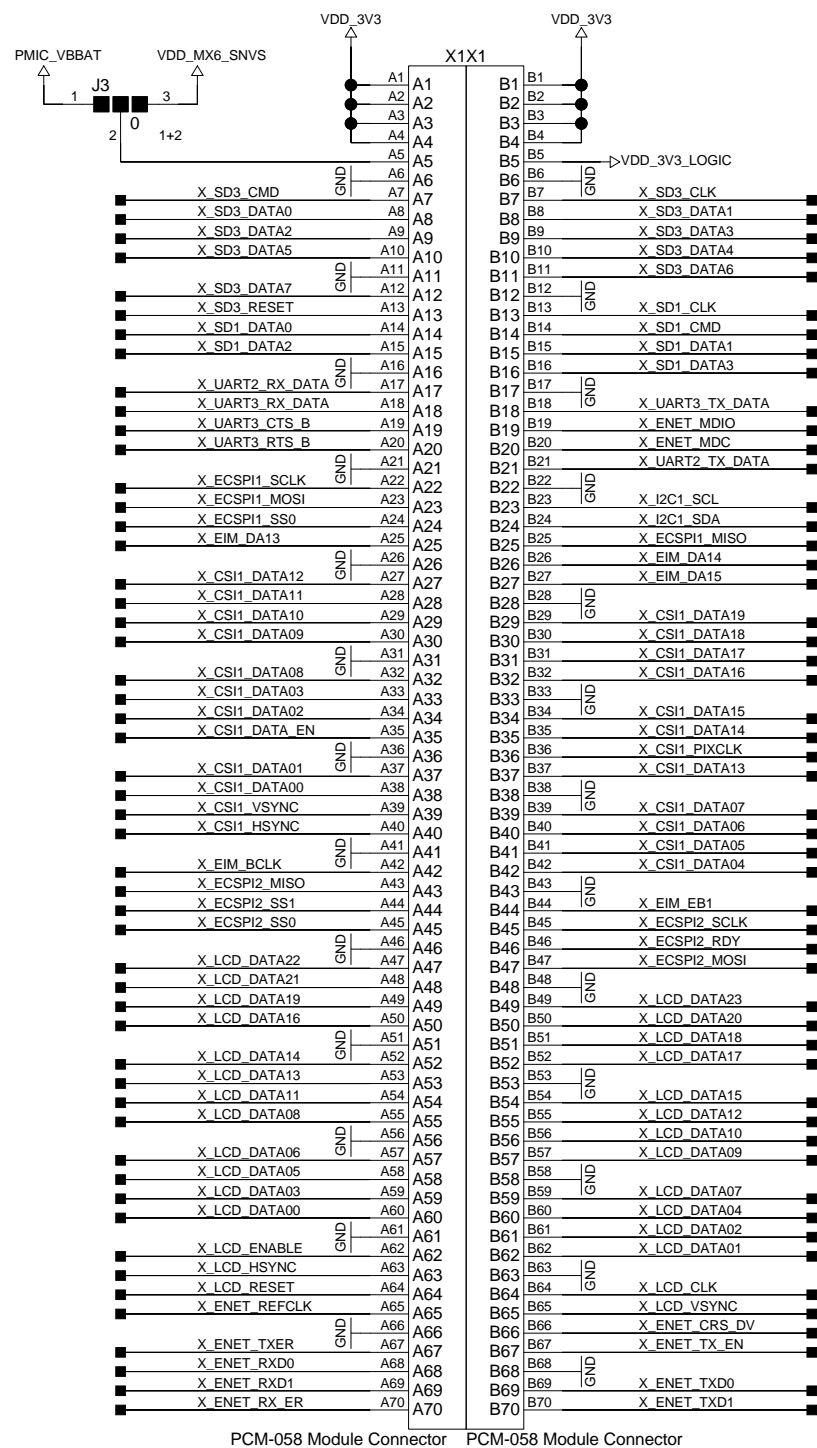
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Project: PCM-058 phyCORE-i.MX 6		Department : F&E Author: M.Dzierzawa	
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phyCORE Connector

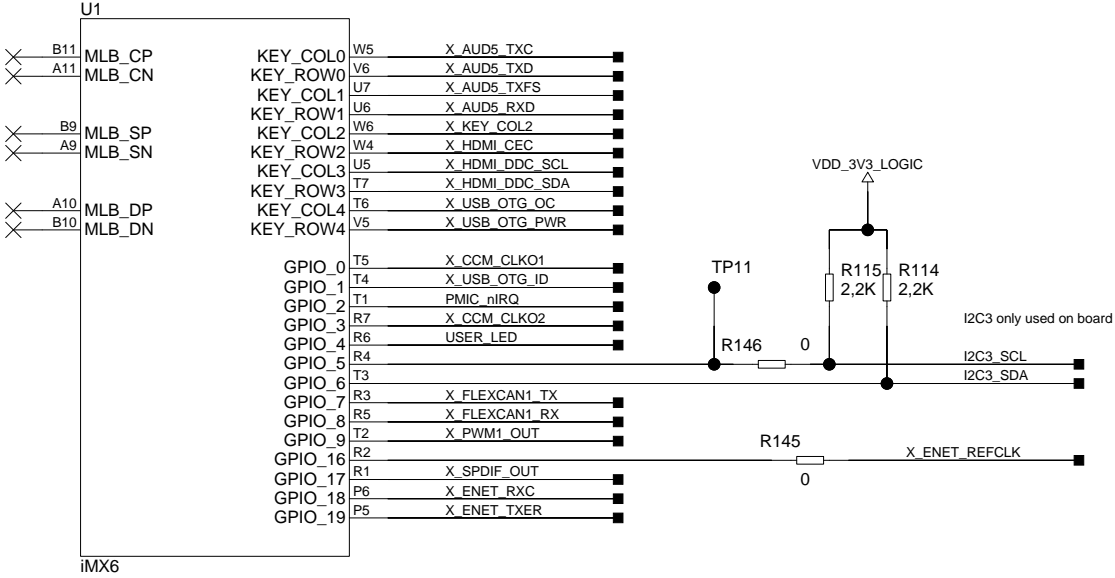
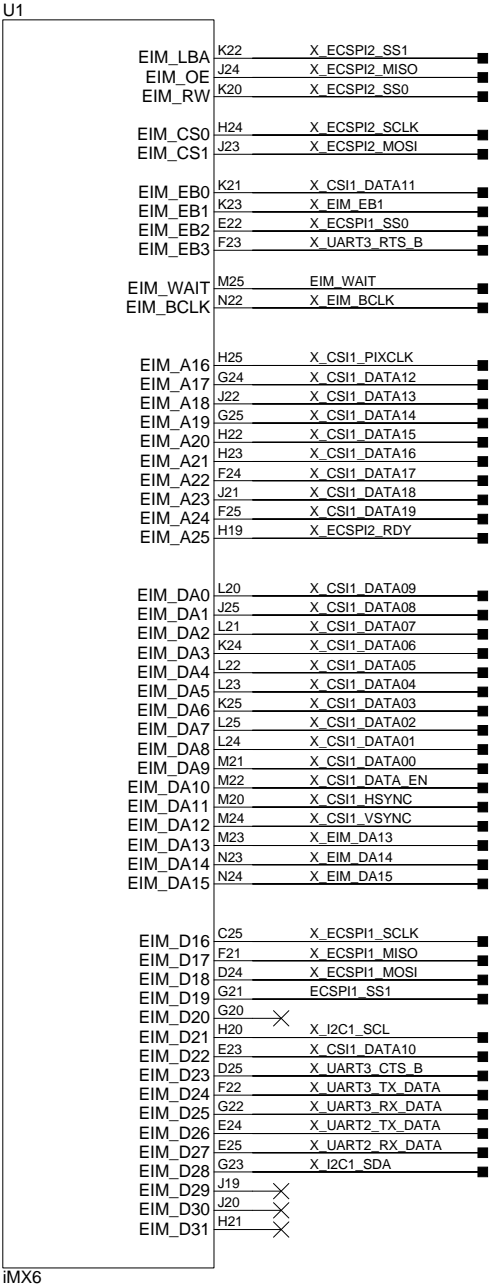
CAUTION:
UART CTS is an output
UART RTS is an input
Normally the direction are swapped



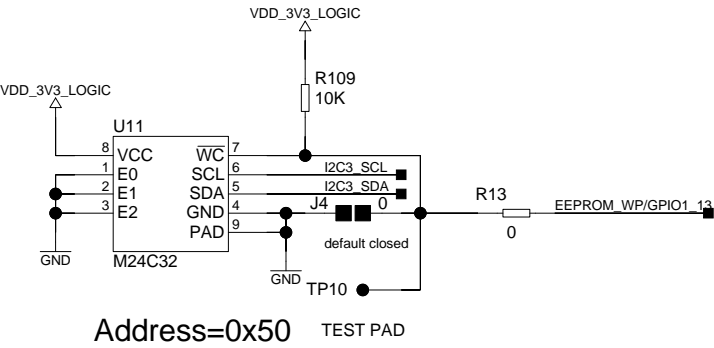
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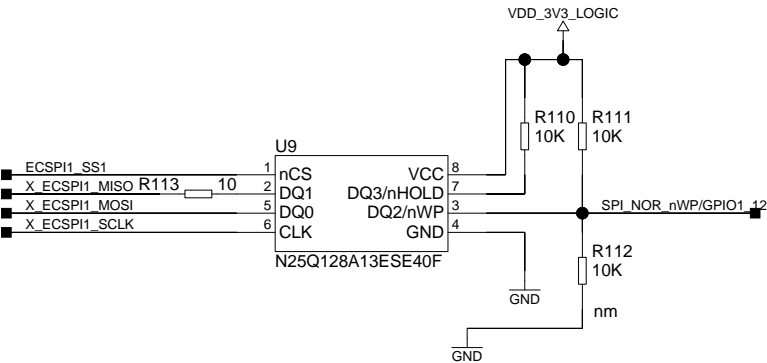
SPI, I2C, UART, CAN, I2S, EEPROM, SPI-Flash



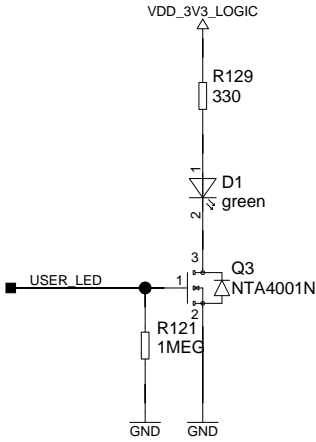
On board I2C EEPROM



On board SPI Flash



User LED



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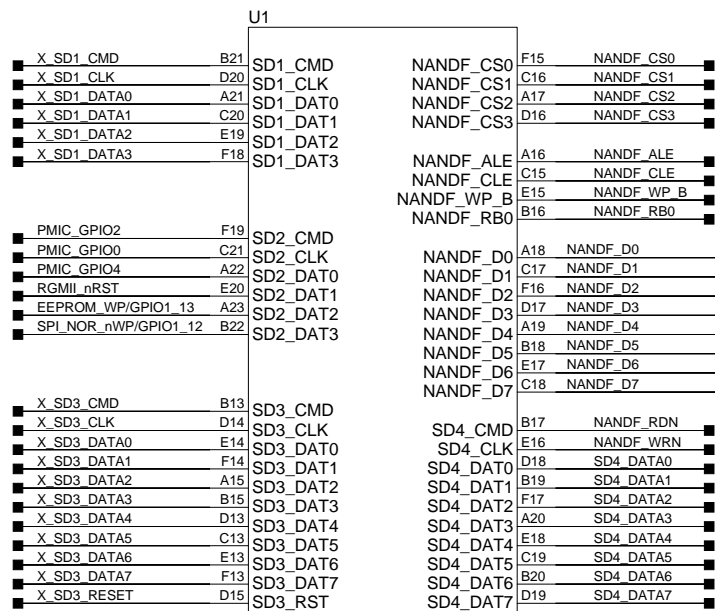
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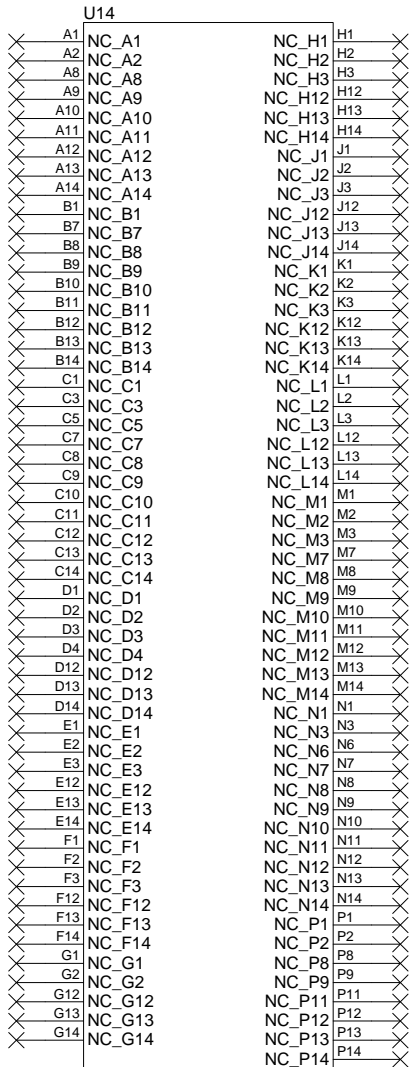
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SDIO, NAND-Flash, eMMC

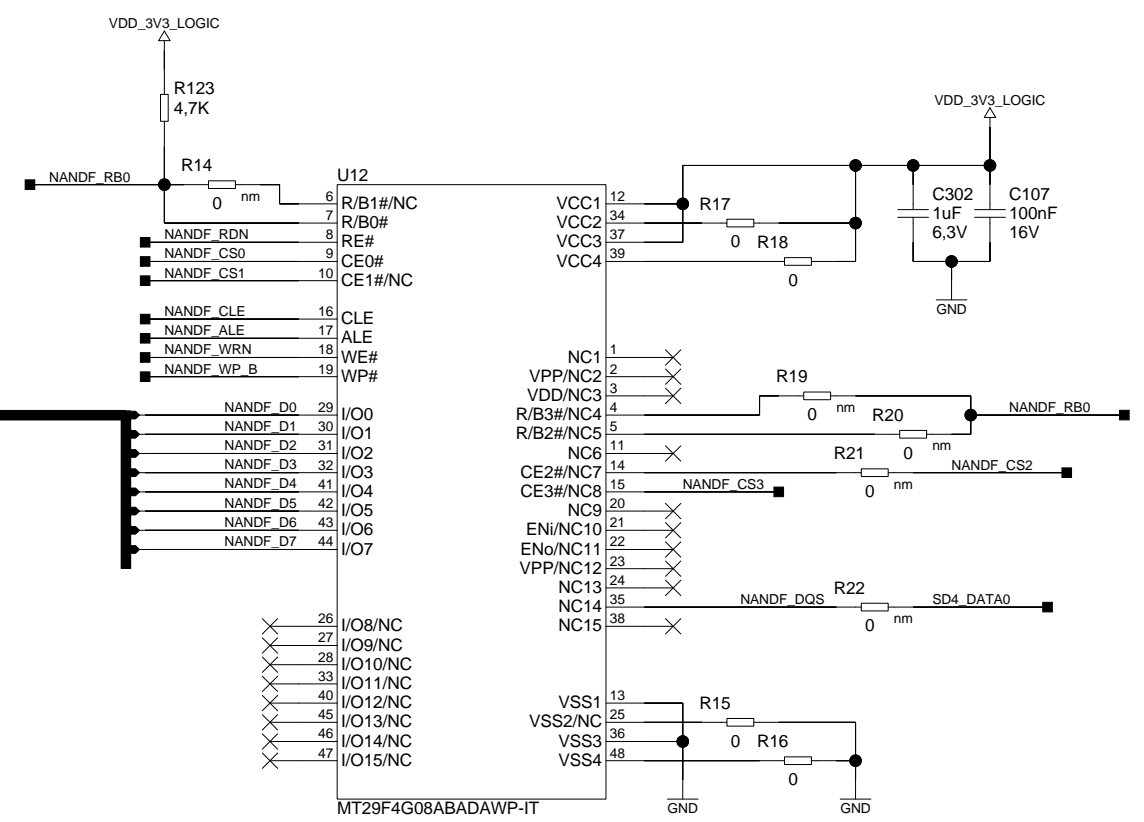


iMX6

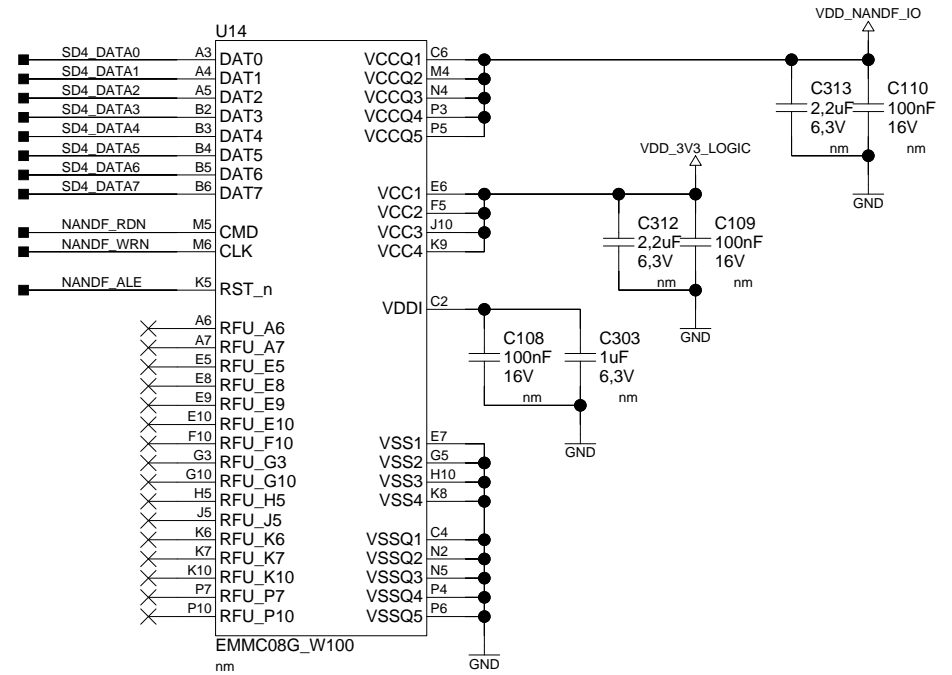


EMMC08G_W100

nm



Either NAND or eMMC can be populated



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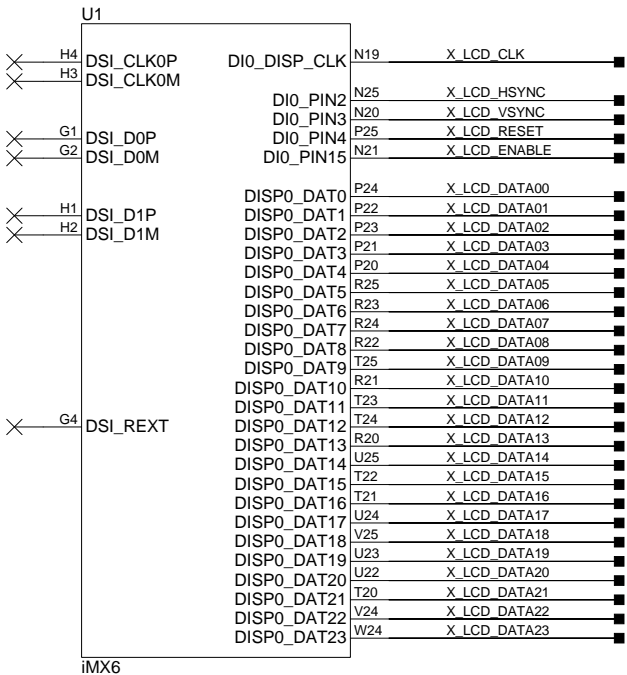
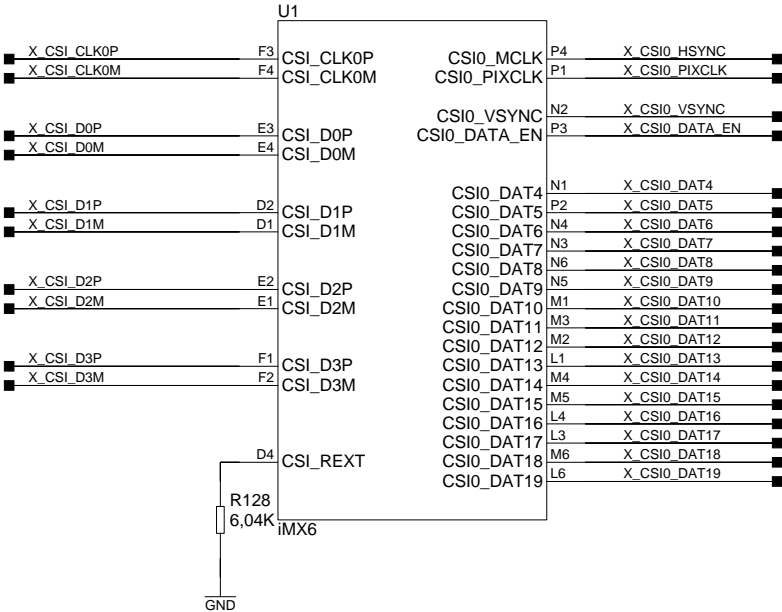
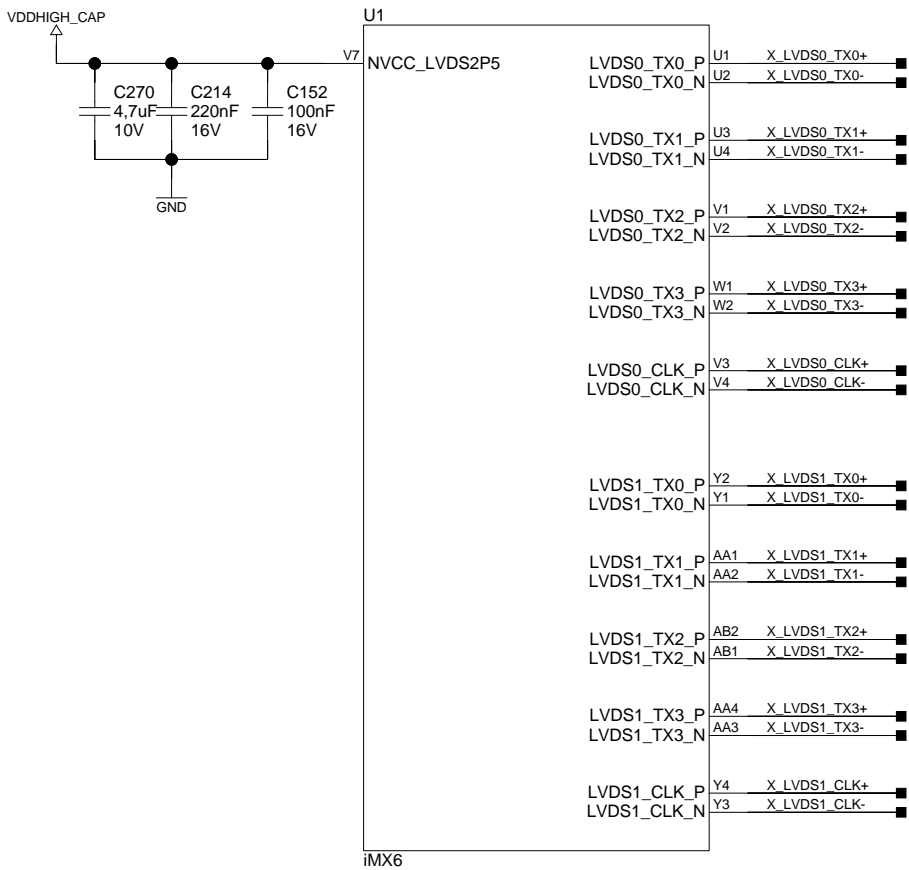
A



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Display and Camera

Camera_2, CSI-2 (MIPI)



A

B

C

D

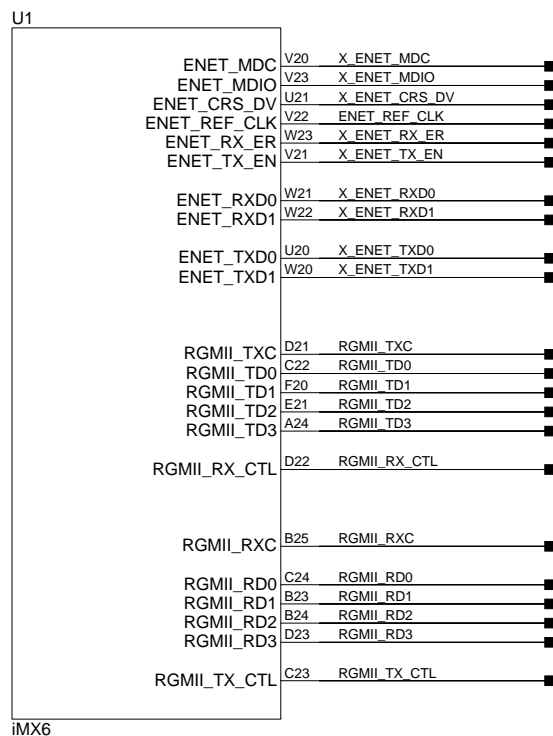
A

B

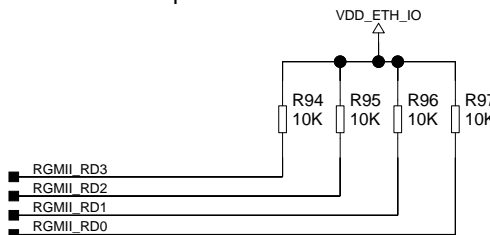
C

D

10/100/1000 Ethernet

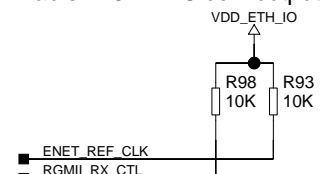


advertise all capabilities

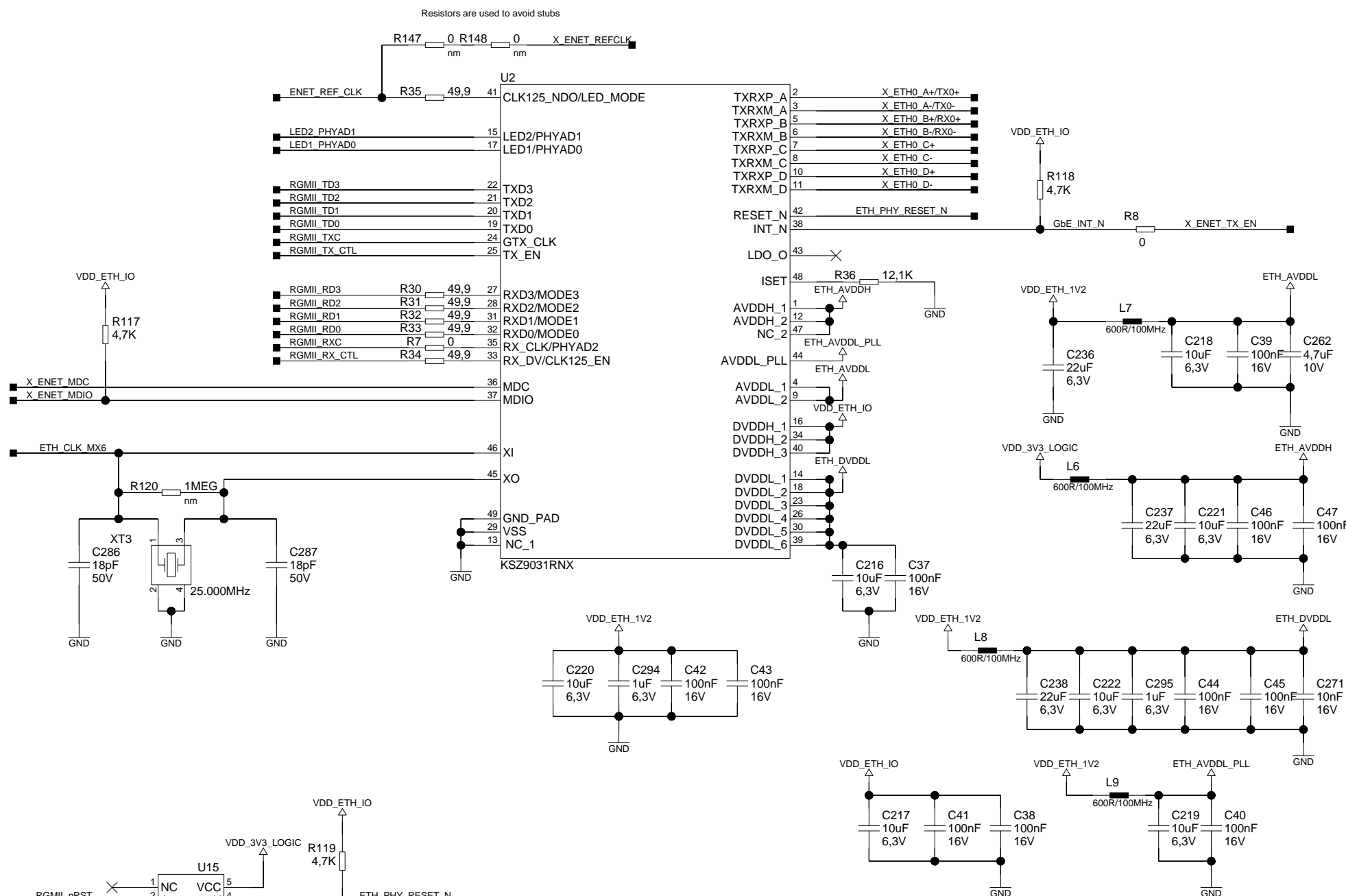
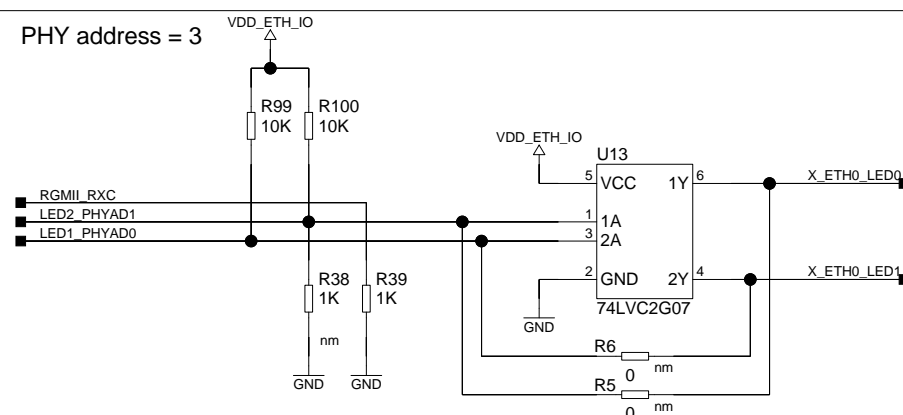


Single LED Mode

Enable 125MHz Clock output



PHY address = 3



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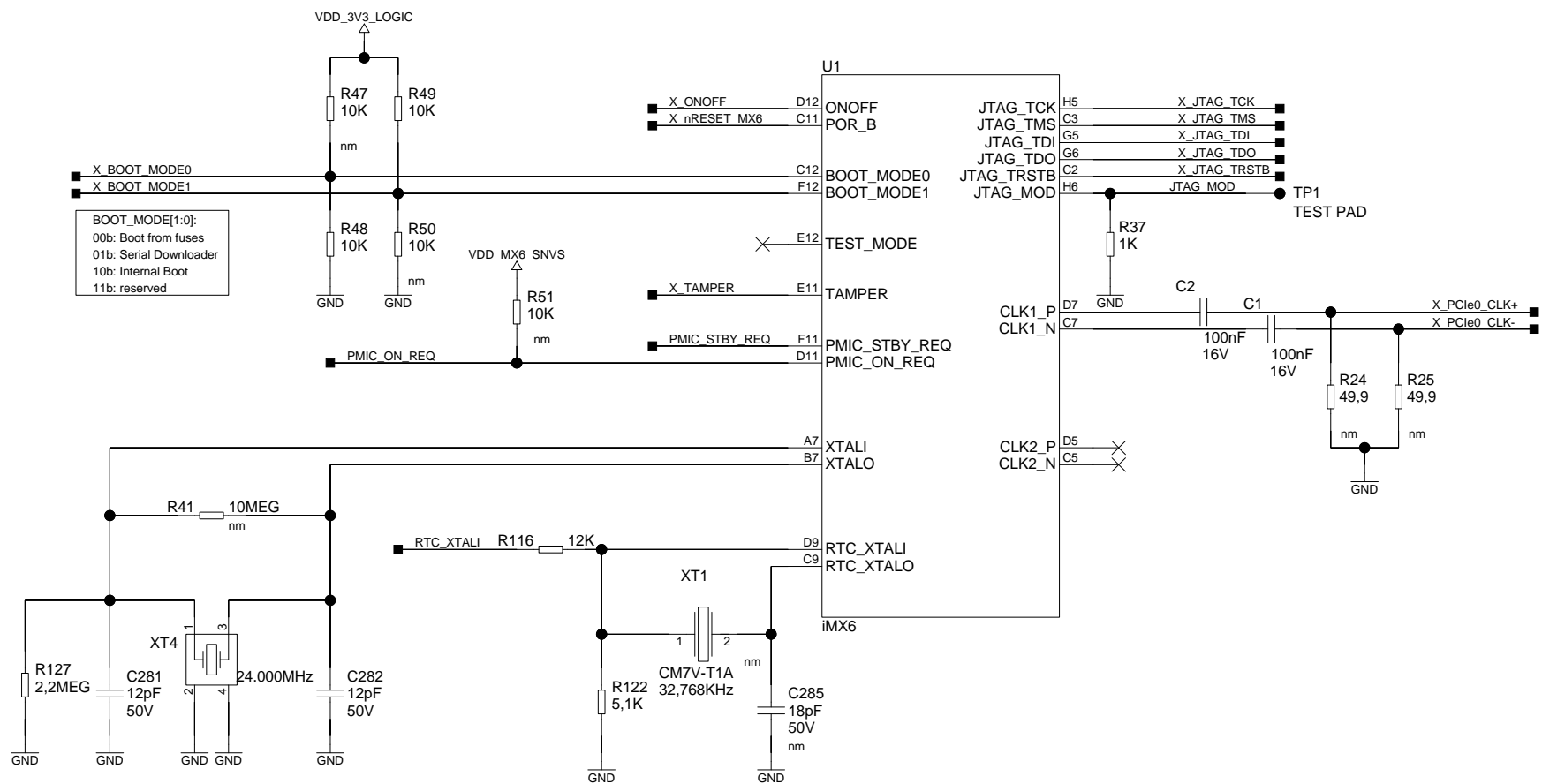
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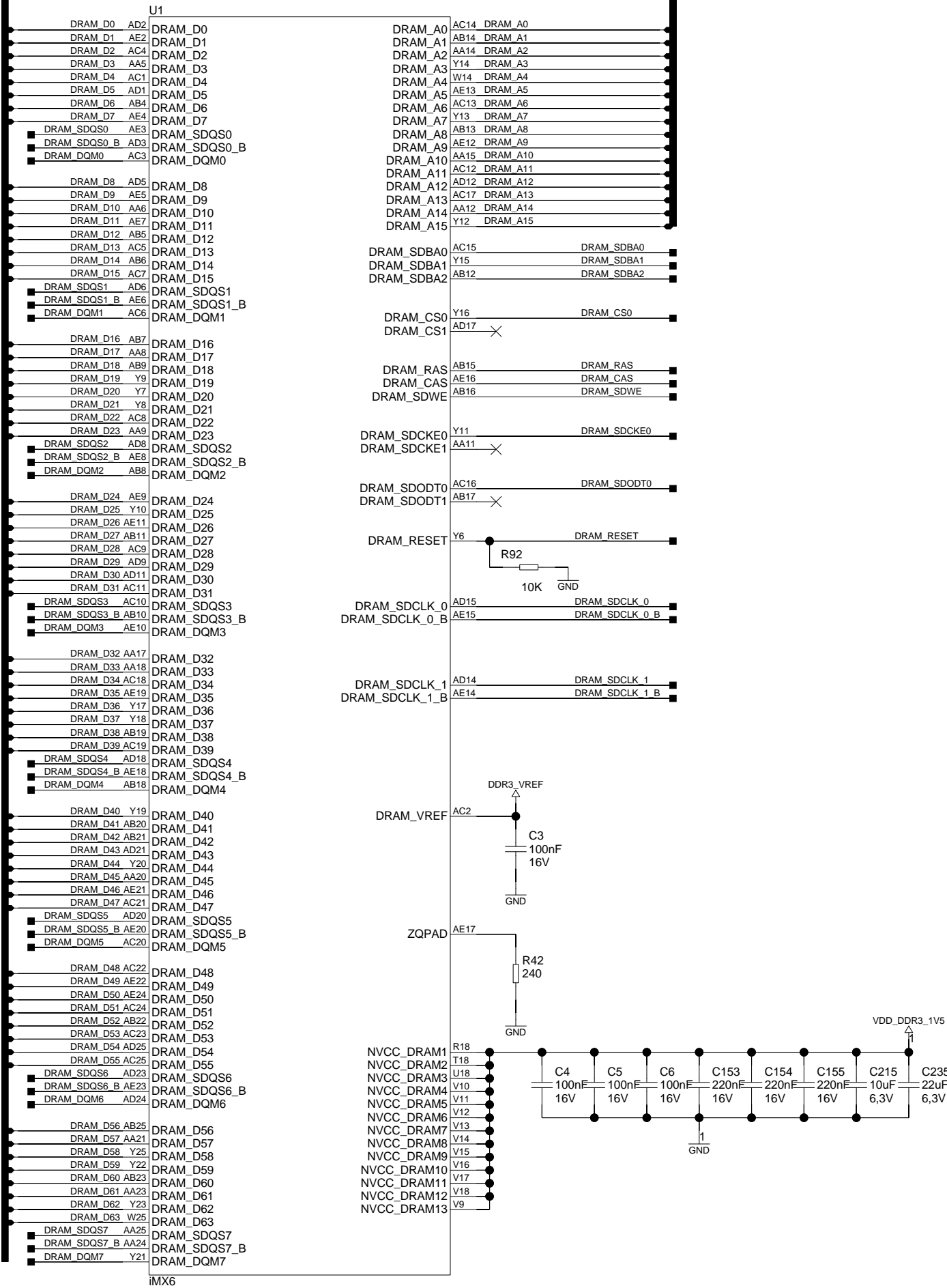
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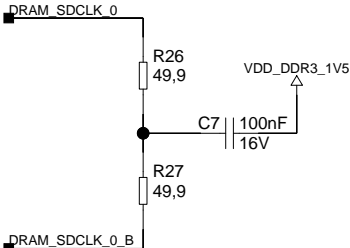
i.MX6 Control



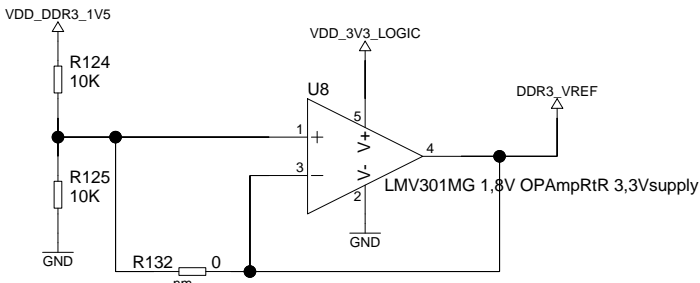
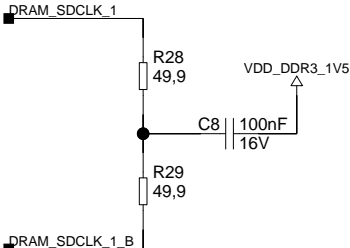
DDR3



Place at the end of the route



Place at the end of the route

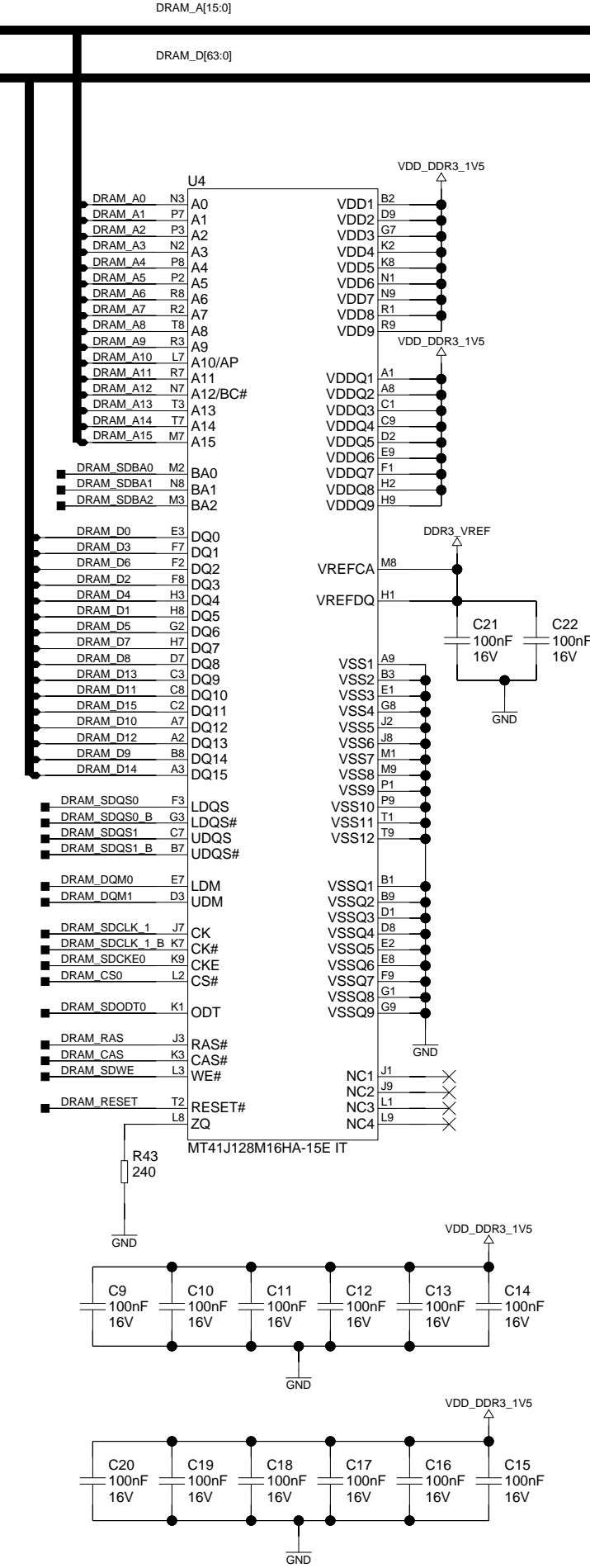


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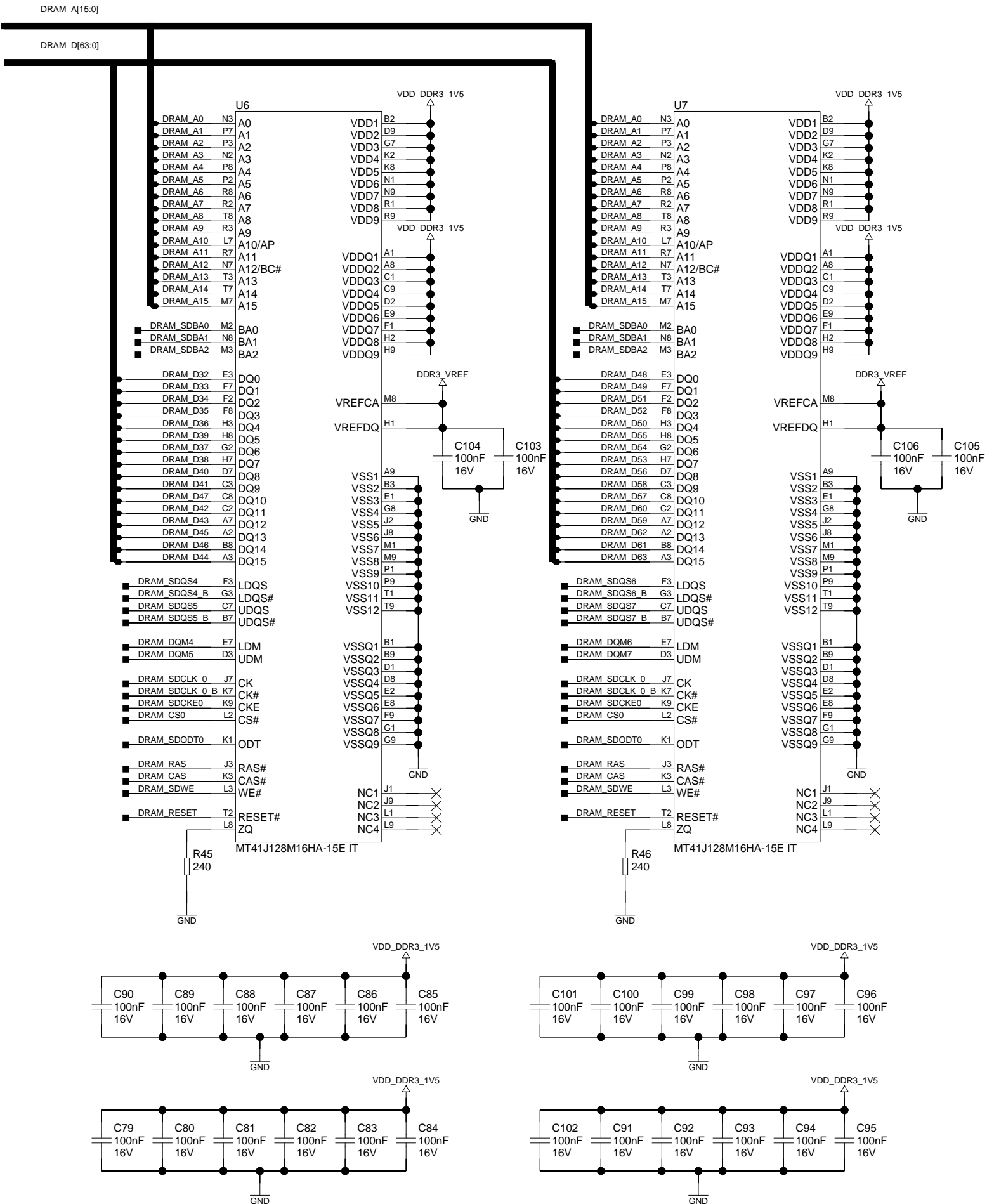
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DDR3 Bank 1 (lower 32 bit)



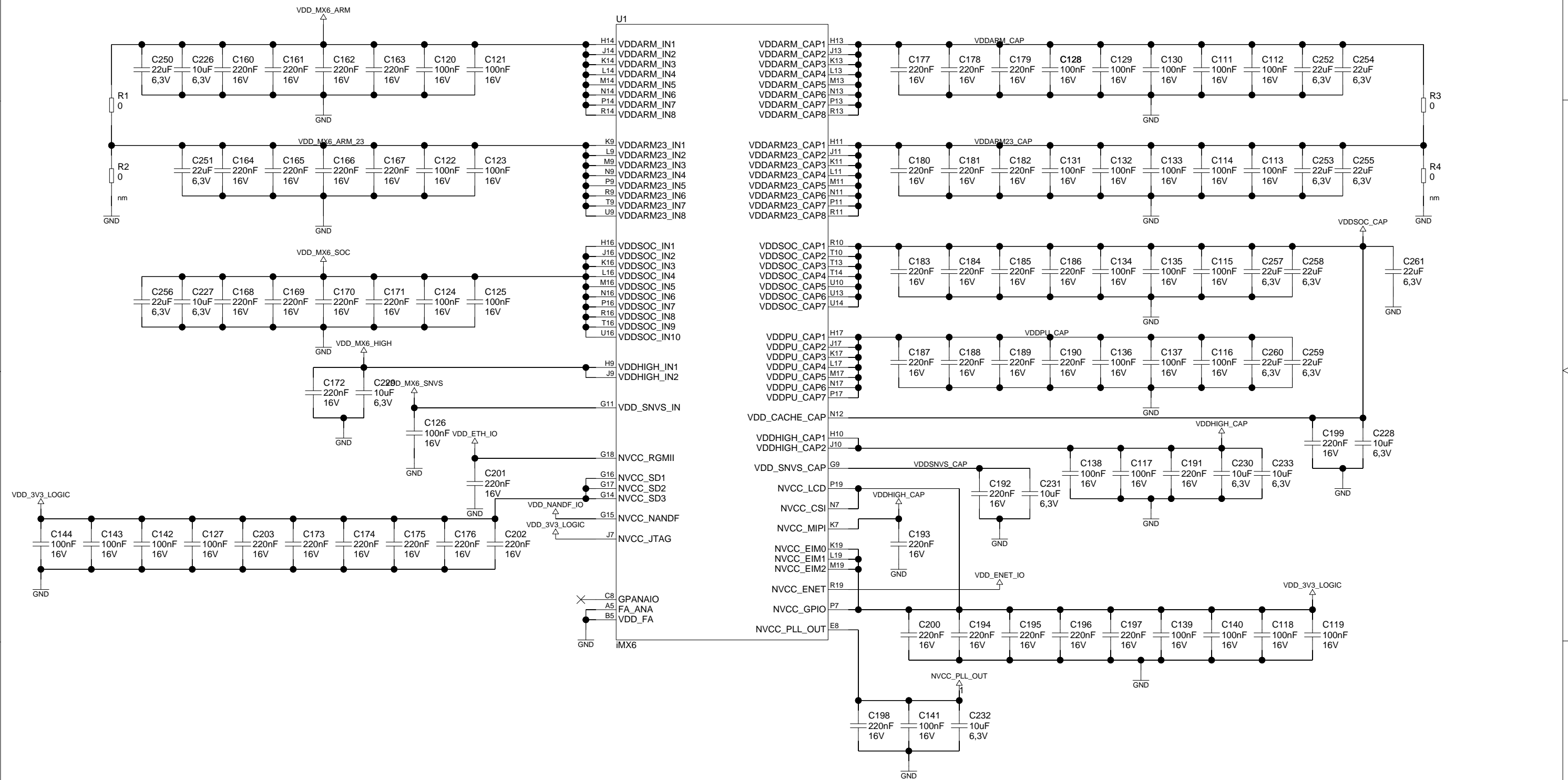
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DDR3 Bank 1 (upper 32 bit)



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i.MX6 Power



1

2

3

4

5

6

i.MX6 GND

A

A

B

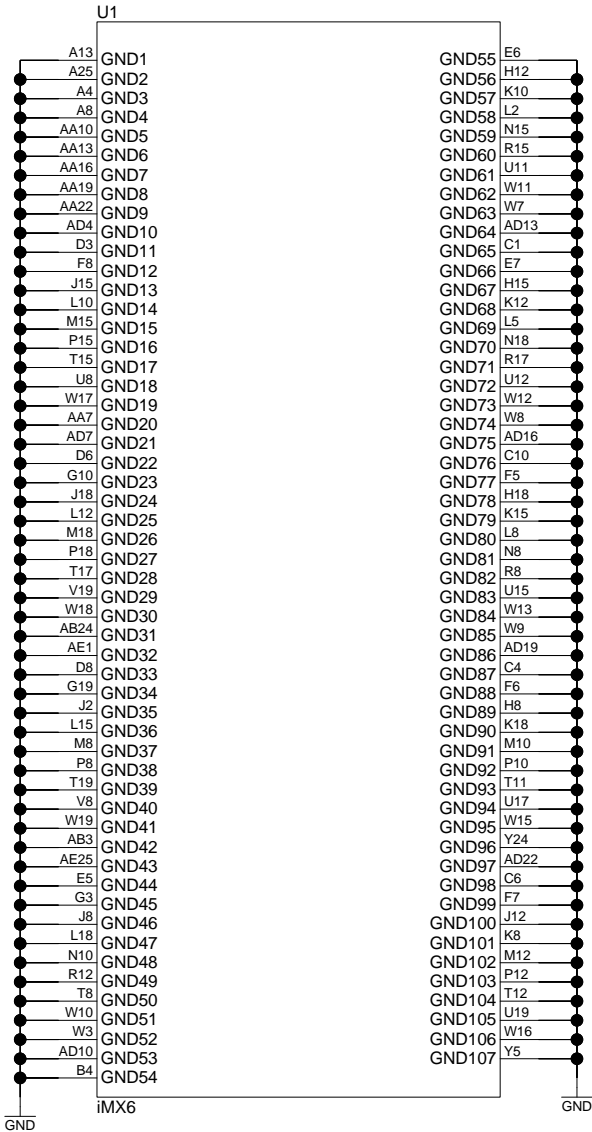
B

C

C

D

D



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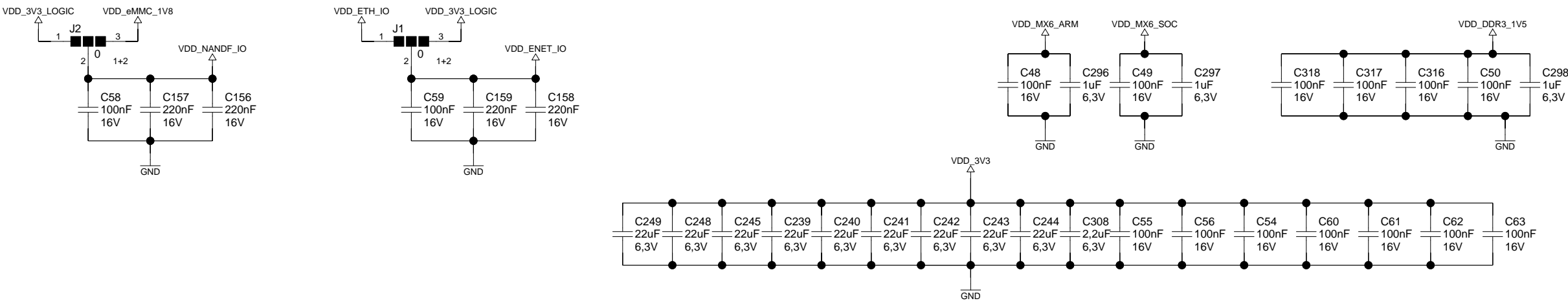
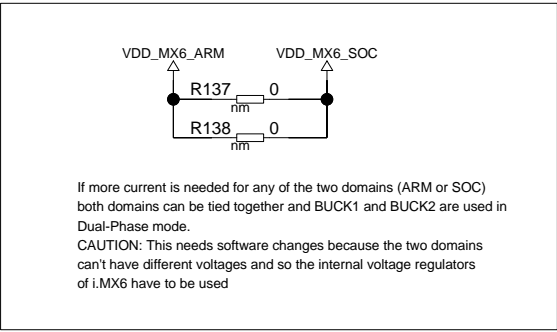
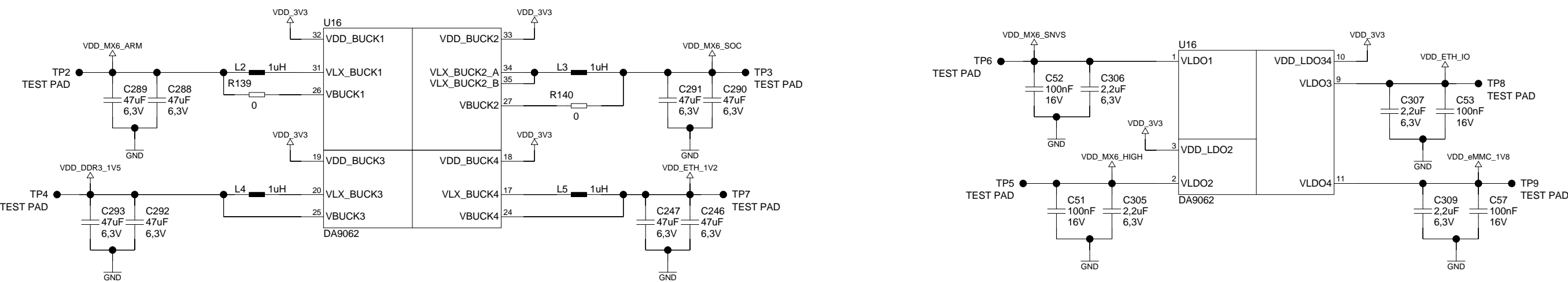
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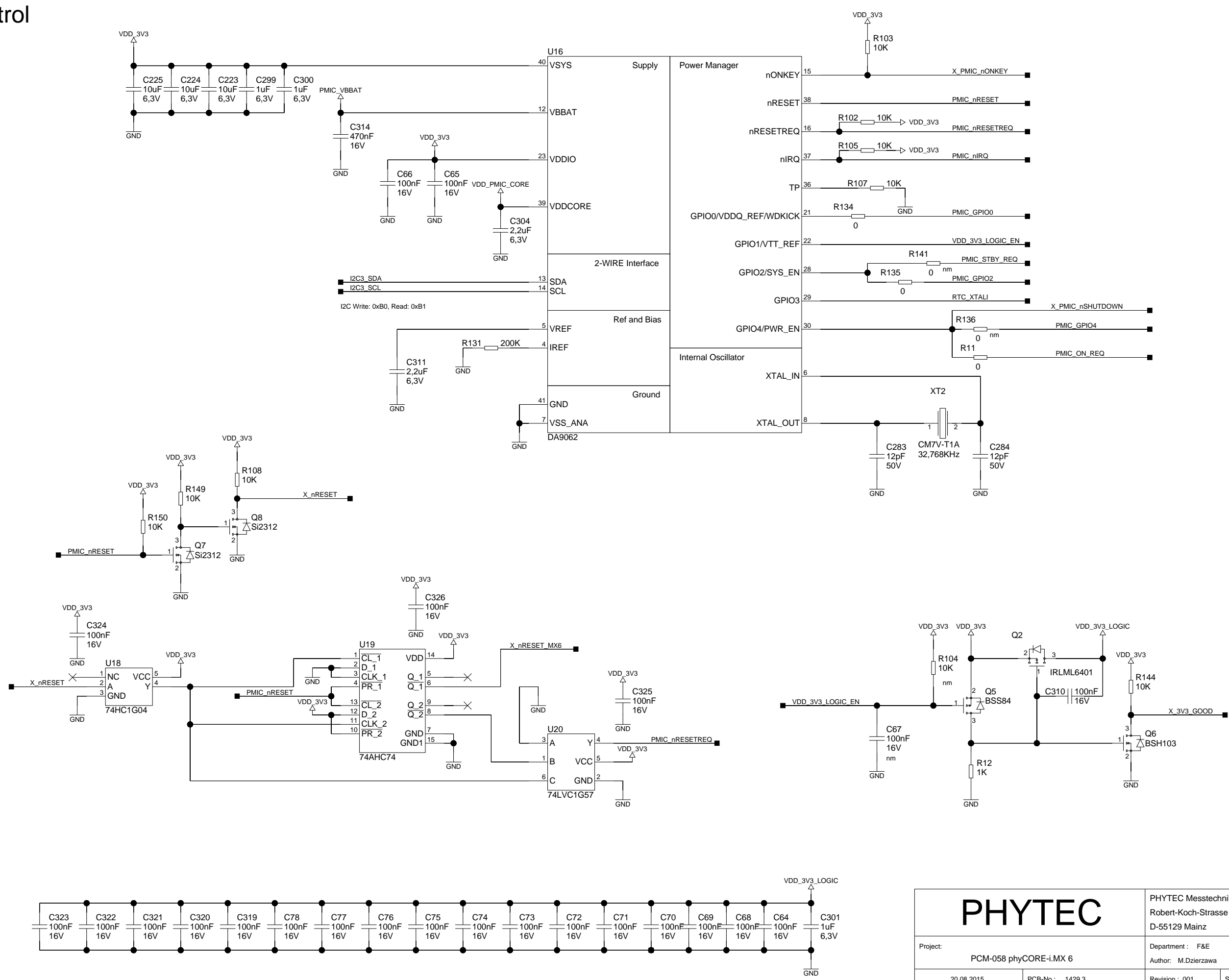
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PMIC, Power Supply

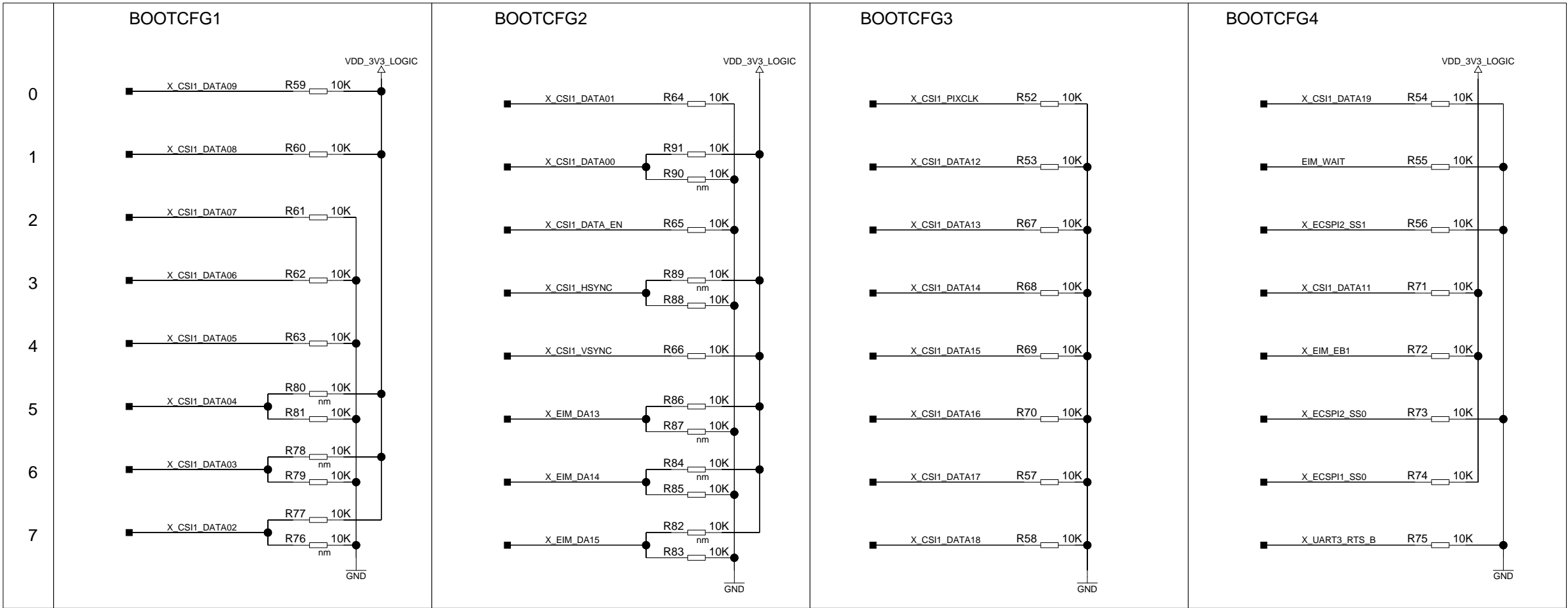


PMIC Control



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Boot Configuration



	BOOTCFG1[7:0]	BOOTCFG2[7:0]	BOOTCFG3[7:0]	BOOTCFG4[7:0]	mounted	not mounted
NAND 4/8Gb (64 pages per block)	XXX00011	XXX1X0X0	00000000	01011000	R77, R79, R81, R83, R85, R86, R88, R91	R76, R78, R80, R82, R84, R87, R89, R90
NAND 16/32Gb (128 pages per block)	10000011	00110010	00000000	01011000	R77, R79, R81, R83, R85, R86, R88, R90	R76, R78, R80, R82, R84, R87, R89, R91
eMMC	01100011	11011000	00000000	01011000	R76, R78, R80, R82, R84, R87, R89, R91	R77, R79, R81, R83, R85, R86, R88, R90