

## Drexel University Electrical and Computer Engineering Dept. ECEC-413

## Assignment 2: Pthread Gaussian Elimination

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Date: 05/03/2020

## **Gaussian Elimination using Barriers:**

For this assignment, the introduction of barriers were used to extend the serial or single thread component into a multi-thread for the Gaussian Elimination. Barriers are used to make sure the threads are running in parallel or synchronously. Each thread has to wait for the other thread to finish in order to proceed to the next step. First you have to initialize the barriers to be used in the algorithm; in this assignment two are intialized: division step and elimination step. After initializing it, each thread is given a thread ID and will be pointed to the matrix structure. Once all threads are finished going through the Gaussian Elimination, the barriers would be destroyed. The pthread\_barrier\_destroy function destroys the barriers pointed to by the barrier arguments. Once the barriers are destroyed it releases the resources it used and should be reinitialized with pthread barrier init before it is called again.

Figure 2: Initializing Pthread Barrier and Destroying Barrier

For the pthread computation for this assignment, it is very similar to the serial or single thread logic. Each thread has to go through the division and elimination algorithm at the same time. In the division component, all of the threads are working on the same row while each thread is managing the columns that they are allocated to. After each thread is finished, it will wait at the first barrier till all threads are finished before it proceeds to the next step. In the elimination step, each thread works on the rows which they are assigned to. In this step, the elimination process will set the pivot points in the matrix to be zero. Once threads are finished, it will wait at the

barrier till all threads are finished before it will proceed through the loop again. Once all the steps are exhausted and the entire matrix is finished, the principal diagonal entry is set to 1 for each row.

Figure 2: Logic Pthread Computation with Barrier Wait

## **Results:Speed Up:**

Matrix Size	Single Thread	4 Threads	8 Threads	16 Threads	32 Threads
512x512	0.06	0.135	0.109	0.12	0.142
1024x1024	0.381	0.619	0.556	0.547	0.572
2048x2048	2.51	3.845	3.36	3.33	3.32

Table 1: Gaussian Elimination Data on Xunil-05

Matrix Size	Single Thread	4 Threads	8 Threads	16 Threads	32 Threads
512x512		44.44%	55.05%	50.00%	42.25%
1024x1024		61.55%	68.53%	69.95%	66.61%
2048x2048		65.28%	74.40%	75.38%	77.71%

Table 1: Speedup of Gaussian Elimination