

Revisión de diseño de alto nivel del alta de planes

20/03/2017

1.0

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**Control De Versiones**

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| Nombre Del Archivo | Versión | Fecha de modificación | Autor | Comentarios |
| Csoft-HLDR\_Alta-De-Planes-200317.docx | 1.0 | 20/03/17 | GVV | Creación del documento |
| Csoft-HLDR\_Alta-De-Planes-200317.docx | 1.1 | 30/03/17 | GVV | Se pusieron las iniciales del autor correcto |
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# Revisión diseño de alto nivel

**PSP2 Design Review Checklist**

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| Student | Guillermo Vivaldo Vazquez | Date | 20/03/2017 |
| Program | Alta De Planes | Program # | N/A |
| Instructor | PEBM | Language | C |

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| Purpose | To guide you in conducting an effective design review |
| General | - Review the entire program for each checklist category; do not attempt to review for more than one category at a time!  - As you complete each review step, check off that item in the box at the right.  - Complete the checklist for one program or program unit before reviewing the next. |

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| Complete | Verify that the design covers all of the applicable requirements.  - All specified outputs are produced.  - All needed inputs are furnished.  - All required includes are stated. | MB  X  X  X | B | R | M |
| External Limits | Where the design assumes or relies upon external limits, determine if behavior is correct at nominal values, at limits, and beyond limits. |  |  |  |  |
| Logic | - Verify that program sequencing is proper.  Stacks, lists, and so on are in the proper order.  Recursion unwinds properly.  - Verify that all loops are properly initiated, incremented, and terminated.  - Examine each conditional statement and verify all cases. | X    X    X |  |  |  |