

Laboratorio de Microcomputadoras - 66.09

Keep Coding And Nobody Explodes

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Observaciones:							

Coloqu	iio
Nota final	
Firma profesor	

Fecha de aprobación

Firma J.T.P

1. Introducción

Keep Talking and Nobody Explodes (KTANE por sus siglas) es un videojuego promotor de la interacción social, en el que un jugador debe desactivar una bomba que sólo él puede ver en su pantalla. Para ello, cuenta con la ayuda de los demás jugadores, que representan al grupo de expertos que saben desarmar las bombas. Bajo esta modalidad, la persona que puede ver la bomba deberá decirle al grupo de expertos qué ve, y dicho grupo deberá responderle qué acciones tomar para evitar que esta explote.

El objetivo del juego entonces consiste en lograr desactivar la bomba antes de que detone, debiendo los participantes resolver en el proceso una serie de minujuegos en forma de módulos de la propia bomba. En la versión original, los módulos van desde minijuegos similares al Simon says hasta laberintos y conjuntos de cables a desconectar.

2. Objetivos propuestos

En este proyecto, nos propusimos crear una versión simplificada y en la vida real de éste juego, en la que los jugadores también deban resolver una serie de módulos para lograr desarmar la bomba. Para esto, utilizamos un microcontrolador de 8 bits, displays de 7 segmentos para la cuenta regresiva de la bomba, y una pantalla táctil para la representación de los juegos. El jugador contará con tres vidas (o *strikes*).

En particular, nos propusimos realizar los siguientes módulos/minijuegos:

- El simón dice: una versión del juego clásico de *simon says*. En este se debe repetir una secuencia de colores apretando los botones de dichos colores. Para esta implementación en particular, los colores a oprimir no son los mismos de la secuencia sino que dependen de la cantidad de veces que el jugador haya perdido en los minijuegos anteriores.
- El juego de la memoria: se compone de una serie de botones con números que aparecen y que el jugador debe ir presionando según determinadas reglas. El juego progresa en etapas, y el jugador debe recordar en cada una qué botones y en qué posiciones presionó en etapas pasadas.
- Los cables: consiste en varios cables conectados al microcontrolador. Dependiendo de ciertos colores y un número en pantalla se deberá desconectar un cable en particular. Es el último minijuego y no cuenta con segundas oportunidades como el resto.
- El código de barras: se tiene un conjunto de barras que indican un subconjunto dentro de un diagrama de Venn, según este conjunto cuál de las barras se deberá presionar.

Además se estos módulos, se implementan:

- Cuenta regresiva: marcará el tiempo disponible para finalizar el juego de forma exitosa.
- Sonidos: se agregan sonidos de victoria y derrota, además de indicadores de click.

¹Página oficial: http://www.keeptalkinggame.com/

• Strikes: marcan la cantidad de vidas perdidas.

En particular, se realizaron las siguientes simplificaciones respecto a la versión original:

- Los juegos se deberán jugar en secuencia en vez de poder elegir el orden a realizarlos.
- Por una cuestión de practicidad los cables se desconectan en vez de cortarse.
- El juego de los cables es el último y es a todo o nada.

La reglamentación para esta versión puede encontrarse en el manual del juego adjunto en este informe.

3. Objetivos logrados

Luego de la realización de este trabajo logramos implementar los juegos propuestos, además, implementamos un driver para la pantalla desde cero específico para el microcontrolador y la pantalla que utilizamos que resultó más corto y velóz que el original (específicamente, el de la librería de la pantalla).

Por último, gracias a que generamos una función de randomización para hacer las inicializaciones de los juegos, pudimos obtener un juego divertido y jugable.

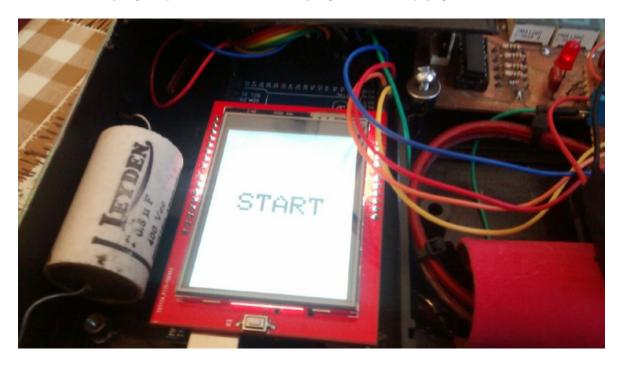


Figura 1: Pantalla mostrando "START" para iniciar el juego.

4. Descripción del Hardware

Como ya se mencionó, el proyecto fue basado en un microcontrolador de 8 bits que maneja la cuenta regresiva de la bomba. Ésta será desactivada al cortar algún cable específico, y para averiguar el mismo se deberán completar minijuegos a realizar sobre una pantalla táctil. El microcontrolador mostrará los minijuegos en esa pantalla, y en caso de que los jugadores no puedan desactivar la bomba, la hará "detonar", terminando con la partida en derrota.

La figura 2 muestra un diagrama en bloques con las conexiones de estos componentes:

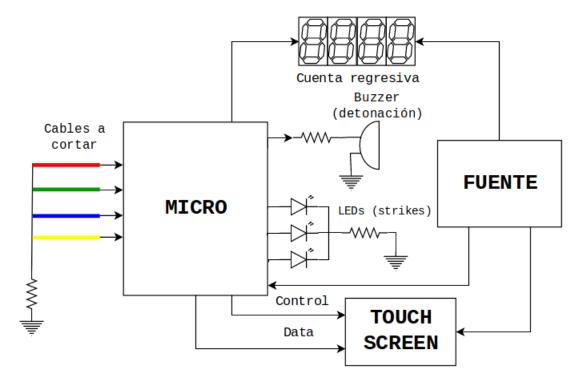


Figura 2: Diagrama en bloques de los componentes del proyecto.

Además, las fig. 3 y 4 muestran los circuitos pertinentes para el proyecto. En la primera de ellas, se observa el esquemático de la placa Arduino Mega (empleada para este proyecto), que incluye la posibilidad de alimentarse por USB o a través del programador. Si bien la placa es utilizada en conjunto para programar el microcontrolador, sólo se utiliza el cristal y la alimentación a través del regulador de tensión en el proyecto en sí.

En la segunda figura, se presenta el esquemático del circuito usado para el proyecto, con las conexiones empleadas para la pantalla táctil y la cuenta regresiva. Ésta en particular consiste en una placa de multiplexado de display de siete segmentos, donde se utilizan tres displays. Además, se cuenta con un buzzer que sonará al perder o ganar y varios LEDs que marcarán la cantidad de vidas que le quedan al jugador.

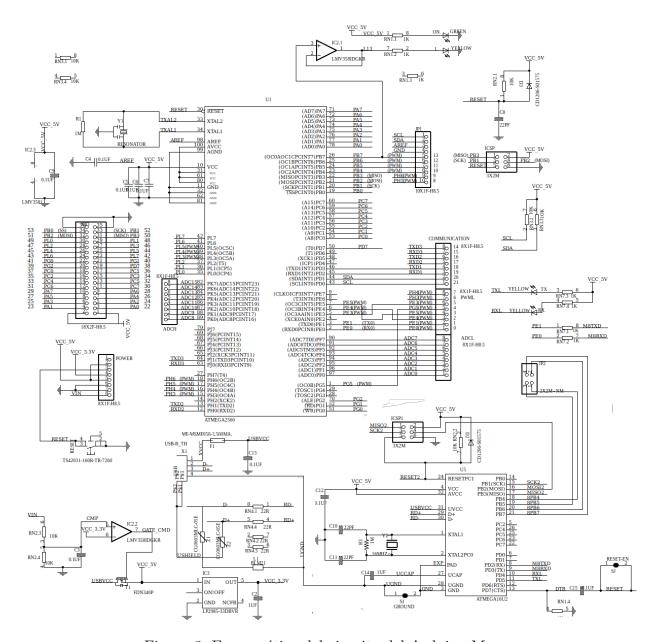


Figura 3: Esquemático del circuito del Arduino Mega.

El hardware específico usado por cada uno de los módulos (minijuegos) es el siguiente:

- Módulo I: "Los cables". Emplea los cables de colores conectados al microcontrolador. Se utiliza además la pantalla para dar pistas sobre qué cable desconectar.
- Módulo II: "Simon says". Este módulo utiliza la pantalla táctil únicamente, tanto para mostrar los diversos colores como para determinar qué color presionó el usuario.
- Módulo III: "El código de barras" Este módulo sólo utiliza la pantalla táctil para mostrar el código de barras y recibir un único toque del usuario.
- Módulo IV: "El juego de la memoria" Emplea sólamente la pantalla táctil para mostrar sucesivas rondas de números y determinar en cada una de ellas un botón presionado por el usuario.

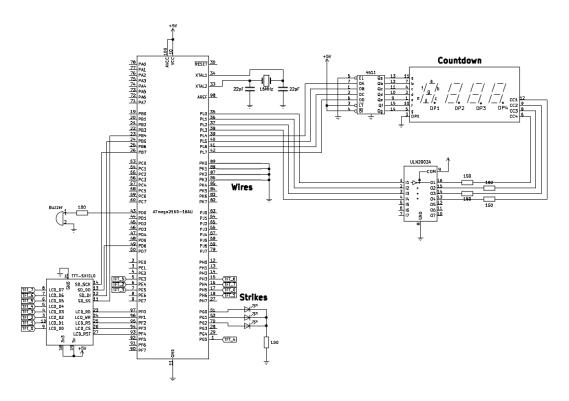


Figura 4: Esquemático del circuito usado para el proyecto.

La siguiente imagen muestra una fotografía de la bomba final funcionando:

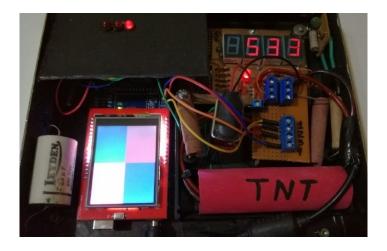


Figura 5: Imagen de la bomba funcionando con el juego del "Simon says".

5. Descripción del Software

El diagrama de flujo básico para el microcontrolador de este proyecto se puede apreciar en la fig. 6. Allí se esquematiza la interrupción por timer, encargada de decrementar la cuenta regresiva de la bomba cada cierto tiempo, y el programa principal del microcontrolador, el cual se invoca obviamente de forma constante en un loop infinito.

Como se puede ver, luego de configurar correctamente el microcontrolador, se invoca una rutina de randomización, en la que se elegirán los distintos parámetros aleatorios de los juegos y el cable correcto que desactiva la bomba. La rutina de randomización utiliza como semilla al timer.

Una vez presionado el botón de inicio, comenzará la cuenta regresiva y los jugadores podrán participar en los distintos minijuegos. Mientras los jugadores no hayan desactivado la bomba ni hayan perdido, el juego continuará normalmente. Luego, ya sea por el caso de victoria o derrota (con la bomba detonando), el juego terminará con algún mensaje en la pantalla, y los jugadores podrán iniciar el proceso de nuevo.

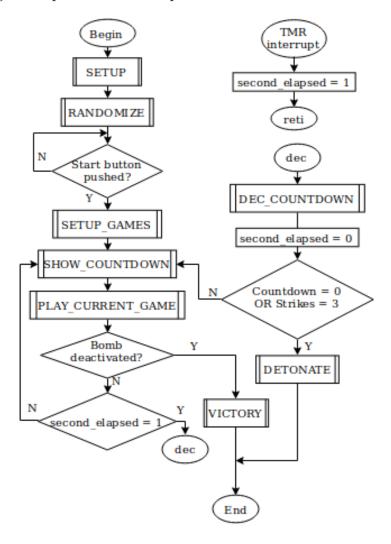


Figura 6: Diagrama de flujo del software desarrollado.

6. Conclusiones y posibles mejoras

Como primera conclusión podemos mencionar la mejora de velocidad del código en assembly respecto de la misma funcionalidad hecha en C++. Pudimos observar esto al hacer desde cero las librerías de control de la pantalla táctil usada para este proyecto y compararlas con las oficiales, las cuales originalmente estaban escritas en C++ y de forma genérica, para soportar diversos dispositivos.

De la mano con esto, nos parece importante también recalcar cómo afecta el cableado de los componentes al momento de la velocidad de ejecución del código. En particular, la pantalla no utilizaba pines de todo un mismo puerto, de forma que fue necesario realizar varias máscaras para no pisar los valores de aquellos pines que no se empleaban. Estos cálculos (aplicar las máscaras para seleccionar pines de un puerto y para cargar los valores a la pantalla) agregaban tiempo de procesamiento extra. Dado que esta configuración nos otorgaba otras ventajas, la mantuvimos mejorando la velocidad usando números de 2 bytes simétricos (es decir, si un número lo consideramos un valor de 16 bits, los primeros 8 bits serán igual que los segundos 8 bits), de forma de no necesitar cargar dos valores separados para luego hacer el seteo.

Por último, dado que los tres miembros del equipo utilizamos distribuciones *Linux*, nos encontramos con dificultades respecto a las diferencias entre la bibliografía y las consecuencias de ensamblar en un entorno que no fuera *Windows*. Por ejemplo, las posiciones de la tabla de interrupciones no coincidían dado que las direcciones que figuran en el manual del *interrupt handler* ya estaban dividas por dos (*shifteadas*) representando una dirección de flash que apuntaba a palabras de 16 bits, mientras que nosotros al utilizar la directiva .ORG debimos direccionar al byte (*avr-gcc*, compilador que utilizamos, direcciona a byte).

Por otro lado, como posibles mejoras y próximos pasos podemos proponer:

- Permitir que los jugadores cambien de un juego a otro en cualquier momento en vez de que se deban jugar uno después del otro.
- Agregar juegos que dependan del tiempo que marca el contador.
- Agregar juegos que dependan de si se realizó ya antes otro juego.
- Agregar juegos que no utilicen la pantalla sólamente sino también otros dispositivos que podrían conectarse al microcontrolador, como sensores de movimiento o temperatura.

7. Bibliografía y recursos

- "AVR Microcontroller and Embedded Systems: Using Assembly and C", Muhammad Ali Mazidi, Sarmad Naimi y Sepehr Naimi. Prentice Hall, 2011
- Manual de referencia de avr-gcc. Disponible en: https://www.microchip.com/webdocAVRLibc ReferenceManualoverview_1overview_gcc.html.
- "Adafruit GFX Graphics Library", Phillip Burgess. Disponible en: https://dn-learn.adafruit .comdownloadspdfadafruit-gfx-graphics-library.pdf
- "AVR341: Four and five-wire Touch Screen Controller", Atmel Application Note. Disponible en: http://www.lysator.liu.se kjell-eembeddeddoc8091.pdf.
- Hojas de datos de los diversos componentes.

8. Anexo I: Código producido

```
main.S
                                                                                     BREQ decrement_countdown RJMP loop
         KEEP CODING AND NOBODY EXPLODES (KCANE)
                                                                                  player_deactivated_bomb:
                                                                                     CALL GAME_VICTORY_SCREEN RJMP main
  Project for 66.09. Laboratorio de Microcomputadoras
  Faculty of Engineering, University of Buenos Aires
                                                                                  decrement_countdown:
  By Ana Czarnitzki, Alejandro García & Juan Fresia
                                                                                     CALL DEC_DISPLAYS
                                                                                    CLR R18
STS second_has_passed, R18
; Check if the displays reached zero
CALL CHECK_DISPLAYS_ARE_ZERO
                  GENERAL PROJECT INFORMATION
                                                                                     CPI R24, 1
BRNE loop
; If the displays reached zero, game over!
  Microcontroller used: ATmega2560 @16MHz
                                                                                  player_exploded:
CALL GAME_DEFEAT_SCREEN
  Other hardware used:
     - 2.4" TFT based on the ILI9320
- 7 segment displays multiplexing board, based
                                                                                     RJMP main
      on CD4511 decoder and ULN2003
- 3v buzzer and LEDs for game results
                                                                                    AUXILIAR FUNCTIONS
  Ports used:
     - TFT control pins on TFT_CONTROL_PORT (PORTF)
- TFT data pins distributed on PORTE, PORTG and
                                                                                  ; Sets the used PORTs as input/output, enables and ; starts timmerO overflow interrupt, and gives an
     PORTH
                                                                                     initial value to used RAM variables.
      - Displays on DISPLAYS_PORT (PORTL)
                                                                                  SETUP:
      - Wires for the bomb on WIRES_PORT (PORTK)
                                                                                     CALL SETUP_INTERRUPTS
CALL SETUP_DISPLAYS
   .MACRO INPUT dest, source
                                                                                     CALL TFT_START
  .IF \source < 0x40
IN \dest, \source
                                                                                     CALL SETUP_BUZZER
   . ELSE
                                                                                     CALL GAME_STARTING_SCREEN
   LDS \dest, \source
   .ENDIF
                                                                                     CALL SETUP GAMES
                                                                                        Clear the second_has_passed flag
   .MACRO OUTPUT dest, source
                                                                                     CLR R20
  .IF \dest<0x40
OUT \dest, \source
                                                                                     STS second_has_passed, R20
   . FLSE
   STS \dest, \source
   .ENDIF
                                                                                    Auxiliar flag to know if a second has passed
   . ENDM
                                                                                  second_has_passed:
  #include "avr-linux.h"
#include "interrupts.S"
#include "displays.S"
#f-lude "displays.S"
  #include "displays.S"
#include "tft-touch.S"
#include "tft-draw.S"
#include "games.S"
#include "buzzer.S"
                                                                                                                interrupts.S
                                                                                     CSEG
                                                                                      .ORG O
                                                                                     JMP main
                              MAIN PROGRAM
  -----
                                                                                     ; TimerO interrupt
                                                                                      ORG TIMERO_OVF_ISR
  CSEG
                                                                                     JMP TO_ISR
                                                                                     .ORG VECTORS SIZE
main:
  ; Stack configuration
LDI R16, LOW(RAMEND)
OUTPUT SPL, R16
LDI R16, HIGH(RAMEND)
OUTPUT SPH, R16
                                                                                  ; Setups the used interrupts and their registers. ; Note this function DOES NOT enable interrupts. SETUP_INTERRUPTS:
                                                                                     ; Configure timer0 interrupt to count up to ; 1 second with an extra count variable. ; Making timer0 count 157 times with a ; 1024:1 prescale and CLK of 16MHZ makes ; it count 1024*157/16M = 0.01 second. Hence
   CALL SETUP
                                                                                        count must count up to 100 for 1 second.
  CALL SHOW_DISPLAYS
CALL PLAY_CURRENT_GAME
CALL CHECK_BOMB_WAS_DEACTIVATED
                                                                                     CLR R20
                                                                                     STS count, R20
                                                                                     LDI R20, (1 << TOIE0)
OUTPUT TIMSKO, R20
   CPI R24, 1
   BREQ player_deactivated_bomb
                                                                                     LDI R20, 99; 256 - 99 = 157

OUTPUT TCNTO, R20

LDI R20, 0x05; Prescaler 1024:1

OUTPUT TCCROB, R20
  CALL CHECK_STRIKES_REACHED_MAX CPI R24, 1 BREQ player_exploded
  LDS R18, second_has_passed CPI R18, 1
```

```
; TimerO interrupt handler that will be called every ; 0.01 seconds. Once the count variable reaches 100 ; (i.e. 1 second elapsed), it must DEC_DISPLAYS.
                                                                                               DEC R18
                                                                                               BRNE setup_displays_loop; Load the PLAYING_MINUTES tens and units
                                                                                                LDI R18, (PLAYING_MINUTES / 10)
STS displays_ram + 3, R18
TO_ISR:
  ; First store the called saved registers
     since we don't know what important value
                                                                                               LDI R18, (PLAYING_MINUTES % 10)
      they could have right now
                                                                                                STS displays_ram + 2, R18
   PUSH R18
PUSH R19
                                                                                               RET
                                                                                            ; Performs a displays multiplexing, showing in the ; displays the values of displays_ram. {\tt DISPLAYS\_PORT}
   PUSH R20
   PUSH R21
                                                                                            ; displays the values of displays_ram. DISPLAYS_PUR; is used for the multiplexing, being DO-D3 used as; the control pins (connected to the ULN) and D4-D7; as the BCD number (connected to the 4511).

SHOW_DISPLAYS:
LDI ZL, LOW(displays_ram)
LDI ZH, HIGH(displays_ram)
   PUSH R22
   PUSH R23
   PUSH R24
   PUSH R25
   PUSH R26
   PUSH R27
                                                                                               LDI R18, 0x1
   PUSH R30
                                                                                            next_display:
LD R19, Z+
   PUSH R31
   INPUT R18, SREG; Very important
                                                                                               LD R19, Z
SWAP R19
   PUSH R18
LDI R20, 99
OUTPUT TCNTO, R20
                                                                                                ADD R19, R18
                                                                                               OUTPUT DISPLAYS_PORT, R19
   LDS R20, count
                                                                                               PUSH R18
   INC R20
CPI R20, 100
                                                                                               PUSH ZL
PUSH ZH
   BREQ clear_count
RJMP store_count
                                                                                               CALL DISPLAY_DELAY
                                                                                               POP ZH
POP ZL
POP R18
clear_count:
      If here, one second has elapsed
   LDI R24, 1
   STS second_has_passed, R24 CLR R20
                                                                                               LSL R18
                                                                                               SBRS R18, DISPLAYS_AMOUNT RJMP next_display
store_count:
   STS count, R20
     Restore the saved registers
   POP R18
OUTPUT SREG, R18
                                                                                            ; Decrement the displays by "1 second", considering ; the displays represent a MM:SS format. This ; function should be called every one second by the
  POP R31
POP R30
POP R27
POP R26
POP R25
POP R24
POP R23
                                                                                            ; main program to properly make the countdown. DEC_DISPLAYS:
                                                                                               LDI ZL, LOW(displays_ram)
LDI ZH, HIGH(displays_ram)
; R18=display number (0 to DISPLAYS_AMOUNT)
                                                                                                CLR R18
   POP R22
POP R21
                                                                                            next_dec:
LD R19, Z
   POP R20
                                                                                                DEC R19
   POP R19
POP R18
                                                                                                CPI R19, 255; display = -1?
                                                                                               BRNE dec_finished
   RETT
                                                                                                ; If here, I have to decrement the
                                                                                                  next display by 1 unit too
; Auxiliar counter for timerO interrupt
                                                                                               LDI R19, 9; reset this display CPI R18, 1
  RYTF 1
                                                                                               BRNE store_and_next_dec; Note the second display must be; reset to 5 and not to 9
LDI R19, 5
                                   displays.S
   .GLOBAL SETUP_DISPLAYS
.GLOBAL SHOW_DISPLAYS
.GLOBAL DEC_DISPLAYS
                                                                                            store_and_next_dec:
                                                                                               ST Z+, R19
INC R18
CPI R18, DISPLAYS_AMOUNT
   .GLOBAL CHECK_DISPLAYS_ARE_ZERO
                                                                                               BRNE next_dec
   .EQU DISPLAYS_PORT, PORTL
   .EQU DISPLAYS_DDR, DDRL
.EQU DISPLAYS_AMOUNT, 4
.EQU PLAYING_MINUTES, 6
                                                                                            dec_finished:
   ST Z, R19
                                                                                                RET
                                                                                             ; Checks if all the displays reached the O value
; Initializes the displays array in RAM memory. Uses ; the PLAYING_MINUTES constant as the initial value ; for a countdown in the displays.
                                                                                            ; (i.e. if the countdown is over). Returns true or ; false on R24 (false as 0, true as 1).
                                                                                            CHECK_DISPLAYS_ARE_ZERO:

LDI ZL, LOW(displays_ram)

LDI ZH, HIGH(displays_ram)

CLR R24; R24 initially false

; R18=display number (0 to DISPLAYS_AMOUNT)
SETUP_DISPLAYS:
LDI R18, 0xFF
   OUTPUT DISPLAYS_DDR, R18
   LDI ZL, LOW(displays_ram)
LDI ZH, HIGH(displays_ram)
                                                                                                CLR R18;
                                                                                            check_zero_loop:
   LDI R18, DISPLAYS_AMOUNT
                                                                                               LD R19, Z+
CPI R19, 0
   CLR R1
setup_displays_loop:
ST Z+, R1
                                                                                               BRNE displays_not_zero
```

```
INC R18 CPI R18, DISPLAYS_AMOUNT
                                                                                      ; The range is set by a pair of points (x1, y1) and ; (x2, y2) which makes a rectangle. Preconditions: ; x2 > x1 and y2 > y1.
   BRNE check_zero_loop
   ; If here, all displays are zero INC R24; R24 = true
                                                                                      ; Each coordinate is a 16 bit number, and all of
displays_not_zero:
                                                                                         are passed through the following registers:
                                                                                               x1 in R25:R24
y1 in R23:R22
x2 in R21:R20
   RĒT
; Performs a small delay: the time each display is ; turned on during the multiplexing.
                                                                                               y2 in R19:R18
DISPLAY_DELAY:
                                                                                      TFT_SET_ADDR_WINDOW:
                                                                                        PUSH R16
PUSH R17
   LDI R18, 80
loop1:
LDI R19, 20
                                                                                         PUSH R20
                                                                                        PUSH R21
PUSH R22
loop2:
LDI R20, 10
                                                                                         PUSH R23
loop3:
   NOP
                                                                                         PUSH R25
   DEC R20
                                                                                        CS_ACTIVE
; Save y1 value
MOVW R16, R22; R17:R16 = R23:R22
; Define column address set
; WRITE_REGISTER_32(ILI9341_COLADDRSET, x1, x2)
   BRNE loop3
   DEC R19
   BRNE loop2
   DEC R18
                                                                                         MOV R23, R25
                                                                                        MOV R22, R24
LDI R25, IL19341_COLADDRSET
CALL WRITE_REGISTER_32
   BRNE loop1
   RET
                                                                                         ; Define rows address set
                                                                                           WRITE_REGISTER_32(ILI9341_COLADDRSET, y1, y2)
   Displays array in RAM memory
                                                                                        MOVW R22, R16; R23:R22 = R17:R16
MOVW R20, R18; R21:R20 = R19:R18
LDI R25, ILI9341_PAGEADDRSET
CALL WRITE_REGISTER_32
displays_ram:
BYTE DISPLAYS_AMOUNT
                                 tft-draw.S
#include "avr-linux.h"

#include "tft-registers.S"

#include "tft-colors.S"

#include "tft-writing.S"

#include "tft-char-ROM.S"

#include "tft-delay.S"
                                                                                         CS_IDLE
                                                                                        POP R25
POP R23
                                                                                        POP R22
                                                                                        POP R21
                                                                                         POP R20
                                                                                         POP R17
CSEG
                                                                                        POP R16
                                                                                         RET
TFT_START:
   CALL TFT_INIT_PORTS
CALL TFT_ANALOG_INIT
                                                                                      TFT_BEGIN:
                                                                                         CALL TFT_RESET
   CALL TFT_BEGIN
                                                                                         LDI R25, 200
CALL MILI_DELAY
   LDI R25, 1
CALL MILI_DELAY
                                                                                        CS_ACTIVE
                                                                                        LDI R25, ILI9341_SOFTRESET LDI R24, 0x0
TFT RESET:
   CS_IDLE
                                                                                         CALL WRITE_REGISTER_8
   WR_IDLE
   RD IDLE
                                                                                        LDI R25, 50 CALL MILI_DELAY
   TFT_RESET_LOW
   LDI R25, 2 ; Magic recipe CALL MILI_DELAY TFT_RESET_HIGH
                                                                                         LDI R25, ILI9341_DISPLAYOFF LDI R24, 0x0
                                                                                         CALL WRITE_REGISTER_8
   CS_ACTIVE
                                                                                        LDI R25, ILI9341_POWERCONTROL1 LDI R24, 0x23
   CD_COMMAND
                                                                                        LDI R24, 0x23
CALL WRITE_REGISTER_8
   CLR R25
   CALL WRITE_8
WR_STROBE
                                                                                        LDI R25, IL19341_POWERCONTROL2
LDI R24, 0x10
CALL WRITE_REGISTER_8
   WR_STROBE WR_STROBE
   CS_IDLE
   RET
                                                                                         LDI R25, HIGH(ILI9341_VCOMCONTROL1)
                                                                                        LDI R24, LOW(ILI9341_VCOMCONTROL1)
LDI R23, 0x2B
LDI R22, 0x2B
CALL WRITE_REGISTER_16
TFT_INIT_PORTS:
   PUSH R19
   INPUT R19, TFT_CONTROL_DDR
ORI R19, LCD_CONTROL_PORT_MASK
OUTPUT TFT_CONTROL_DDR, R19
                                                                                        LDI R25, ILI9341_VCOMCONTROL2 LDI R24, 0xC0
                                                                                         CALL WRITE_REGISTER_8
   CALL SET_WRITE_DIR
   POP R19
                                                                                        LDI R25, IL19341_MEMCONTROL LDI R24, IL19341_MADCTL_BGR
   RET
                                                                                         CALL WRITE_REGISTER_8
; Defines the valid range for the screen addresses.
```

```
x1 in R25:R24
  LDI R25, ILI9341_PIXELFORMAT LDI R24, 0x55
                                                                                                 y1 in R23:R22
x2 in R21:R20
   CALL WRITE_REGISTER_8
                                                                                            y2 in R19:R18
color in R17:R16
   LDI R25, HIGH(ILI9341_FRAMECONTROL)
                                                                                        TFT_FILL_RECT:
  LDI R24, LOW(ILI9341_FRAMECONTROL)
LDI R23, 0x00
LDI R22, 0x1B
CALL WRITE_REGISTER_16
                                                                                           PUSH R14
PUSH R15
                                                                                           PUSH R18
                                                                                           PUSH R19
                                                                                           PUSH R20
  LDI R25, ILI9341_ENTRYMODE LDI R24, 0x07
                                                                                           PUSH R21
                                                                                           PUSH R22
   CALL WRITE_REGISTER_8
                                                                                           PUSH R23
                                                                                           PUSH R24
  LDI R25, ILI9341_SLEEPOUT LDI R24, 0x00
                                                                                           PUSH R25
                                                                                           PUSH R26
   CALL WRITE_REGISTER_8
                                                                                           PUSH R27
                                                                                           PUSH R28
  LDI R25, 150
CALL MILI_DELAY
                                                                                           PUSH R29
                                                                                           CALL TFT_INIT_PORTS
                                                                                           ; x2-x1 <--- R29:R28
; y2-y1 <--- R27:R26
  LDI R25, ILI9341_DISPLAYON LDI R24, 0x00
   CALL WRITE_REGISTER_8
                                                                                           MOVW R28, R20
                                                                                           SUB R28, R24
SBC R29, R25
                                                                                           MOVW R26, R18
SUB R26, R22
SBC R27, R23
; Fills the screen with a given color. The color is
  received on R25:R24.
TFT_FILL_SCREEN:
                                                                                           ; setAddrWindow(x1, y1, x2, y2) CALL TFT_SET_ADDR_WINDOW
  PUSH R16
  PUSH R17
                                                                                           ; Loop to print every pixel CS_ACTIVE
   MOVW R16, R24; R17:R16 = R25:R24
   CLR R25
                                                                                           CD_COMMAND
                                                                                           LDI R25, ILI9341_MEMORYWRITE CALL WRITE_8
   CLR R24
   CLR R23
  CLR R25
CLR R22
LDI R21, HIGH(TFT_WIDTH - 1)
LDI R20, LOW(TFT_WIDTH - 1)
LDI R19, HIGH(TFT_HEIGHT - 1)
LDI R18, LOW(TFT_HEIGHT - 1)
CALL TFT_FILL_RECT
                                                                                           CD_DATA;
                                                                                           ; for (int x = (x2-x1); x > 0; x--)
; for (int y = (y2-y1); y > 0; y--)
; WRITE_8(hi(color))
                                                                                                     WRITE_8(lo(color))
                                                                                             x = R29:R28
                                                                                              v = R27:R26
  POP R17
                                                                                           , y - 12: 12
ADIW R28, 1
ADIW R26, 1
MOVW R14, R26; R15:R14 = R27:R26
  POP R16
; Draws a filled rectangle in a range of screen
; addresses. The range is set by a point (x, y) on ; the upper-left vertex of the rectangle and a witdh ; and height. Each value is a 16 bit number, and all ; of them are passed through the following registers
                                                                                           ; If hi(color) == lo(color), write color once
                                                                                           ; the rest of the loop will only do WR_STROBE CP R16, R17
BRNE tft_fill_rect_loop_x
                                                                                           MOV R25, R17
                                                                                           CALL WRITE_8
WR_STROBE
           x in R25:R24
    y in R23:R22
width in R21:R20
  height in R19:R18 color in R17:R16
                                                                                        tft_fill_rect_loop_x:
   MOVW R26, R14; R27:R26 = R15:R14
   CPI R28, 0x0
TFT_FILL_RECT_HW:
  PUSH R18
                                                                                           BRNE tft_fill_rect_loop_y
  PUSH R19
                                                                                           CPI R29, 0x0
                                                                                           BRNE tft_fill_rect_loop_y
RJMP tft_fill_rect_exit
  PUSH R20
PUSH R21
  ADD R20, R24
ADC R21, R25
                                                                                        tft_fill_rect_loop_y:
                                                                                           CPI R27, 0x0
BRNE tft_fill_rect_color_pixel
   ADD R18, R22
                                                                                           CPI R26, 0x0
                                                                                           BRNE tft_fill_rect_color_pixel
SBIW R28, 1
RJMP tft_fill_rect_loop_x
  ADC R19, R23
CALL TFT_FILL_RECT
   POP R21
  POP R20
POP R19
                                                                                        tft_fill_rect_color_pixel:
                                                                                           ; Skip optimization
CP R16, R17
   POP R18
                                                                                           BRNE tft_fill_rect_color_pixel_diff
   RET
                                                                                        tft_fill_rect_color_pixel_same:
; Draws a filled rectangle in a range of screen
; addresses. The range is set by a pair of points; (x1, y1) and (x2, y2) which makes a rectangle.; Precondition x2 > x1 and y2 > y1. Each value is a; 16 bit number, and all of them are passed through; the following registers:
                                                                                              WR_STROBE
                                                                                           WR_STROBE
                                                                                           SBIW R26, 1
RJMP tft_fill_rect_loop_y
```

```
tft_fill_rect_color_pixel_diff:
  ; WRITE_8(high(color))
  MOV R25, R17
  CALL_WRITE_8
                                                                                   tft_drawChar_drawRect:
   ; fillRect_HW(x+(i*size), y+(j*size), size, size,
                                                                                       fg | bg);
CALL TFT_FILL_RECT_HW
     WRITE_8(low(color))
   MOV R25, R16
  CALL WRITE_8
SBIW R26, 1
RJMP tft_fill_rect_loop_y
                                                                                   \verb|tft_draw_char_line_loopNext|:
                                                                                       ; y += size
ADD R22, R14
ADC R23, R15
tft_fill_rect_exit:
                                                                                       LSR R11
  POP R29
POP R28
                                                                                       RJMP tft_draw_char_line_loop
  POP R27
POP R26
                                                                                   tft_draw_char_end_line:
    INC R26
   POP R25
                                                                                         x += size
   POP R24
                                                                                       ADD R24, R14
  POP R23
POP R22
POP R21
                                                                                       ADC R25, R15
                                                                                      MOVW R22, R12; R23:R22 = R13:12
RJMP tft_draw_char_next_line
   POP R20
   POP R19
                                                                                   tft_draw_char_exit:
  POP R18
POP R15
                                                                                      POP R31
POP R30
  POP R14
                                                                                      POP R27
                                                                                      POP R26
POP R15
  RET
  Draws a character c on the screen with upper-left position (x, y), colour fg and size s. Each value is 16 bits, and are passed through registers:

x in R25:R24
                                                                                      POP R14
                                                                                      POP R13
                                                                                      POP R12
                                                                                       POP R11
           y in R23:R22
                                                                                      RET
    char c in R21:R20 (R21 not used)
          fg in R19:R18
s in R17:R16 (R17 not used)
                                                                                                                   tft-touch.S
TFT_DRAW_CHAR:
                                                                                       ; TouchScreen control pins
  PUSH R11
                                                                                       ; xp (x+) H5; xm (x-) F2
  PUSH R12
   PUSH R13
                                                                                      ; yp (y+) F3
; ym (y-) H6
   PUSH R14
   PUSH R15
   PUSH R26
                                                                                       .EQU TOUCH_XP_DDR, DDRH
.EQU TOUCH_XM_DDR, DDRF
.EQU TOUCH_YP_DDR, DDRF
   PUSH R27
  PUSH R30
  PUSH R31
                                                                                       .EQU TOUCH_YM_DDR, DDRH
  ; Save size in temporal registers CLR R15 \ensuremath{\text{MOV}} R14 , R16
                                                                                       .EQU TOUCH_XP_PORT, PORTH
                                                                                      .EQU TOUCH_XM_PORT, PORTF .EQU TOUCH_YP_PORT, PORTF
     Save color in R17:R16 to prepare for fill rect
  ; Save color in R17:R16 to MOVW R16, R18; Save y coordinate MOVW R12, R22; Z = tft_char_ROM + (c*5) LDI ZH, HIGH(tft_char_ROM) LDI ZL, LOW(tft_char_ROM)
                                                                                       .EQU TOUCH_YM_PORT, PORTH
                                                                                       .EQU TOUCH_XP_MASK, 0x20
                                                                                      .EQU TOUCH_XM_MASK, 0x04
.EQU TOUCH_YP_MASK, 0x08
.EQU TOUCH_YM_MASK, 0x40
  LDI R26, 0x5
MUL R20, R26
ADD ZL, R0
ADC ZH, R1
                                                                                      .EQU TOUCH_Y_ADC, 0x3
.EQU TOUCH_X_ADC, 0x2
                                                                                       .EQU ADC_CHAN_MASK, OxOF
   CLR R26 ; R26 = i
                                                                                   CSEG
   ; Set width and height to size
  CLR R21
MOV R20, R14
CLR R19
                                                                                   ; Configures the ADCs used for reading the touched
                                                                                   ; point on screen coordinaates.
TFT_ANALOG_INIT:
   MOV R18, R14
                                                                                      PUSH R16
tft_draw_char_next_line:
                                                                                      LDI R16, (1 << REFS0)
STS ADMUX, R16
LDI R16, (1 << ADEN | 1 << ADPS0 | 1 << ADPS1 | 1
  CPI R26, TFT_CHAR_W
BREQ tft_draw_char_exit
                                                                                             << ADPS2)
   CLR R27; R27 = j
                                                                                      STS ADCSRA, R16
   ; Get char ROM data
   LPM R11, Z+
CPI R26, 0x5
                        ; line = font[char*5 + i]
                                                                                      POP R16
                                                                                      RET
   BRNE tft_draw_char_line_loop
   CLR R11
                                                                                    ; ANALOG_READ performs an ADC read from a channel
                                                                                    ; (ADC pin) indicated by register R25.
tft_draw_char_line_loop:
CPI R27, TFT_CHAR_H
                                                                                   ; Result is stored in R25:R24.
ANALOG_READ:
PUSH R16
   BREQ tft_draw_char_end_line
  INC R27
SBRS R11, 0 ; line & 0x1
                                                                                      PUSH R17
                                                                                      ; Trigger ADC read LDS R16, ADMUX
   RJMP tft_draw_char_line_loopNext
```

```
ANDI R16, ~ADC_CHAN_MASK
ANDI R25, ADC_CHAN_MASK
                                                                                                                          INPUT R17, TOUCH_XP_PORT ANDI R17, ~TOUCH_XP_MASK
    ANDI R25, AI
OR R16, R25
   UR RIG, R25
STS ADMUX, R16
LDI R16, (1 << ADSC)
LDS R17, ADCSRA
OR R17, R16
STS ADCSRA, R17
                                                                                                                          OUTPUT TOUCH_XP_PORT, R17
                                                                                                                          INPUT R17, TOUCH_XM_PORT ANDI R17, ~TOUCH_XM_MASK OUTPUT TOUCH_XM_PORT, R17
adc_wait:
   LDS R17, ADCSRA
SBRC R17, ADSC
RJMP adc_wait
                                                                                                                         ; yp as output high
INPUT R17, TOUCH_YP_DDR
ORI R17, TOUCH_YP_MASK
OUTPUT TOUCH_YP_DDR, R17
   LDS R24, ADCL
LDS R25, ADCH
                                                                                                                         INPUT R17, TOUCH_YP_PORT ORI R17, TOUCH_YP_MASK OUTPUT TOUCH_YP_PORT, R17
   POP R17
    POP R16
                                                                                                                         ; ym as output low
INPUT R17, TOUCH_YM_DDR
ORI R17, TOUCH_YM_MASK
OUTPUT TOUCH_YM_DDR, R17
    RET
; Performs an ADC conversion of the x coordinate ; with {\tt ANALOG\_READ} . Returns the result on {\tt R25:R24} .
TFT_READ_TOUCH_X:
                                                                                                                         INPUT R17, TOUCH_YM_PORT ANDI R17, ~TOUCH_YM_MASK OUTPUT TOUCH_YM_PORT, R17
    PUSH R17
   PUSH R16
    ; yp and ym as input with port = 0 INPUT R17, TOUCH_YP_DDR ANDI R17, ~TOUCH_YP_MASK
                                                                                                                          ; Returns the read value in R25:R24. ; Read ADC from XP (coordintes are inverted).
    OUTPUT TOUCH_YP_DDR, R17
                                                                                                                          LDI R25, TOUCH_X_ADC
                                                                                                                         CALL ANALOG_READ
   LDS R17, TOUCH_YM_DDR ANDI R17, "TOUCH_YM_MASK STS TOUCH_YM_DDR, R17
                                                                                                                         POP R16
POP R17
                                                                                                                         RET
   INPUT R17, TOUCH_YP_PORT ANDI R17, ~TOUCH_YP_MASK OUTPUT TOUCH_YP_PORT, R17
                                                                                                                     ; Reads both \boldsymbol{x} and \boldsymbol{y} analog pins from touchscreen
                                                                                                                        and return its readings in registers:
z2 (from yp): R25:R24
z1 (from xm): R23:R22
   LDS R17, TOUCH_YM_PORT ANDI R17, ~TOUCH_YM_MASK STS TOUCH_YM_PORT, R17
                                                                                                                     TFT_READ_TOUCH_Z:
                                                                                                                         PUSH R16
                                                                                                                          PUSH R17
        xp as output high
   LDS R17, TOUCH_XP_DDR ORI R17, TOUCH_XP_MASK
                                                                                                                         ; xp as output low INPUT R17, TOUCH_XP_DDR ORI R17, TOUCH_XP_MASK OUTPUT TOUCH_XP_DDR, R17
    STS TOUCH_XP_DDR, R17
   LDS R17, TOUCH_XP_PORT ORI R17, TOUCH_XP_MASK STS TOUCH_XP_PORT, R17
                                                                                                                         INPUT R17, TOUCH_XP_PORT ANDI R17, ~TOUCH_XP_MASK OUTPUT TOUCH_XP_PORT, R17
   ; xm as output low INPUT R17, TOUCH_XM_DDR ORI R17, TOUCH_XM_MASK OUTPUT TOUCH_XM_DDR, R17
                                                                                                                             ym as output high
                                                                                                                         INPUT R17, TOUCH_YM_DDR
ORI R17, TOUCH_YM_MASK
OUTPUT TOUCH_YM_DDR, R17
   INPUT R17, TOUCH_XM_PORT ANDI R17, ~TOUCH_XM_MASK OUTPUT TOUCH_XM_PORT, R17
                                                                                                                         INPUT R17, TOUCH_YM_PORT ORI R17, TOUCH_YM_MASK OUTPUT TOUCH_YM_PORT, R17
    ; Returns the read value in R25:R24.
   ; Yes, this is right, the touchscreen coords; are inverted with respect to the TFT ones. LDI R25, TOUCH_Y_ADC
                                                                                                                          ; xm and yp as input low INPUT R17, TOUCH_XM_DDR ANDI R17, "TOUCH_XM_MASK
    CALL ANALOG_READ
                                                                                                                          OUTPUT TOUCH_XM_DDR, R17
                                                                                                                          INPUT R17, TOUCH_XM_PORT ANDI R17, ~TOUCH_XM_MASK
    POP R16
                                                                                                                         ANDI R17, "TOUCH_XM_MASK
OUTPUT TOUCH_XM_PORT, R17
   POP R17
    RET
; Performs an ADC conversion of the y coordinate ; with {\tt ANALOG\_READ} . Returns the result on {\tt R25:R24} .
                                                                                                                          INPUT R17, TOUCH_YP_DDR ANDI R17, ~TOUCH_YP_MASK
                                                                                                                          OUTPUT TOUCH_YP_DDR, R17
TFT_READ_TOUCH_Y:
    PUSH R17
                                                                                                                         INPUT R17, TOUCH_YP_PORT ANDI R17, ~TOUCH_YP_MASK OUTPUT TOUCH_YP_PORT, R17
    PUSH R16
    ; xp and xm as input with port = 0 INPUT R17, TOUCH_XP_DDR ANDI R17, ~TOUCH_XP_MASK
                                                                                                                         ; Get both analog readings
; z1 = ANALOG_READ xm (R25:R24)
; z2 = ANALOG_READ yp (R23:R22)
LDI R25, TOUCH_X_ADC
CALL ANALOG_READ
    OUTPUT TOUCH_XP_DDR, R17
    INPUT R17, TOUCH_XM_DDR ANDI R17, ~TOUCH_XM_MASK
    OUTPUT TOUCH_XM_DDR, R17
```

```
ADD R25, R17; y = y / 8 = y >> 3
LSR R25
    LDI R23, HIGH(1024)
   LDI R22, LOW(1024)
   SUB R22, R24
SBC R23, R25
                                                                                                                           ROR R24
                                                                                                                           LSR R25
                                                                                                                           ROR R24
   LDI R25, TOUCH_Y_ADC
CALL ANALOG_READ
LDI R17, HIGH(1024)
LDI R16, LOW(1024)
SUB R16, R24
SBC R17, R25
MOV R24, R16; R25:R24 = R17:R16
                                                                                                                           LSR R25
                                                                                                                          ROR R24
                                                                                                                          POP R17
POP R16
                                                                                                                           RET
                                                                                                                      {\tt TFT\_READ\_TOUCH:}
                                                                                                                          PUSH R18
PUSH R19
    POP R16
    POP R17
                                                                                                                           PUSH R20
                                                                                                                           PUSH R21
  Performs an scaling of the x coordinate returned from ANALOG_READ and maps it to an interval suited for the screen. The mapping range is empirical and based on tftpaint example. The mapping is: [120, 920] ---> [0, 239] Which can be done like this: x' = (x - 120) * 239/800
                                                                                                                           PUSH R22
                                                                                                                           PUSH R23
                                                                                                                           PUSH R24
                                                                                                                           PUSH R25
                                                                                                                           CALL TFT_READ_TOUCH_X
                                                                                                                           MOVW R18, R24; R19:R18 = R25:R24
; The multiplication is approximated with 5/16.; The x and x' values are 16 bit numbers passed and; returned through registers R25:R24.

TFT_SCALE_COORD_X:
                                                                                                                          CALL TFT_READ_TOUCH_Y
MOVW R20, R24; R21:R20 = R25:R24
                                                                                                                           CALL TFT_READ_TOUCH_Z
CPI_R24, 0x32
    PUSH R16
   PUSH R16
PUSH R17; x = x - 120
LDI R16, LOW(120)
LDI R17, HIGH(120)
SUB R24, R16
SBC R25, R17
                                                                                                                           {\tt BRSH} \ {\tt tft\_read\_touch\_valid}
                                                                                                                           CPI R22, 0x32
                                                                                                                           {\tt BRSH} \ {\tt tft\_read\_touch\_valid}
                                                                                                                           LDI R25, 0x0
                                                                                                                          STS TOUCH_Z_VALID, R25
RJMP tft_read_touch_exit
    ; \underline{x} = x * 5
   This can never go out of range LDI R16, 5
MUL R24, R16
MOV R24, R0
MOV R17, R1
                                                                                                                      tft_read_touch_valid:
                                                                                                                           LDI R25, 0xFF
STS TOUCH_Z_VALID, R25
   MUL R25, R16
MOV R25, R0
    ADD R25, R17; x = x / 16 = x >> 4
                                                                                                                           MOVW R24, R20; R25:R24 = R21:R20 CALL TFT_SCALE_COORD_Y
                                                                                                                          STS TOUCH_Y_HIGH, R25
STS TOUCH_Y_LOW, R24
    LSR R25
    ROR R24
    LSR R25
                                                                                                                          MOVW R24, R18; R25:R24 = R19:R18 CALL TFT_SCALE_COORD_X STS TOUCH_X_HIGH, R25 STS TOUCH_X_LOW, R24
    ROR R24
    LSR R25
   ROR R24
    LSR R25
    ROR R24
                                                                                                                       tft_read_touch_exit:
                                                                                                                          POP R25
POP R24
   POP R16
                                                                                                                           POP R23
   RET
                                                                                                                           POP R22
  Performs an scaling of the y coordinate returned from ANALOG_READ and maps it to an interval suited for the screen. The mapping range is empirical and based on tftpaint example. The mapping is:

[120, 940] ---> [0, 319]

Which can be done like this:

y' = (y - 120) * 319/820
                                                                                                                           POP R21
                                                                                                                           POP R20
                                                                                                                           POP R19
                                                                                                                          POP R18
                                                                                                                           RET
, which we have y' = (y - 120) * 319/820; The multiplication is approximated with 3/8.; The y and y' values are 16 bit numbers passed and; returned through registers R25:R24.

TFT_SCALE_COORD_Y:
                                                                                                                      DSEG
                                                                                                                       TOUCH_X_HIGH: BYTE 1
                                                                                                                       TOUCH_X_LOW: BYTE 1
                                                                                                                       TOUCH_Y_HIGH: BYTE 1
   PUSH R16
PUSH R17
                                                                                                                      TOUCH_Y_LOW: BYTE 1
TOUCH_Z_VALID: BYTE 1
   PUSH R17
; y = y - 120
LDI R16, LOW(120)
LDI R17, HIGH(120)
SUB R24, R16
SBC R25, R17
; y = y * 3
; This can never go out of range
                                                                                                                                                                       games.S
                                                                                                                          #include "randgen.S"
#include "simon.S"
                                                                                                                          #include "wires.S"
#include "memory.S"
#include "barcode.S"
    LDI R16, 3
   MUL R24, R16
MOV R24, R0
MOV R17, R1
MUL R25, R16
                                                                                                                           .GLOBAL PLAY_CURRENT_GAME
                                                                                                                           .GLOBAL SETUP_GAMES
.GLOBAL GET_STRIKES
.GLOBAL INC_STRIKES
    MOV R25, RO
```

```
.GLOBAL CHECK_STRIKES_REACHED_MAX .GLOBAL CHECK_BOMB_WAS_DEACTIVATED
                                                                                                                                                   games_starting_screen_text_loop:
  LDI R23, HIGH(SS_TEXT_Y)
  LDI R22, LOW(SS_TEXT_Y)
      .EQU GAMES_AMOUNT,
                                                                                                                                                          CLR R21
     .EQU MAX_STRIKES_AMOUNT, 3
.EQU STRIKES_LEDS_PORT, PORTG
                                                                                                                                                         LPM R20, Z+; Char in R20 \,
                                                                                                                                                        LPM R20, Z+; Char in R20
LDI R19, HIGH(SS_TEXT_COLOR)
LDI R18, LOW(SS_TEXT_COLOR)
LDI R17, HIGH(SS_FONT_SIZE)
LDI R16, LOW(SS_FONT_SIZE)
PUSH R25
PUSH R24
CALL TET DRAW CHAR
      .EQU STRIKES_LEDS_DDR, DDRG
     ; Starting screen constants
.EQU SS_BCK_COLOR, WHITE
.EQU SS_TEXT_COLOR, BLUE_LIGHT
.EQU SS_FONT_SIZE, 5
.EQU SS_CHAR_W, (SS_FONT_SIZE * TFT_CHAR_W)
.EQU SS_CHAR_H, (SS_FONT_SIZE * TFT_CHAR_H)
.EQU SS_TEXT_CHARS, 5; "START" is the text
.EQU SS_TEXT_Y, ((TFT_HEIGHT - SS_CHAR_H) / 2)
.EQU SS_TEXT_X, ((TFT_WIDTH - SS_CHAR_W *
                                                                                                                                                          CALL TFT_DRAW_CHAR
                                                                                                                                                         POP R24
POP R25
                                                                                                                                                         ADIW R24, SS_CHAR_W; R25:R24 += SS_CHAR_W DEC R15
                                                                                                                                                         BRNE games_starting_screen_text_loop
                                                                                                                                                    BRNE games_starting_screen_text_loop;
Text was shown, so loop until screen is touched
games_starting_screen_touch_loop:
CALL TFT_READ_TOUCH
LDS R18, TOUCH_Z_VALID
CPI R18, 0
BREQ games_starting_screen_touch_loop
CALL BUZZER_TONE_2
CALL BUZZER_TONE_2
CALL BUZZER_TONE_1
               SS_TEXT_CHARS) / 2)
    ; Defeat screen constants
.EQU DS_BCK_COLOR, RED_LIGHT
.EQU DS_TEXT_COLOR, WHITE
.EQU DS_FONT_SIZE, 6
.EQU DS_CHAR_W, (DS_FONT_SIZE * TFT_CHAR_W)
.EQU DS_CHAR_H, (DS_FONT_SIZE * TFT_CHAR_H)
.EQU DS_TEXT_CHARS, 5; "BOOM!" is the text
.EQU DS_TEXT_Y, ((TFT_HEIGHT - DS_CHAR_H) / 2)
.EQU DS_TEXT_X, ((TFT_WIDTH - DS_CHAR_W * DS_TEXT_CHARS) / 2)
                                                                                                                                                         CALL BUZZER_TONE_1
CALL BUZZER_TONE_1
CALL BUZZER_TONE_2
CALL BUZZER_TONE_2
                                                                                                                                                         POP R17
POP R16
POP R15
    ; Victory screen constants
.EQU VS_BCK_COLOR, GREEN_LIGHT
.EQU VS_TEXT_COLOR, WHITE
.EQU VS_FONT_SIZE, 4
.EQU VS_CHAR_W, (VS_FONT_SIZE * TFT_CHAR_W)
.EQU VS_CHAR_H, (VS_FONT_SIZE * TFT_CHAR_H)
.EQU VS_TEXT_CHARS, 8 ; "VICTORY!" is the text
.EQU VS_TEXT_Y, ((TFT_HEIGHT - VS_CHAR_H) / 2)
.EQU VS_TEXT_X, ((TFT_WIDTH - VS_CHAR_W * VS_TEXT_CHARS) / 2)
                                                                                                                                                         RET
                                                                                                                                                    ; Shows a the defeat screen with the message ; defined on the defeat_screen_text bufffer. The ; screen vanishes after the player touches it. ; IMPORTANT: This function is blocking! GAME_DEFEAT_SCREEN: DISH R17
                                                                                                                                                         PUSH R17
PUSH R16
                                                                                                                                                          PUSH R15
    CSEG
                                                                                                                                                          ; First fill the screen with the right color
                                                                                                                                                          LDI R25, HIGH(DS_BCK_COLOR)
                                                                                                                                                         LDI R24, LOW(DS_BCK_COLOR)
CALL TFT_FILL_SCREEN
; Boom explosion sound (?)
CALL BUZZER_EXPLOSION_FANFARE
; Initializes all minigames variables together with ; the strike counter. Should be called after ; setting up the random generator. SETUP_GAMES:
                                                                                                                                                        ; Now loop showing the defeat_screen_text
LDI ZL, LOW(defeat_screen_text)
LDI ZH, HIGH(defeat_screen_text)
LDI R16, DS_TEXT_CHARS
MOV R15, R16; R15 is the iterator
LDI R25, HIGH(DS_TEXT_X)
LDI R24, LOW(DS_TEXT_X)
     CALL SETUP_RANDGEN
     CLR R1
     STS strikes_counter, R1
    STS current_minigame, R1; Setup LEDs for strikes INPUT R18, STRIKES_LEDS_DDR ORI R18, 0X07
    ONT RIS, ONO?
OUTPUT STRIKES_LEDS_DDR, R18
INPUT R18, STRIKES_LEDS_PORT
ANDI R18, 0xF8
OUTPUT STRIKES_LEDS_PORT, R18
; Call setup for all minigames
CALL WIRES_SETUP
CALL MEMORY_SETUP
                                                                                                                                                    games_defeat_screen_text_loop:
  LDI R23, HIGH(DS_TEXT_Y)
  LDI R22, LOW(DS_TEXT_Y)
                                                                                                                                                          CLR R21
                                                                                                                                                         LPM R20, Z+; Char in R20
LDI R19, HIGH(DS_TEXT_COLOR)
LDI R18, LOW(DS_TEXT_COLOR)
LDI R17, HIGH(DS_FONT_SIZE)
     CALL SIMON_SAYS_SETUP
                                                                                                                                                         LDI R16, LOW(DS_FONT_SIZE)
PUSH R25
PUSH R24
     CALL BARCODE_SETUP
                                                                                                                                                          CALL TFT_DRAW_CHAR
    Shows a nice starting screen with the message
     defined on the starting_screen_text bufffer. The
                                                                                                                                                         POP R24
                                                                                                                                                         POP R25
ADIW R24, DS_CHAR_W; R25:R24 += DS_CHAR_W
     screen vanishes after the player touches it. IMPORTANT: This function is blocking!
GAME_STARTING_SCREEN:
                                                                                                                                                    BRNE games_defeat_screen_text_loop; Text was shown, so loop until screen is touched games_defeat_screen_touch_loop:
     PUSH R17
     PUSH R16
     PUSH R15
    ; First fill the screen with the right color LDI R25, HIGH(SS_BCK_COLOR)
LDI R24, LOW(SS_BCK_COLOR)
CALL TFT_FILL_SCREEN
; Now loop showing the starting_screen_text
LDI ZL, LOW(starting_screen_text)
LDI ZH, HIGH(starting_screen_text)
LDI R16 SS TEXT CHARS
                                                                                                                                                         CALL TFT_READ_TOUCH
LDS R18, TOUCH_Z_VALID
CPI R18, 0
                                                                                                                                                         BREQ games_defeat_screen_touch_loop
POP R17
POP R16
                                                                                                                                                         POP R15
    LDI R16, SS_TEXT_CHARS
MOV R15, R16; R15 is the iterator
LDI R25, HIGH(SS_TEXT_X)
LDI R24, LOW(SS_TEXT_X)
                                                                                                                                                         RET
                                                                                                                                                    ; Shows a the victory screen with the message
                                                                                                                                                    ; defined on the victory_screen_text bufffer. The
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; screen vanishes after the player touches it. ; IMPORTANT: This function is blocking! GAME_VICTORY_SCREEN:
                                                                                                       variable
                                                                                                 ; current_minigame reaches MINIGAMES_AMOUNT value.
; The function returns true or false (1 or 0) on R24
   PUSH R17
   PUSH R16
                                                                                                 CHECK_BOMB_WAS_DEACTIVATED:
                                                                                                    CLR R24
   PUSH R15
   ; First fill the screen with the right color LDI R25, HIGH(VS_BCK_COLOR)
LDI R24, LOW(VS_BCK_COLOR)
CALL TFT_FILL_SCREEN
; Victory sound!
                                                                                                    LDS R18, current_minigame CPI R18, GAMES_AMOUNT
                                                                                                    BRLO check_deactivated_ret
                                                                                                    INC R24
                                                                                                 check_deactivated_ret:
   CALL BUZZER_DEACTIVATION_FANFARE
   CALL BUZZER_DEACTIVATION_FANFARE;
Now loop showing the victory_screen_text
LDI ZL, LOW(victory_screen_text)
LDI ZH, HIGH(victory_screen_text)
LDI R16, VS_TEXT_CHARS
MOV R15, R16; R15 is the iterator
LDI R25, HIGH(VS_TEXT_X)
LDI R24, LOW(VS_TEXT_X)
                                                                                                 ; REALLY THE MOST IMPORTANT FUNCTION OF THE PROJECT!
                                                                                                 ; This function allows the user to play the current; minigame in a non-blocking manner, and advances to; the next minigame when it is finished by
                                                                                                        increasing
                                                                                                the current_minigame variable. In order for this, all minigames must implement these two functions: 

* <MINIGAME NAME>_PLAY: allows the user to play 
that minigame in a NON-BLOCKING way. The easiest 
way to achieve this is by simply asking if the 
player has touched the screen and returning if not
games_victory_screen_text_loop:
  LDI R23, HIGH(VS_TEXT_Y)
  LDI R22, LOW(VS_TEXT_Y)
   CLR R21
   LPM R20, Z+; Char in R20
LDI R19, HIGH(VS_TEXT_COLOR)
   LDI R18, LOW(VS_TEXT_COLOR)
LDI R17, HIGH(VS_FONT_SIZE)
LDI R16, LOW(VS_FONT_SIZE)
PUSH R25
                                                                                                 ; * <MINIGAME NAME>_FINISHED : must store on R24 a
                                                                                                 ; true or false value, telling if the player has or ; not cleared that game. 
 \label{eq:player} {\tt PLAY\_CURRENT\_GAME:}
                                                                                                    PUSH R16
   PUSH R24
                                                                                                    CLR R1; Load in R16 the current minigame number
   CALL TFT_DRAW_CHAR
   POP R24
POP R25
                                                                                                    LDS R16, current_minigame CPI R16, 0
   ADIW R24, VS_CHAR_W ; R25:R24 += VS_CHAR_W
         R.15
                                                                                                    BREQ minigame_0
                                                                                                    CPI R16, 1
   BRNE games_victory_screen_text_loop
; Text was shown, so loop until screen is touched games_victory_screen_touch_loop:
CALL TFT_READ_TOUCH
LDS_R18_TOUCH_Z_VALID
                                                                                                    BREQ minigame_1
                                                                                                    CPI R16, 2
                                                                                                    BREQ minigame_2
   LDS R18, TOUCH_Z_VALID CPI R18, 0
                                                                                                    CPI R16, 3
BREQ minigame_3
   BREQ games_victory_screen_touch_loop
                                                                                                       SHOULD NOT HAPPEN!
   POP R17
POP R16
   POP R15
                                                                                                 check_minigame_finished:
                                                                                                    ; R16 still has the current minigame. If R24 is CPI R24, 0; 1, the current minigame was cleared BREQ keep_playing_current_minigame; Current minigame was cleared if here
   RET
; Increases by 1 the number of strikes of the player
; during this game. INC_STRIKES:
                                                                                                     INC R16
   LDS R18, strikes_counter
INC R18
STS strikes_counter, R18
                                                                                                    STS current_minigame, R16 CALL BUZZER_VICTORY_FANFARE
                                                                                                 keep_playing_current_minigame:
    POP R16
    ; Turn on LEDs strikes
    NPUT R18, STRIKES_LEDS_PORT
   MOV R19, R18
ANDI R19, OXO7
                                                                                                 ; ----- Games sorted by number -----
   LSL R19
INC R19
                                                                                                 minigame_0:
                                                                                                    CALL SIMON_SAYS_PLAY
CALL SIMON_SAYS_FINISHED
   ANDI R19,
                    0X07
   OR R18, R19
OUTPUT STRIKES_LEDS_PORT, R18
                                                                                                    RJMP check_minigame_finished
   CALL BUZZER_DEFEAT_FANFARE
                                                                                                minigame_1:
    CALL BARCODE_PLAY
    CALL BARCODE_FINISHED
    RJMP check_minigame_finished
   Returns on R24 the number of strikes of this game.
GET_STRIKES:
   LDS R24, strikes_counter
   RET
                                                                                                 minigame_2:
                                                                                                    .nrgame_z.
CALL MEMORY_PLAY
CALL MEMORY_FINISHED
RJMP check_minigame_finished
; Checks whether or not the number of strikes has ; reached the MAX_STRIKES_AMOUNT value (i.e. if the ; game is over or not). Returns true or false (as 1 \,
   or 0) on R24.
                                                                                                 minigame_3:
                                                                                                    CALL WIRES_PLAY
CALL WIRES_FINISHED
RJMP check_minigame_finished
CHECK_STRIKES_REACHED_MAX:
   CLR R24
   LDS R18, strikes_counter CPI R18, MAX_STRIKES_AMOUNT
   BRLO check_strikes_ret
                                                                                                   Text to show on the starting screen
                                                                                                 starting_screen_text:
DB 'S', 'T', 'A', 'R', 'T', 0x00
   INC R24
check_strikes_ret:
                                                                                                   Text to show on the defeat screen
   RET
                                                                                                 defeat_screen_text:

DB 'B', 'O', 'O', 'M', '!', 0x00
; Text to show on the victory screen
; Checks if the bomb was deactivated by clearing all
```

; the minigames on it. That happens when the

```
victory_screen_text:
   DB 'V', 'I', 'C', 'T', 'O', 'R', 'Y', '!'
                                                                                    BUZZER TONE 2:
                                                                                       LDI R24, 200
                                                                                       LDI R22, 200
   DSEG
  Number of strikes during this game
                                                                                       LDI R20, 198
strikes_counter:
                                                                                       LDI R18,
   BYTE 1
                                                                                       CALL BUZZER_TONE_GENERIC
  Current game counter
current_minigame:
BYTE 1
                                                                                    BUZZER_TONE_3:
                                                                                       LDI R24, 180
LDI R22, 180
LDI R20, 110
                                  buzzer.S
                                                                                                    2
                                                                                       LDI R18,
   .GLOBAL SETUP_BUZZER
                                                                                       CALL BUZZER_TONE_GENERIC
   .GLOBAL RANDGEN
   .EQU BUZZER_PORT, PORTD
                                                                                    BUZZER_TONE_4:
   .EQU BUZZER_DDR, DDRD .EQU BUZZER_PIN_NUMBER, O
                                                                                       LDI R24, 160
LDI R22, 160
LDI R20, 123
LDI R18, 2
CSEG
                                                                                       CALL BUZZER_TONE_GENERIC
; Sets the buzzer pin as output and clears it. SETUP_BUZZER:
SBI BUZZER_DDR, BUZZER_PIN_NUMBER
CBI BUZZER_PORT, BUZZER_PIN_NUMBER
                                                                                    BUZZER_TONE_5:
                                                                                       LDI R24, 120
LDI R22, 120
                                                                                       LDI R20, 115
; Plays a tone on the buzzer for some time. The ; 'on' and 'off' times of the buzzer in microseconds ; are received on R24 and R22 respectively.
                                                                                       LDI R18,
                                                                                       CALL BUZZER_TONE_GENERIC
                                                                                       RET
BUZZER_PLAY:
   SBI BUZZER_PORT, BUZZER_PIN_NUMBER
                                                                                    BUZZER_TONE_6:
LDI R24, 80
LDI R22, 80
   MOV R25, R24
CALL HALF_MICRO_DELAY
   MOV R25, R24
CALL HALF_MICRO_DELAY
                                                                                       LDI R20, 165
LDI R18, 3
                                                                                       LDI R18,
  CALL HALF_MICRO_DELAY
CBI BUZZER_PORT, BUZZER_PIN_NUMBER
MOV R25, R22
CALL HALF_MICRO_DELAY
MOV R25, R22
                                                                                       CALL BUZZER_TONE_GENERIC
                                                                                    ; Plays a victory fanfare with the buzzer. BUZZER_VICTORY_FANFARE: CALL BUZZER_TONE_3
   CALL HALF_MICRO_DELAY
   RET
                                                                                       CALL BUZZER_TONE_3
CALL BUZZER_TONE_4
CALL BUZZER_TONE_4
CALL BUZZER_TONE_5
  Plays a tone on the buzzer that will last for (R24 + R22) * R20 * R18 microseconds. In the
  bottom, R24 and R22 will directly map to the 'on'
   and 'off' times of the buzzer, so they should be
BUZZER_TONE_GENERIC:
PUSH R13
                                                                                    ; Plays a defeat fanfare with the buzzer. \mathtt{BUZZER\_DEFEAT\_FANFARE}:
   PUSH R14
                                                                                       CALL BUZZER_TONE_2
CALL BUZZER_TONE_2
   PUSH R15
   PUSH R16
                                                                                       CALL BUZZER_TONE_2
CALL BUZZER_TONE_1
CALL BUZZER_TONE_1
   PUSH R17
  MOV R17, R24
MOV R16, R22
MOV R15, R20
MOV R14, R18
                                                                                       RET
                                                                                    BUZZER_DELAY:
PUSH ZL
PUSH ZH
buzzer_tone_loop_1:
  MOV R13, R15
buzzer_tone_loop_2:

MOV R24, R17

MOV R22, R16

CALL BUZZER_PLAY
                                                                                       PUSH R16
                                                                                       PUSH R17
PUSH R18
                                                                                       PUSH R19
   DEC R13
                                                                                       PUSH R20
   BRNE buzzer_tone_loop_2
                                                                                       LDI R16, 15
   DEC R14
                                                                                    buzzer_delay_loop:
CALL SHOW_DISPLAYS
   BRNE buzzer_tone_loop_1
   POP R17
                                                                                       DEC R16
BRNE buzzer_delay_loop
   POP R16
  POP R15
                                                                                       POP R20
POP R19
   POP R13
                                                                                       POP R18
   RET
                                                                                       POP R17
                                                                                       POP R16
; Buzzer tone functions to be called on minigames
                                                                                       POP ZH
                                                                                       POP ZL
BUZZER_TONE_1:
                                                                                       RET
   LDI R24, 220
LDI R22, 220
LDI R20, 180
                                                                                    BUZZER_EXPLOSION_FANFARE:
                                                                                       CALL BUZZER_TONE_2
CALL BUZZER_TONE_2
   LDI R18,
   CALL BUZZER_TONE_GENERIC
                                                                                       CALL BUZZER_TONE_2
```

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CALL BUZZER_TONE_2
CALL BUZZER_TONE_2
CALL BUZZER_DEFEAT_FANFARE
                                                                                            CLR R20
                                                                                            CLR R21
                                                                                            SBRS R18, 7
   CALL BUZZER_DELAY
CALL BUZZER_TONE_1
                                                                                            INC R20
                                                                                            SBRS R18, 6
   CALL BUZZER_TONE_1
                                                                                            INC R21
   CALL BUZZER_TONE_1
CALL BUZZER_TONE_2
CALL BUZZER_TONE_2
CALL BUZZER_TONE_2
CALL BUZZER_TONE_2
                                                                                            EOR R21, R20
                                                                                            I.SI. R.18
                                                                                            ADD R18, R21
                                                                                            INC R19
   CALL BUZZER_DELAY
                                                                                            CPI R19, RANDGEN_ITERATIONS
                                                                                            BRNE randgen_loop
   CALL BUZZER_TONE_2
   CALL BUZZER_TONE_2
   CALL BUZZER_TONE_2
CALL BUZZER_TONE_1
CALL BUZZER_TONE_1
                                                                                            STS randgen_buffer, R18
                                                                                            ; Since we must return a N bits random number ; we must do R18 % (2**N). Note this is the
                                                                                            ; same than taking the least N significative; bits from R18 register.
   CALL BUZZER_DEFEAT_FANFARE
                                                                                         mask_loop:
LSL R24
INC R24
BUZZER_DEACTIVATION_FANFARE:
   CALL BUZZER_TONE_3
CALL BUZZER_TONE_3
CALL BUZZER_TONE_3
                                                                                            DEC R22
                                                                                            BRNE mask_loop
   CALL BUZZER_TONE_3
                                                                                             ; R24 is now a mask of N bits
   CALL BUZZER_DELAY
CALL BUZZER_TONE_4
CALL BUZZER_TONE_4
CALL BUZZER_TONE_4
                                                                                             AND R24, R18
                                                                                         randgen_panic:
                                                                                            RET
   CALL BUZZER_DELAY
   CALL BUZZER_TONE_3
CALL BUZZER_TONE_3
                                                                                         ; Randgen buffer for generating random numbers
                                                                                         randgen_buffer:
   CALL BUZZER_DELAY
                                                                                            BYTE 1
   CALL BUZZER_TONE_5
CALL BUZZER_TONE_5
   CALL BUZZER_DELAY
                                                                                                                             simon.S
   CALL BUZZER_TONE_4
CALL BUZZER_TONE_4
                                                                                            .GLOBAL SIMON_SAYS_SETUP
.GLOBAL SIMON_SAYS_PLAY
.GLOBAL SIMON_SAYS_FINISHED
   CALL BUZZER_DELAY
                                                                                                2**SIMON_COLORS_BITS = SIMON_COLORS
   CALL BUZZER_VICTORY_FANFARE
                                                                                            .EQU SIMON_COLORS, 4
.EQU SIMON_COLORS_BITS, 2
.EQU SIMON_SAYS_ROUNDS, 5
                                  randgen.S
                                                                                            ; Screen constants for Simon says .EQU SIMON_RECTS_WIDTH, (TFT_WIDTH / 2) .EQU SIMON_RECTS_HEIGHT, (TFT_HEIGHT / 2)
   .GLOBAL SETUP_RANDGEN
    .GLOBAL RANDGEN
   .EQU RANDGEN_ITERATIONS, 5 .EQU RANDGEN_MAGIC, OxAA
                                                                                         CSEG
                                                                                         ; Configures the Simon says minigame by choosing the
                                                                                         ; first random color and storing it. Once the Simon ; says minigame is started, the color will be shown. SIMON_SAYS_SETUP:
CSEG
  Setups randgen_buffer variable for using RANDGEN. Basically, loads the RANDGEN_MAGIC value to the randgen_buffer (so it aint start with a O value).
                                                                                            CLR R1
                                                                                            STS simon_chosen_colors_amount, R1
STS simon_player_last_color, R1
STS simon_game_cleared, R1
CALL SIMON_SAYS_PICK_NEXT_COLOR
SETUP_RANDGEN:
LDI R18, RANDGEN_MAGIC
   STS randgen_buffer, R18
   RET
                                                                                            RET
; Returns a random N bits number on R24. The value ; of N is received on R24. It uses timer0 register \,
                                                                                         ; Picks the next correct color the player should ; guess, and stores it in the correct address of the ; simon_correct_colors buffer. It also sets the flag
  value as an entropy source, looping RANDGEN_ITERATIONS times performing some XORs. The
                                                                                           for showing the colors on the next iteration and
                                                                                         ; increases the chosen colors variable SIMON_SAYS_PICK_NEXT_COLOR:
   randgen_buffer is used for persisting the random
   values through calls.
                                                                                            ; If simon_chosen_colors_amount has already ; reached SIMON_SAYS_ROUNDS, then the game ; has been cleared
RANDGEN:
     IF N > 8 or N == 0, panic (return 0)
   ; IF N > 0 0
MOV R22, R24
                                                                                            LDS R18, simon_chosen_colors_amount CPI R18, SIMON_SAYS_ROUNDS
   CLR R24
   CPI R22, 8
   BRSH randgen_panic
                                                                                            BRNE simon_continue_choosing_colors
   CPI R22, \bar{\text{O}}
                                                                                            LDI R18, 1
   BREQ randgen_panic
                                                                                            STS simon_game_cleared, R18
     Load the randgen_buffer and do randomness
   LDS R18, randgen_buffer
   ; Load timer 0 value and XOR it with the
                                                                                         simon_continue_choosing_colors:
   ; previous randgen_buffer value INPUT R19, TCNT0 EOR R18, R19
                                                                                            LDI R24, SIMON_COLORS_BITS
CALL RANDGEN
; R24 has now a random color to store
                                                                                            CLR R1
                                                                                            LDS R18, simon_chosen_colors_amount
LDI ZL, LOW(simon_correct_colors)
LDI ZH, HIGH(simon_correct_colors)
      Now do RANDGEN_ITERATIONS XORs
   CLR R19
randgen_loop:
```

```
ADD ZL, R18
ADC ZH, R1
                                                                                                                                  CLR R24
                                                                                                                                  CLR R23
    ; The storing address for R24 is pointed by Z ST Z, R24 \,
                                                                                                                                  CLR R22
                                                                                                                                  RET
        Increase the chosen colors amount
                                                                                                                              simon_test_one:
    INC R18
                                                                                                                                  CPI R24, 1
   STS simon_chosen_colors_amount, R18
STS simon_player_last_color, R1
; Set the boolean for showing the colors
LDI R18, 1
                                                                                                                                  BRNE simon_test_two
LDI R25, HIGH(SIMON_RECTS_WIDTH)
LDI R24, LOW(SIMON_RECTS_WIDTH)
                                                                                                                                  CLR R23
    STS simon_show_colors, R18
                                                                                                                                  CLR R22
                                                                                                                                  RET
                                                                                                                              simon_test_two:
; Blink in white a corner of the Simon says.; Receives the (x,y) coordinates of the upper-left; button point on R25:R24 and R23:R22. SIMON_SAYS_BLINK_BUTTON:
                                                                                                                                   CPI R24, 2
                                                                                                                                  {\tt BRNE \ simon\_test\_three}
                                                                                                                                  CLR R25
                                                                                                                                  CLR R24
    PUSH R17
                                                                                                                                  LDI R23, HIGH(SIMON_RECTS_HEIGHT)
    PUSH R16
                                                                                                                                   LDI R22, LOW(SIMON_RECTS_HEIGHT)
    LDI R21, HIGH(SIMON_RECTS_WIDTH)
LDI R20, LOW(SIMON_RECTS_WIDTH)
LDI R19, HIGH(SIMON_RECTS_HEIGHT)
                                                                                                                                  RET
                                                                                                                             simon_test_three:
LDI R25, HIGH(SIMON_RECTS_WIDTH)
LDI R24, LOW(SIMON_RECTS_WIDTH)
LDI R23, HIGH(SIMON_RECTS_HEIGHT)
LDI R22, LOW(SIMON_RECTS_HEIGHT)
   LDI R19, HIGH(SIMON_RECTS_HEIGHT
LDI R18, LOW(SIMON_RECTS_HEIGHT)
LDI R17, HIGH(WHITE)
LDI R16, LOW(WHITE)
CALL TFT_FILL_RECT_HW
CALL SIMON_SAYS_DELAY
CALL SIMON_SAYS_SHOW_SCREEN
                                                                                                                              ; Delay used by the Simon says minigame after ; blinking a color.
    POP R17
                                                                                                                              SIMON_SAYS_DELAY:
                                                                                                                                  PUSH ZL
PUSH ZH
    RET
; Shows SIMON_COLORS coloured rectangles on screen. ; They should be arranged in a matrix according to ; SIMON_SCREEN_ROWS and SIMON_SCREEN_COLS. SIMON_SAYS_SHOW_SCREEN:
                                                                                                                                  PUSH R16
                                                                                                                                  PUSH R17
                                                                                                                                  PUSH R18
                                                                                                                                  PUSH R19
    PUSH R16
                                                                                                                                  PUSH R20
    PUSH R17
                                                                                                                                  LDI R16, 25
                                                                                                                              simon_says_delay_loop:
CALL SHOW_DISPLAYS
    CLR R24
   CALL SIMON_SAYS_MAP_BUTTON_TO_COORDS
LDI R21, HIGH(SIMON_RECTS_WIDTH)
LDI R20, LOW(SIMON_RECTS_WIDTH)
LDI R19, HIGH(SIMON_RECTS_HEIGHT)
LDI R18, LOW(SIMON_RECTS_HEIGHT)
                                                                                                                                  DEC R16
                                                                                                                                  BRNE simon_says_delay_loop
                                                                                                                                  POP R19
POP R18
POP R17
   LDI R17, HIGH(GREEN_DARK)
LDI R16, LOW(GREEN_DARK)
CALL TFT_FILL_RECT_HW
LDI R24, 1
                                                                                                                                  POP R16
POP ZH
    CALL SIMON_SAYS_MAP_BUTTON_TO_COORDS
                                                                                                                                  POP ZL
   CALL SIMON_SAYS_MAP_BUTTON_TO_COU
LDI R21, HIGH(SIMON_RECTS_WIDTH)
LDI R20, LOW(SIMON_RECTS_WIDTH)
LDI R19, HIGH(SIMON_RECTS_HEIGHT)
LDI R18, LOW(SIMON_RECTS_HEIGHT)
LDI R17, HIGH(RED_LIGHT)
LDI R16, LOW(RED_LIGHT)
CALL TFT_FILL_RECT_HW
LDI R24
                                                                                                                              ; Shows the correct colors chosen so far in sequence ; by highlighting them on the screen. 
 {\tt SIMON\_SAYS\_SHOW\_RIGHT\_COLORS:}
                                                                                                                                   PUSH R16
                                                                                                                                   CALL SIMON_SAYS_SHOW_SCREEN
    LDI R24,
                                                                                                                                   CALL SIMON_SAYS_DELAY
   LDI R24, 2
CALL SIMON_SAYS_MAP_BUTTON_TO_COORDS
LDI R21, HIGH(SIMON_RECTS_WIDTH)
LDI R20, LOW(SIMON_RECTS_WIDTH)
LDI R19, HIGH(SIMON_RECTS_HEIGHT)
LDI R18, LOW(SIMON_RECTS_HEIGHT)
LDI R17, HIGH(MAGENTA)
LDI R16, LOW(MAGENTA)
CALL TFT_FILL_RECT_HW
LDI R24, 3
                                                                                                                                  LDS R16, simon_chosen_colors_amount
LDI ZL, LOW(simon_correct_colors)
LDI ZH, HIGH(simon_correct_colors)
                                                                                                                              simon_show_colors_loop:
LD R24, Z+
                                                                                                                                   CALL SIMON_SAYS_PLAY_TONE
                                                                                                                                  CALL SIMON_SAYS_MAP_BUTTON_TO_COORDS CALL SIMON_SAYS_BLINK_BUTTON
                                                                                                                                  CALL SIMON_SAYS_DELAY
    LDI R24,
   LDI R24, 3
CALL SIMON_SAYS_MAP_BUTTON_TO_COORDS
LDI R21, HIGH(SIMON_RECTS_WIDTH)
LDI R20, LOW(SIMON_RECTS_WIDTH)
LDI R19, HIGH(SIMON_RECTS_HEIGHT)
LDI R18, LOW(SIMON_RECTS_HEIGHT)
LDI R17, HIGH(BLUE_DARK)
LDI R16, LOW(BLUE_DARK)
CALL TFT_FILL_RECT_HW
POP R17
                                                                                                                                  DEC R16
                                                                                                                                  BRNE simon_show_colors_loop
                                                                                                                                   ; Clear the show colors flag
CLR R1
                                                                                                                                  STS simon_show_colors, R1 POP R16
                                                                                                                              ; Retrieves the button number of a touched button; on screen by reading the TFT touched x,y values.; Note: this function assumes the player has indeed; touched the screen (i.e. you should validate the; z coordinate before calling this function).

Returns
    POP R17
POP R16
    RET
; Returns on R25:R24 the (x,y) coordinates of the ; upper-left point of a button. The button number is
                                                                                                                              ; the pressed button number on R24. SIMON_SAYS_MAP_TOUCH_TO_BUTTON:
    received on R24.
SIMON_SAYS_MAP_BUTTON_TO_COORDS:
    CPI R24, 0
BRNE simon_test_one
                                                                                                                                  CLR R24
LDS R19, TOUCH_Y_HIGH
LDS R18, TOUCH_Y_LOW
```

CLR R25

```
ROR R19
                                                                                                                                                                                    CALL SIMON_SAYS_PLAY_TONE
CALL SIMON_SAYS_MAP_BUTTON_TO_COORDS
CALL SIMON_SAYS_BLINK_BUTTON
      ROR R18
      CPI R18, (TFT_HEIGHT / 4)
      BRLO_simon_touch_test_x
      LDI R24, 2
                                                                                                                                                                                     POP R24
simon_touch_test_x:
                                                                                                                                                                                     CALL SIMON_SAYS_MAP_COLOR_BLINK_TO_TOUCH
     LDS R19, TOUCH_X_HIGH
LDS R18, TOUCH_X_LOW
                                                                                                                                                                                     CLR R1
                                                                                                                                                                                    LDS R19, simon_player_last_color
LDI ZL, LOW(simon_correct_colors)
LDI ZH, HIGH(simon_correct_colors)
ADD ZL, R19
      ROR R19
      ROR R18
      CPI R18, (TFT_WIDTH / 4)
                                                                                                                                                                                     ADC ZH, R1
      BRLO simon_touch_ret
      TNC R24
                                                                                                                                                                                    LD R18, Z
                                                                                                                                                                                     CP R18, R24
\verb|simon_touch_ret|:
                                                                                                                                                                                    BREQ simon_play_good_color_touched; If here, player has touched a bad color CALL INC_STRIKES
     RET
; Performs the magic conversion of blinked to ; touch button of the game by using the ; simon_touch_to_blink_table. The initial and final
                                                                                                                                                                                     CALL SIMON_SAYS_SETUP
                                                                                                                                                                                    RET
; color values are received/returned on R24.
SIMON_SAYS_MAP_COLOR_BLINK_TO_TOUCH:
MOV R18, R24
CALL_GET_STRIKES
                                                                                                                                                                              simon_play_good_color_touched:
  ; If here, player has touched a good color
  LDS R18, simon_chosen_colors_amount
                                                                                                                                                                                     LDS R19, simon_player_last_color
      LSL R24
                                                                                                                                                                                     INC R19
      LSL R24
                                                                                                                                                                                     CP R18, R19
     ADD R24, R18
LDI ZL, LOW(simon_touch_to_blink_table)
LDI ZH, HIGH(simon_touch_to_blink_table)
ADD ZL, R24
ADC ZH, R1
                                                                                                                                                                                    BREQ simon_play_pick_another_color STS simon_player_last_color, R19 CALL SIMON_SAYS_DELAY
      LPM R24, Z
                                                                                                                                                                               simon_play_pick_another_color:
    CALL SIMON_SAYS_PICK_NEXT_COLOR
      RET
                                                                                                                                                                                     RET
; Pretty function that plays a different buzzer tone ; according to the button touched. The button is ; received on R24. The function also returns the
                                                                                                                                                                               show_colors_and_exit:
                                                                                                                                                                                     CALL SIMON_SAYS_SHOW_RIGHT_COLORS
      same button number on R24.
                                                                                                                                                                               simon_play_no_button_touched:
SIMON_SAYS_PLAY_TONE:
     PUSH R24
                                                                                                                                                                              ; Returns true (as 1) on R24 if the simon says ; minigame was cleared by the player. If it hasn't ; already, returns false (as 0).
SIMON_SAYS_FINISHED:
      CPI R24, 0
      BREQ simon_tone_0
      CPI R24, 1
      BREQ simon_tone_1
      CPI R24, 2
                                                                                                                                                                                    LDS R24, simon_game_cleared
     BREQ simon_tone_2 CPI R24, 3
                                                                                                                                                                              ; Tables used for the magic mapping of this game {\tt simon\_touch\_to\_blink\_table}:
      BREQ simon_tone_3
simon_tone_0:
      CALL BUZZER_TONE_1
                                                                                                                                                                                         Cases when strikes are 0
                                                                                                                                                                                     DB 2, 3, 0, 1; Cases when strikes are 1
      POP R24
     RET
                                                                                                                                                                                    DB 3, 2, 1, 0; Cases when strikes are 2 DB 1, 3, 0, 2
simon_tone_1:
   CALL BUZZER_TONE_2
      POP R24
      RET
simon_tone_2:
CALL BUZZER_TONE_3
                                                                                                                                                                                      ; Buffer for storing the correct colors
      POP R24
                                                                                                                                                                              simon_correct_colors:
BYTE SIMON_SAYS_ROUNDS
      RET
                                                                                                                                                                                      ; Number of colors shown so far
simon_tone_3:
      CALL BUZZER_TONE_5
                                                                                                                                                                               simon_chosen_colors_amount:
      POP R24
                                                                                                                                                                                    BYTE 1
     RET
                                                                                                                                                                                     ; Last number of color pressed by player
                                                                                                                                                                               simon_player_last_color:
; Plays the simon says minigame in a non-blocking ; way by checking if the player has touched a button ; or not. Every time a player touches a button, the ; function determinates whether or not the button
                                                                                                                                                                                    BYTE 1
                                                                                                                                                                                     ; Boolean to determinate whether or not the ; correct colors should be shown on the
                                                                                                                                                                                         next minigame iteration
                                                                                                                                                                               simon_show_colors:
           was
; the correct one, and makes progress on the
                                                                                                                                                                                    BYTE 1
           minigame
                                                                                                                                                                                     ; Boolean to set when minigame finishes % \left( \frac{1}{2}\right) =\left( \frac{1}{2}\right) =
; by calling SIMON_SAYS_PICK_NEXT_COLOR if necessary
                                                                                                                                                                              simon_game_cleared:
   BYTE 1
SIMON_SAYS_PLAY:
            Check if the right colors should be shown
     LDS R18, simon_show_colors
CPI R18, 1
                                                                                                                                                                                                                                                  barcode.S
                                                                                                                                                                                     .GLOBAL BARCODE_SETUP
      BREQ show_colors_and_exit
                                                                                                                                                                                     .GLOBAL BARCODE_PLAY
       ; Now check if the user pressed a color
                                                                                                                                                                                     .GLOBAL BARCODE_FINISHED
       CALL TFT_READ_TOUCH
      LDS R18, TOUCH_Z_VALID CPI R18, 0
                                                                                                                                                                                     . EQU BARCODE BUTTONS . 4
     BREQ simon_play_no_button_touched; If here, the touchscreen was pressed CALL SIMON_SAYS_MAP_TOUCH_TO_BUTTON
                                                                                                                                                                                     ; 2**BARCODE_COMBS is the amount of possible
                                                                                                                                                                                            combinations for this minigame
```

.EQU BARCODE_COMBS, 4

```
; Returns true (as 1) on R24 if the barcode minigame ; was cleared by the player. If it hasn't already, returns false (as 0).
    ; Entry size in the barcode_combinations table .EQU BARCODE_COMB_SIZE, 2
                                                                                                    BARCODE_FINISHED:
                   screen constants
    EQU BARCODE_BORDER, 30
                                                                                                        LDS R24, barcode_game_cleared
    .EQU BARCODE_BARS_AMOUNT, 10
   .EQU BARCODE_BAR_H, ((TFT_HEIGHT - 3 *
BARCODE_BORDER) / BARCODE_BARS_AMOUNT)
                                                                                                    ; Retrieves the button number of a touched button ; on screen by reading the TFT touched x,y values. ; Note: this function assumes the player has indeed ; touched the screen (i.e. you should validate the ; z coordinate before calling this function).
CSEG
   Randomly picks one of the possible entries on the barcode_combinations table and stores that data on the barcode_bars_buffer variable.
                                                                                                          Returns
                                                                                                    ; the button number on R24. Stolen from Simon says. \texttt{BARCODE\_MAP\_TOUCH\_TO\_BUTTON}:
LDI R24, BARCODE_COMBS
CALL RANDGEN
                                                                                                        CLR R24
                                                                                                        LDS R19, TOUCH_Y_HIGH
LDS R18, TOUCH_Y_LOW
   ; R24 now has
                             a random number between 0 and
    ; 2**BARCODE_COMBS-1, so we use it to select
                                                                                                        ROR R19
   ; one entry in the barcode_combinations table LDI ZL, LOW(barcode_combinations)
LDI ZH, HIGH(barcode_combinations)
                                                                                                       ROR R18
CPI R18, (TFT_HEIGHT / 4)
                                                                                                        BRLO barcode_touch_test_x
   LDI R18, BARCODE_COMB_SIZE
MUL R24, R18
ADD ZL, R0
                                                                                                        LDI R24, 2
                                                                                                     barcode_touch_test_x:
                                                                                                        LDS R19, TOUCH_X_HIGH
   ADC ZH, R1
CLR R1
                                                                                                       LDS R18, TOUCH_X_LOW ROR R19
   ; Now that Z is pointing to some entry on ; the table, load that entry's values into ; the variables we will be using
                                                                                                        ROR R18
                                                                                                        CPI R18, (TFT_WIDTH / 4)
                                                                                                       BRLO barcode_touch_ret
   LDI XL, LOW(barcode_bars_buffer)
LDI XH, HIGH(barcode_bars_buffer)
                                                                                                        INC R24
                                                                                                    barcode_touch_ret:
   LDI R19, BARCODE_COMB_SIZE
                                                                                                       RET
barcode_setup_loop:
   LPM R18, Z
ST X+, R18
                                                                                                        Shows the barcode on screen
                                                                                                    BARCODE_SHOW_SCREEN:
   DEC R19
BRNE barcode_setup_loop
                                                                                                       PUSH R17
PUSH R16
                                                                                                       PUSH R15
      Tell the game to show the screen once
   ; left the game to bloom.

LDI R18, 1

STS barcode_need_show_screen, R18
; Finally, clear the game cleared flag

STS barcode_game_cleared, R1
                                                                                                       ; First fill screen with white LDI R25, HIGH(WHITE) LDI R24, LOW(WHITE)
                                                                                                        LDI R24, LOW(WHITE)
CALL TFT_FILL_SCREEN
                                                                                                        ; Draw two small squares at the bottom
                                                                                                        CLR R25
; Plays the barcode minigame in a non-blocking way
                                                                                                        CLR R24
                                                                                                       LDI R23, HIGH(TFT_HEIGHT - BARCODE_BORDER)
LDI R22, LOW(TFT_HEIGHT - BARCODE_BORDER)
LDI R21, HIGH(BARCODE_BORDER)
      bγ
  just asking if the screen was touched. Only when a the screen is touched, performs a check to see if it was on the correct place or not.
                                                                                                       LDI R20, LOW(BARCODE_BORDER)
LDI R19, HIGH(BARCODE_BORDER)
BARCODE_PLAY:
                                                                                                       LDI R18, LOW(BARCODE_BORDER)
LDI R17, HIGH(BLACK)
LDI R16, HIGH(BLACK)
   ; Check if the game needs to show the screen LDS R18, barcode_need_show_screen CPI R18, \ensuremath{\text{0}}
   BREQ barcode_check_button_touched
                                                                                                        CALL TFT_FILL_RECT_HW
                                                                                                       LDI R25, HIGH(TFT_WIDTH - BARCODE_BORDER)
LDI R24, LOW(TFT_WIDTH - BARCODE_BORDER)
LDI R23, HIGH(TFT_HEIGHT - BARCODE_BORDER)
LDI R22, LOW(TFT_HEIGHT - BARCODE_BORDER)
LDI R21, HIGH(BARCODE_BORDER)
LDI R20, LOW(BARCODE_BORDER)
   CALL BARCODE_SHOW_SCREEN
   CLR R1
   STS barcode_need_show_screen, R1
barcode_check_button_touched:
CALL TFT_READ_TOUCH
LDS R18, TOUCH_Z_VALID
CPI R18, 0
                                                                                                       LDI R19, HIGH(BARCODE_BORDER)
LDI R18, LOW(BARCODE_BORDER)
   BREQ barcode_no_button_touched
                                                                                                       LDI R17, HIGH(BLACK)
LDI R16, HIGH(BLACK)
CALL TFT_FILL_RECT_HW
; Point at the barcode_bars_buffer and prepare
   ; If here, the touchscreen was pressed CALL BARCODE_MAP_TOUCH_TO_BUTTON ; Check if player touched the proper button ; Point at the current stage correct button
                                                                                                       ; Point at the barcode_bars_buffer and ; to loop for BARCODE_BARS_AMOUNT times LDI ZL, LOW(barcode_bars_buffer) LDI ZH, HIGH(barcode_bars_buffer) LDI R16, BARCODE_BARS_AMOUNT LDI R22, LOW(BARCODE_BORDER) LDI R23, HIGH(BARCODE_BORDER)
   LDI ZL, LOW(barcode_bars_buffer +
          BARCODE_COMB_SIZE - 1)
   LDI ZH, HIGH(barcode_bars_buffer +
          BARCODE_COMB_SIZE - 1)
   LD R18, Z
   ANDI R18, OXOF; R18 is the correct button
   CP R18, R24
                                                                                                    barcode_show_screen_byte_loop:
   BREQ barcode_play_good_button_touched; If here, player has touched a bad button CALL INC_STRIKES
                                                                                                       CLR R17; R17 is a bit counter
LD R15, Z+
                                                                                                    barcode_show_screen_bit_loop:
   CPI R17, 8
   CALL BARCODE_SETUP
                                                                                                        BREQ barcode_show_screen_byte_loop; If the 7th bit of R15 is 1, show a black bar
   RET
barcode_play_good_button_touched:
   LDI R18, 1
STS barcode_game_cleared, R18
                                                                                                       SBRC R15, 7
RJMP bardcode_show_screen_bar
                                                                                                    bardcode_show_screen_next_bit:
LDI R25, HIGH(BARCODE_BAR_H)
LDI R24, LOW(BARCODE_BAR_H)
barcode_no_button_touched:
```

```
ADD R22, R24
ADC R23, R25
   LSL R15
    INC R17
                                                                                                             MEMORY_GIANT_NUMBER_H, (IFT_CHAR_H *

MEMORY_GIANT_NUMBER_SIZE)

.EQU MEMORY_GIANT_NUMBER_W, (TFT_CHAR_W *

MEMORY_GIANT_NUMBER_SIZE)

.EQU MEMORY_GIANT_NUMBER_X, ((TFT_WIDTH -

MEMORY_GIANT_NUMBER_W) / 2 - 2 *

MEMORY_BUTTON_BORDER)

.EQU MEMORY_GIANT_NUMBER_Y, ((TFT_HEIGHT -

MEMORY_GIANT_NUMBER_H) / 2)

.EQU MEMORY_BUTTON_FONT_SIZE, 6

.EQU MEMORY_BUTTON_FONT_SIZE * TFT_CHAR_W) / 2)

.EQU MEMORY_BUTTON_FONT_SIZE * TFT_CHAR_W) / 2)

.EQU MEMORY_BUTTON_FONT_SIZE * TFT_CHAR_H) / 2)

.EQU MEMORY_BUTTON_FONT_SIZE * TFT_CHAR_H) / 2)

.EQU MEMORY_STAGE_BAR_H, (MEMORY_GIANT_NUMBER_H /

(MEMORY_STAGE_BAR_W, MEMORY_BUTTON_BORDER

.EQU MEMORY_STAGE_BAR_X, (MEMORY_GIANT_NUMBER_X +

MEMORY_GIANT_NUMBER_H -

MEMORY_GIANT_NUMBER_H -

MEMORY_GIANT_NUMBER_H -

MEMORY_GIANT_NUMBER_SIZE))
   DEC R16
   BRNE barcode_show_screen_bit_loop
   POP R15
POP R16
   POP R17
   RET
bardcode_show_screen_bar:
      Show a black horizontal bar
   ; Show CLR R25
   CLR R24
   LDI R21, HIGH(TFT_WIDTH)
LDI R20, LOW(TFT_WIDTH)
    LDI R19, HIGH (BARCODE_BAR_H)
   LDI R18, LOW(BARCODE_BAR_H)
PUSH R17
PUSH R16
   PUSH R22
   PUSH R23
   LDI R17, HIGH(BLACK)
LDI R16, HIGH(BLACK)
                                                                                                                     MEMORY_GIANT_NUMBER_SIZE))
   CALL TFT_FILL_RECT_HW POP R23 POP R22
                                                                                                         CSEG
   POP R16
                                                                                                          ; Randomly picks one of the possible entries on the
                                                                                                          ; memory_combinations table and stores that data on
   POP R17
                                                                                                             the memory_stage_data buffer.
   RJMP bardcode_show_screen_next_bit
                                                                                                          MEMORY_SETUP:
                                                                                                             LDI R24, MEMORY_COMBS
CALL RANDGEN
; The following is a list of the barcode
       combinations
  (i.e. every possible scenario that could happen). The format for listing them is:
                                                                                                              ; R24 now has a random number between 0 and
                                                                                                             ; R24 now has a random number between 0 and

; 2**MEMORY_COMBS-1, so we use it to select

; one entry in the memory_combinations table

LDI ZL, LOW(memory_combinations)

LDI ZH, HIGH(memory_combinations)

LDI R18, MEMORY_COMB_SIZE

MUL R24, R18

ADD ZL, R0

ADC ZH, R1
; a stream of bits (1 for black bar, 0 for white one
   plus 4 bits for the right button to be touched.
barcode_combinations:
   DB 0x5A, 0x80
   DB OxAD, OX41
   DB 0xD5, 0X40
DB 0xB5, 0X43
                                                                                                             CLR R1
                                                                                                             ; Now that Z is pointing to some entry on ; the table, load that entry's values into ; the variables we will be using LDI XL, LOW(memory_stage_data) LDI XH, HIGH(memory_stage_data) LDI R19, MEMORY_COMB_SIZE
   DB 0xA6, 0xC0
DB 0xDA, 0x82
   DB OxAA, 0X80
DB OxB6, 0XC2
DB Ox5B, 0x40
   DB 0xD5, 0X03
   DB 0xAB, 0X42
                                                                                                         {\tt memory\_setup\_loop:}
   DB OxAA, OXC3
DB OxD6, Ox82
DB OxDA, OXC2
DB OxDB, OX42
                                                                                                             LPM R18, Z+
ST X+, R18
                                                                                                             DEC R19
                                                                                                             BRNE memory_setup_loop
                                                                                                             ; Tell the game to show the screen once LDI R18, 1
   DB 0x6A, 0x83
   DSEG
                                                                                                             STS memory_need_show_screen, R18
                                                                                                             ; Finally, clear the variables STS memory_current_stage, R1
   BYTE 1
    ; Buffer for allocating the bars data
barcode_bars_buffer:
                                                                                                             STS memory_game_cleared, R1
   BYTE BARCODE_COMB_SIZE
    ; Boolean to set when minigame finishes
                                                                                                         ; Plays the memory minigame in a non-blocking way by just asking if the screen was touched. Only when a ; a button is touched, performs a check to see ; if it was the correct button or not. The game then
barcode_game_cleared:
   BYTE 1
    ; Boolean to set when need to show screen
barcode_need_show_screen:
                                                                                                         ; proceeds for MEMORY_STAGES iterations, resetting; completely if a wrong button is touched.

MEMORY_PLAY:
   BYTE 1
                                                                                                             ; Check if the game needs to show the screen LDS R18, memory_need_show_screen CPI R18, 0
                                        memory.S
    .GLOBAL MEMORY_SETUP.GLOBAL MEMORY_PLAY
                                                                                                             BREQ memory_check_button_touched CALL MEMORY_SHOW_SCREEN
    .GLOBAL MEMORY_FINISHED
                                                                                                             CLR R1
    .EQU MEMORY_STAGES, 5
                                                                                                             STS memory_need_show_screen, R1
   .EQU MEMORY_BUTTONS, 4
                                                                                                         memory_check_button_touched:
    CALL TFT_READ_TOUCH
    LDS R18, TOUCH_Z_VALID
    CPI R18, 0
    ; 2**MEMORY_COMBS is the amount of possible
    ; combinations for this minigame .EQU MEMORY_COMBS, 4
                                                                                                             BREQ memory_no_button_touched; If here, the touchscreen was pressed CALL MEMORY_MAP_TOUCH_TO_BUTTON CALL MEMORY_PLAY_TONE
    ; Entry size in the memory_combinations table .EQU MEMORY_COMB_SIZE, ((MEMORY_BUTTONS + 2) *
           MEMORY_STAGES)
```

```
; Check if player touched the proper button ; Point at the current stage correct button LDS R18, memory_current_stage
                                                                                                      LDS R18, TOUCH_Y_LOW
                                                                                                      ROR R19
                                                                                                      ROR R18
    LDI ZL, LOW(memory_stage_data)
LDI ZH, HIGH(memory_stage_data)
LDI R19, (MEMORY_BUTTONS + 2)
                                                                                                      CPI R18, (TFT_HEIGHT / 4)
                                                                                                      BRLO memory_touch_test_x
                                                                                                      LDI R24, 2
   MUL R19, R18
ADD ZL, RO
ADC ZH, R1
                                                                                                  memory_touch_test_x:
LDS R19, TOUCH_X_HIGH
LDS R18, TOUCH_X_LOW
    CLR R1
                                                                                                      ROR R19
   ADD ZL, R18
ADC ZH, R1
                                                                                                      ROR R18
                                                                                                      CPI R18, (TFT_WIDTH / 4)
                                                                                                      BRLO memory_touch_ret
   LD R18, Z;
CP R18, R24
                                                                                                      INC R24
                        R18 is the correct button
                                                                                                  memory_touch_ret:
   BREQ memory_play_good_button_touched; If here, player has touched a bad button
    CALL INC_STRIKES
                                                                                                   ; Delay used by the memory minigame after showing
                                                                                                  , bully used by the memory miniga; the giant number to memoize it.

MEMORY_DELAY:

PUSH ZL

PUSH ZH
    CALL MEMORY_SETUP
    RET
memory_play_good_button_touched:
    ; If here, player has touched a good button
LDS R18, memory_current_stage
    INC R18
                                                                                                      PUSH R16
                                                                                                      PUSH R17
    CPI R18, MEMORY_STAGES
                                                                                                      PUSH R18
                                                                                                      PUSH R19
PUSH R20
    {\tt BREQ\ memory\_player\_cleared\_game}
   STS memory_current_stage, R18 LDI R18, 1
                                                                                                  LDI R16, 40
memory_delay_loop:
CALL SHOW_DISPLAYS
DEC R16
    STS memory_need_show_screen, R18
                                                                                                      BRNE memory_delay_loop
memory_player_cleared_game:
LDI R18, 1
STS memory_game_cleared, R18
memory_no_button_touched:
                                                                                                      POP R20
POP R19
                                                                                                      POP R18
                                                                                                      POP R17
                                                                                                      POP R16
; Returns true (as 1) on R24 if the memory minigame ; was cleared by the player. If it hasn't already, ; returns false (as 0). 
  \texttt{MEMORY\_FINISHED} : 
                                                                                                      POP ZH
                                                                                                      POP ZL
                                                                                                      RET
                                                                                                     Shows the current stage giant number and buttons labels on the screen in a pretty format.  
    LDS \overline{R}24, memory_game_cleared
                                                                                                   MEMORY_SHOW_SCREEN:
; Pretty function that plays a different buzzer tone ; according to the button touched. The button is ; received on R24. The function also returns the
                                                                                                      PUSH R17
PUSH R16
                                                                                                      PUSH R15
                                                                                                      ; Start by filling the screen with black LDI R25, HIGH(BLACK)
LDI R24, LOW(BLACK)
CALL TFT_FILL_SCREEN
    same button number on R24. Stolen from Simon says.
MEMORY_PLAY_TONE:
   PUSH R24
    CPI R24, 0
                                                                                                     ; Now point at the current stage data
LDS R18, memory_current_stage
LDI ZL, LOW(memory_stage_data)
LDI ZH, HIGH(memory_stage_data)
LDI R19, (MEMORY_BUTTONS + 2)
MUL R19, R18
   BREQ memory_tone_0 CPI R24, 1
    BREQ memory_tone_1
    CPI R24, 2
    BREQ memory_tone_2
                                                                                                      MUL R19, R
ADD ZL, RO
ADC ZH, R1
    CPI R24, 3
BREQ memory_tone_3
memory_tone_0:
CALL BUZZER_TONE_1
                                                                                                      CLR R1
                                                                                                      ; Load the first giant number and sh
LDI R25, HIGH(MEMORY_GIANT_NUMBER_X)
LDI R24, LOW(MEMORY_GIANT_NUMBER_X)
LDI R23, HIGH(MEMORY_GIANT_NUMBER_Y)
LDI R22, LOW(MEMORY_GIANT_NUMBER_Y)
    POP R24
                                                                                                                                                            and show it
    RET
memory_tone_1:
   CALL BUZZER_TONE_2
POP R24
   RET
                                                                                                      CLR R21
memory_tone_2:
                                                                                                      LD R20, Z+
    CALL BUZZER_TONE_3
                                                                                                      LDI R26, 48
    POP R24
                                                                                                      ADD R20, R26; number to ASCII
   RET
                                                                                                      CLR R21
                                                                                                      LDI R19, HIGH(WHITE)
LDI R18, LOW(WHITE)
memory_tone_3:
    CALL BUZZER_TONE_5
    POP R24
                                                                                                      CLR R17
                                                                                                      LDI R16, MEMORY_GIANT_NUMBER_SIZE
                                                                                                      PUSH ZL
; Retrieves the button number of a touched button ; on screen by reading the TFT touched x,y values.; Note: this function assumes the player has indeed ; touched the screen (i.e. you should validate the
                                                                                                      PUSH ZH
                                                                                                      CALL TFT_DRAW_CHAR
                                                                                                      ; Draw the current stage progress bar LDS R15, memory_current_stage
  z coordinate before calling this function).
                                                                                                      INC R15
                                                                                                      LDI R23, HIGH(MEMORY_STAGE_BAR_Y)
LDI R22, LOW(MEMORY_STAGE_BAR_Y)
; the button number on R24. Stolen from Simon says. \tt MEMORY\_MAP\_TOUCH\_TO\_BUTTON: CLR R24
                                                                                                  memory_show_stage_progress_bar_loop:
LDI R25, HIGH(MEMORY_STAGE_BAR_X)
LDI R24, LOW(MEMORY_STAGE_BAR_X)
```

LDS R19, TOUCH_Y_HIGH

```
LDI R21, HIGH(MEMORY_STAGE_BAR_W)
LDI R20, LOW(MEMORY_STAGE_BAR_W)
LDI R19, HIGH(MEMORY_STAGE_BAR_H)
                                                                                       LDI R25, HIGH(MEMORY_BUTTON_NUMBER_X)
LDI R24, LOW(MEMORY_BUTTON_NUMBER_X)
LDI R23, HIGH(MEMORY_BUTTON_NUMBER_Y + TFT_HEIGHT
LDI R18, LOW(MEMORY_STAGE_BAR_H)
LDI R17, HIGH(GREEN_LIGHT)
LDI R16, LOW(GREEN_LIGHT)
                                                                                        LDI R22, LOW(MEMORY_BUTTON_NUMBER_Y + TFT_HEIGHT /
                                                                                                2)
                                                                                        CLR R21
PUSH R23
                                                                                        LD R20, Z+
LDI R26, 48
PUSH R22
CALL TFT_FILL_RECT_HW
POP R22
                                                                                        ADD R20, R26; number to ASCII
POP R23
                                                                                        CLR R21
LDI R19, HIGH(MEMORY_STAGE_BAR_H + 4)
                                                                                        LDI R19, HIGH(WHITE)
LDI R18, LOW(MEMORY_STAGE_BAR_H + 4)
                                                                                        LDI R18, LOW(WHITE)
SUB R22, R18
SBC R23, R19
DEC R15
                                                                                        CLR R17
                                                                                        LDI R16, MEMORY_BUTTON_FONT_SIZE
                                                                                        PUSH ZL
BRNE memory_show_stage_progress_bar_loop
                                                                                        PUSH ZH
CALL MEMORY_DELAY
                                                                                        CALL
                                                                                                TFT_DRAW_CHAR
; Clear the screen and draw the button borders LDI R25, HIGH(BLACK)
LDI R24, LOW(BLACK)
CALL TFT_FILL_SCREEN
                                                                                        POP ZH
POP ZL
                                                                                        LDI R25, HIGH(MEMORY_BUTTON_NUMBER_X + TFT_WIDTH /
CLR R25 ; Horizontal line
                                                                                        LDI R24, LOW(MEMORY_BUTTON_NUMBER_X + TFT_WIDTH /
CLR R24
LDI R23, HIGH(TFT_HEIGHT / 2)
                                                                                        LDI R23, HIGH(MEMORY_BUTTON_NUMBER_Y + TFT_HEIGHT
LDI R22, LOW(TFT_HEIGHT / 2)
LDI R21, HIGH(TFT_WIDTH)
LDI R20, LOW(TFT_WIDTH)
                                                                                                 2)
                                                                                        LDI R22, LOW(MEMORY_BUTTON_NUMBER_Y + TFT_HEIGHT /
LDI R20,
                                                                                                2)
LDI R19, HIGH(MEMORY_BUTTON_BORDER)
                                                                                        CLR R21
LDI R18, LOW(MEMORY_BUTTON_BORDER)
                                                                                        LD R20, Z+
                                                                                        LDI R26, 48
ADD R20, R26; number to ASCII
LDI R17, HIGH(WHITE)
LDI R16, LOW(WHITE)
CALL TFT_FILL_RECT_HW
                                                                                        CLR R21
                                                                                        LDI R19, HIGH(WHITE)
CLR R23 ; Vertical line
CLR R22
                                                                                        LDI R18, LOW(WHITE)
LDI R25, HIGH(TFT_WIDTH / 2)
                                                                                        CLR R17
LDI R24, LOW(TFT_WIDTH /
LDI R19, HIGH(TFT_HEIGHT)
                                        2)
                                                                                        LDI R16,
                                                                                                      MEMORY_BUTTON_FONT_SIZE
                                                                                        CALL TFT_DRAW_CHAR
LDI R18, LOW(TFT_HEIGHT)
                                                                                        POP R15
LDI R21, HIGH (MEMORY_BUTTON_BORDER)
                                                                                        POP R16
LDI R20, LOW(MEMORY_BUTTON_BORDER)
                                                                                        POP R17
LDI R17, HIGH(WHITE)
                                                                                        RET
LDI R16, LOW(WHITE)
CALL TFT_FILL_RECT_HW
                                                                                     ; The following is a list of the memory combinations
                                                                                     ; (i.e. every possible scenario that could happen).; The format for listing them is: ; <stage 1>, <stage 2>, ..., <stage MEMORY_STAGES>
  Now draw the four button labels
POP ZL
                                                                                     ; <stage 1>, <stage 2>, ..., <stage MEMORY_STAGES>; Where each stage is made of MEMORY_BUTTONS+2 bytes
LDI R25, HIGH(MEMORY_BUTTON_NUMBER_X)
LDI R24, LOW(MEMORY_BUTTON_NUMBER_X)
LDI R23, HIGH(MEMORY_BUTTON_NUMBER_Y)
                                                                                     ; - The giant initial number on screen ; - The MEMORY_BUTTONS labels for the buttons
LDI R22, LOW(MEMORY_BUTTON_NUMBER_Y)
                                                                                       - The right button (from 0 to MEMORY_BUTTONS - 1)
                                                                                    CLR R21
LD R20, Z+
LDI R26, 48
ADD R20, R26; number to ASCII
CLR R21
LDI R19, HIGH(WHITE)
LDI R18, LOW(WHITE)
CLR R17
LDI R16, MEMORY_BUTTON_FONT_SIZE
PUSH ZL
PUSH ZH
CALL TFT_DRAW_CHAR
POP ZH
POP ZL
LDI R25, HIGH(MEMORY_BUTTON_NUMBER_X + TFT_WIDTH /
                                                                                        1, 3, 4, 2, 1, 4, 3, 0, 2, 3, 4, 1, 2, 2
DB 3, 4, 1, 2, 3, 2, 2, 1, 2, 3, 4, 2, 3, 2,
1, 2, 2, 1, 2, 3, 4, 0, 3, 4, 2, 1, 3, 2
DB 1, 1, 4, 3, 2, 1, 4, 2, 3, 1, 4, 1, 3, 1, 3
3, 2, 2, 2, 3, 4, 1, 0, 3, 4, 1, 2, 3, 2
DB 2, 3, 1, 4, 2, 1, 4, 4, 3, 2, 1, 1, 2, 2, 3
4, 2, 2, 1, 4, 3, 2, 0, 4, 2, 4, 3, 1, 3
LDI R24, LOW(MEMORY_BUTTON_NUMBER_X + TFT_WIDTH /
      2)
LDI R23, HIGH(MEMORY_BUTTON_NUMBER_Y)
                                                                                                                                                        3, 1, 2, 4,
                                                                                                                                                          3,
2,
3
LDI R22, LOW(MEMORY_BUTTON_NUMBER_Y) CLR R21
                                                                                                                                                                  3. 1.
LD R20, Z+
                                                                                       4, 2, 2, 1, 4, 3, 2, 0, 4, 2, 4, 3, 1, 3

DB 4, 2, 1, 3, 4, 3, 3, 4, 1, 2, 3, 0, 1, 2, 3, 4,

1, 2, 3, 2, 3, 4, 1, 0, 3, 2, 4, 3, 1, 0

DB 3, 2, 4, 1, 2, 2, 4, 3, 2, 1, 4, 2, 4, 2, 3, 4,

1, 2, 4, 1, 4, 3, 2, 2, 1, 1, 4, 2, 3, 0

DB 1, 3, 2, 1, 4, 1, 1, 2, 3, 4, 1, 2, 4, 3, 4, 1,

2, 1, 1, 2, 1, 3, 4, 1, 4, 3, 2, 1, 4, 3

DB 2, 3, 4, 1, 2, 1, 1, 3, 4, 1, 2, 1, 4, 2, 3, 4,

1, 2, 1, 2, 4, 3, 1, 1, 1, 2, 3, 4, 1, 2

DB 3, 4, 3, 2, 1, 2, 3, 4, 1, 2, 3, 4, 1, 2

DB 3, 4, 3, 2, 1, 2, 3, 4, 1, 2, 3, 0, 1, 1, 4, 3,

2, 1, 3, 1, 2, 4, 3, 0, 2, 1, 4, 3, 2, 1

DB 4, 2, 4, 3, 1, 3, 4, 2, 1, 3, 4, 3, 4, 2, 2
LDI R26, 48
ADD R20, R26; number to ASCII
CLR R21
LDI R19, HIGH(WHITE)
LDI R18, LOW(WHITE)
CLR R17
LDI R16, MEMORY_BUTTON_FONT_SIZE
PUSH ZL
PUSH ZH
\begin{array}{cccc} \mathtt{CALL} & \mathtt{TFT\_DRAW\_CHAR} \\ \mathtt{POP} & \mathtt{ZH} \end{array}
POP ZL
```

```
wires_copy_rects:
LPM R18. Z+
  DSEG
  ; Current minigame stage (0 to MEMORY_STAGES - 1)
                                                                                      ST X+, R18
                                                                                      INC R24
memory_current_stage:
    BYTE 1
                                                                                      CPI R24, (SCREEN_RECTS_AMOUNT * 2)
                                                                                      BRNE wires_copy_rects
   ; Buffer for allocating all of the stage data
memory_stage_data:
BYTE MEMORY_COMB_SIZE
                                                                                       Tell the game to show the screen once
                                                                                     ; lerr ...
LDI R18, 1
   ; Boolean to set when minigame finishes
                                                                                     STS wires_need_show_screen, R18
memory_game_cleared:
BYTE 1
                                                                                       Finally, clear the wires_game_cleared var
                                                                                      STS wires_game_cleared, R1
   ; Boolean to set when need to show screen
memory_need_show_screen:
  BYTE 1
                                                                                   ; Plays the wires minigame in a non-blocking way by
                                                                                   ; just asking if a wire has been cut. Only when one ; has, performs a check to determinate if it was the ; correct wire or not. Detonates the bomb or clears
                                  wires.S
                                                                                      the minigame accordingly on the result.
   .GLOBAL WIRES_SETUP
.GLOBAL WIRES_PLAY
.GLOBAL WIRES_FINISHED
                                                                                   WIRES PLAY
                                                                                     CALL WIRES_SHOW_SCREEN
   .EQU WIRES_PORT, PORTK .EQU WIRES_DDR, DDRK .EQU WIRES_PIN, PINK
                                                                                      CLR R1
                                                                                      STS wires_need_show_screen, R1
   .EQU WIRES_AMOUNT, 4
                                                                                  wires_check_wires_present:
    ; Check if all wires are still present
    ; (i.e. their value in WIRES_PORT is 0)
   ; 2**WIRES_COMBS is the amount of possible
      combinations for this minigame
                                                                                     INPUT R18, WIRES_PIN
LDI R19, ((1 << WIRES_AMOUNT) - 1)
   .EQU WIRES_COMBS, 4
.EQU WIRES_COMB_SIZE, 10; Each entry in the; wires_combinations table has this size
                                                                                       R19 has a mask for taking the wires bits
                                                                                     AND R18, R19
CPI R18, 0
BREQ no_wire_cut
; If here, at least one wire has been cut.
; Load now in R20 the correct wire
      Wires screen constants
   .EQU SCREEN_RECTS_AMOUNT, 4
   EQU WIRES_FONT_SIZE, 10

EQU WIRES_RECTS_WIDTH, (TFT_WIDTH /
SCREEN_RECTS_AMOUNT)

EQU WIRES_RECTS_HEIGHT, 60

EQU WIRES_NUMBER_H, (TFT_CHAR_H * WIRES_FONT_SIZE
                                                                                      LDS R20, correct_wire; Since the correct wire bit should be in 1; and the others in 0, make R19 like that
                                                                                  LDI R19, 0x01
wires_small_loop:
CPI R20, 0
   .EQU WIRES_NUMBER_W, (TFT_CHAR_W * WIRES_FONT_SIZE
                                                                                      BREQ wires_comp_wires
   .EQU WIRES_NUMBER_X, ((TFT_WIDTH - WIRES_NUMBER_W)
                                                                                     LSL R19
DEC R20
   .EQU WIRES_NUMBER_Y, (WIRES_RECTS_HEIGHT + (
                                                                                      RJMP wires_small_loop
         TFT_HEIGHT - WIRES_RECTS_HEIGHT - WIRES_NUMBER_H)/ 2)
                                                                                   wires_comp_wires:
                                                                                      ; R19 has now a 1 in the correct_wire-th bit
                                                                                      ; So if R18 != R19 then a wrong wire was cut
CP R18, R19
   .EQU WIRES_RECTS_BORDER, 2
                                                                                     BREQ correct_wire_cut; If here, a wrong wire has been cut! The; game should end and bomb should explode!
LDI R18, MAX_STRIKES_AMOUNT
  Configures the WIRES_PORT as input and enables its
  pull ups. It also setups the variables for the minigame by calling RANDGEN.
                                                                                      ; Call INC_STRIKES MAX_STRIKES_AMOUNT times
WIRES_SETUP:
                                                                                       to force the game to be over
   ; First just configure the port CLR R18
                                                                                   wires_strikes_loop:
                                                                                     PUSH R18
   OUTPUT WIRES_DDR, R18
                                                                                      CALL INC_STRIKES
   LDI R18, 0xFF
                                                                                     POP R18
DEC R18
   OUTPUT WIRES_PORT, R18
   ; We must now select some random variables ; values using the wires_combinations table LDI R24, WIRES_COMBS
                                                                                      BRNE wires_strikes_loop
                                                                                     RET
                                                                                   correct_wire_cut:
   CALL RANDGEN
                                                                                     ; If here, the right wire has been cut! It's ; a happy day and everybody was saved! LDI R18, 1; Make wires_game_cleared = true
   ; R24 now has a random number between 0 and
  ; 2**WIRES_COMBS-1, so we use it to select; one entry in the wires_combinations table LDI ZL, LOW(wires_combinations)
LDI ZH, HIGH(wires_combinations)
                                                                                     STS wires_game_cleared, R18
                                                                                   no_wire_cut:
  LDI R18, WIRES_COMB_SIZE
MUL R24, R18
ADD ZL, R0
ADC ZH, R1
CLR R1
                                                                                     RET
                                                                                  ; Returns true (as 1) on R24 if the wires minigame; was cleared by the player. If it hasn't already,; returns false (as 0).
WIRES_FINISHED:
LDS R24, wires_game_cleared
   ; Now that Z is pointing to some entry on ; the table, load that entry's values into
     the variables we will be using
   LPM R18, Z+
                                                                                  ; Shows the number and rectangles on screen.
   STS correct_wire, R18 LPM R18, Z+
                                                                                   ; the wires minigame doesn't use the touchability of
                                                                                  ; the screen, this function only draws. WIRES_SHOW_SCREEN: PUSH R17
   STS wires_screen_number, R18
   LDI XL, LOW(wires_screen_rects)
LDI XH, HIGH(wires_screen_rects)
                                                                                     PUSH R16
   CLR R24
```

```
; Start by filling the screen with black LDI R25, HIGH(BLACK)
   LDI R24, LOW(BLACK)
CALL TFT_FILL_SCREEN
    ; Draw the wires_number on its position
   LDI R25, HIGH(WIRES_NUMBER_X)
LDI R24, LOW(WIRES_NUMBER_X)
LDI R23, HIGH(WIRES_NUMBER_Y)
LDI R22, LOW(WIRES_NUMBER_Y)
    CLR R21
   LDS R20, wires_screen_number
   ADD R20, R26; number to ASCII
                                                                                                                  ; Cases when wires_screen_number = 3
DB 3, 3, HIGH(WHITE), LOW(WHITE), HIGH(BLUE_DARK),
LOW(BLUE_DARK), HIGH(GREEN_DARK), LOW(
GREEN_DARK), HIGH(BLUE_DARK), LOW(BLUE_DARK)
DB 0, 3, HIGH(GREEN_DARK), LOW(GREEN_DARK), HIGH(
BLUE_DARK), LOW(BLUE_DARK), HIGH(GREEN_DARK),
LOW(GREEN_DARK), HIGH(RED_LIGHT), LOW(
DED_LIGHT)
   CLR R21
   LDI R19, HIGH(WHITE)
LDI R18, LOW(WHITE)
    CLR R17
   LDI R16, WIRES_FONT_SIZE
CALL TFT_DRAW_CHAR
; Iteratively draw the wires rects
CLR R15; R15 is the index i for iteration
                                                                                                                           RED_LIGHT)
                                                                                                                  RED_LIGHT)

DB 2, 3, HIGH(WHITE), LOW(WHITE), HIGH(WHITE), LOW

(WHITE), HIGH(GREEN_DARK), LOW(GREEN_DARK),

HIGH(RED_LIGHT), LOW(RED_LIGHT)

DB 0, 3, HIGH(WHITE), LOW(WHITE), HIGH(GREEN_DARK),

LOW(GREEN_DARK), HIGH(BLUE_DARK), LOW(

BLUE_DARK), HIGH(RED_LIGHT), LOW(RED_LIGHT)

; Cases when wires_screen_number = 4
   LDI ZL, LOW(wires_screen_rects)
LDI ZH, HIGH(wires_screen_rects)
wires_show_rects:
   ; Load this rect color and fill the args CLR R18 \, CLR R25 \,
                                                                                                                  ; Cases when wires_screen_number = 4
DB 2, 4, HIGH(BLUE_DARK), LOW(BLUE_DARK), HIGH(
    RED_LIGHT), LOW(RED_LIGHT), HIGH(WHITE), LOW(
    WHITE), HIGH(RED_LIGHT), LOW(RED_LIGHT)

DB 3, 4, HIGH(BLUE_DARK), LOW(BLUE_DARK), HIGH(
    GREEN_DARK), LOW(GREEN_DARK), HIGH(WHITE), LOW
    (WHITE), HIGH(RED_LIGHT), LOW(RED_LIGHT)

DB 1, 4, HIGH(WHITE), LOW(WHITE), HIGH(BLUE_DARK),
    LOW(BLUE_DARK), HIGH(GREEN_DARK), LOW(
    GREEN_DARK), HIGH(GREEN_DARK), LOW(GREEN_DARK)

DB WIRES_AMOUNT-1, 4, HIGH(WHITE), LOW(WHITE),
    HIGH(RED_LIGHT), LOW(RED_LIGHT), HIGH(WHITE),
    LOW(WHITE), HIGH(GREEN_DARK), LOW(GREEN_DARK)
   LDI R23, HIGH(WIRES_RECTS_WIDTH)
LDI R22, LOW(WIRES_RECTS_WIDTH)
; Do rect.x = WIRES_RECT_WIDTH * i
wires_rectx_loop:
   CP R18, R15
BREQ wires_rectx_loop_end
    ADD R24, R22
   ADC R25, R23
INC R18
RJMP wires_rectx_loop
wires_rectx_loop_end:
CLR R23
                                                                                                                           LOW(WHITE), HIGH(GREEN_DARK), LOW(GREEN_DARK)
    CLR R22
   LDI R21, HIGH(WIRES_RECTS_WIDTH -
   WIRES_RECTS_BORDER)
LDI R20, LOW(WIRES_RECTS_WIDTH - WIRES_RECTS_BORDER)
                                                                                                                   ; Correct wire number (should be in the
                                                                                                                      range from 0 to WIRES_AMOUNT - 1)
                                                                                                               correct_wire:
BYTE 1
   LDI R19, HIGH(WIRES_RECTS_HEIGHT - WIRES_RECTS_BORDER)
                                                                                                                   ; Number to be shown on the screen
   LDI R18, LOW(WIRES_RECTS_HEIGHT -
                                                                                                                wires_screen_number:
           WIRES_RECTS_BORDER)
                                                                                                                   BYTE 1
   LD R17, Z+
                                                                                                                    ; Rectangles colors to appear on screen
   LD R16, Z+
CALL TFT_FILL_RECT_HW
INC R15
                                                                                                               wires_screen_rects:
BYTE (WIRES_COMB_SIZE - 2)
; Boolean to set when minigame finishes
   MOV R22, R15
CPI R22, SCREEN_RECTS_AMOUNT
                                                                                                                wires_game_cleared:
                                                                                                                   BYTE
   BRNE wires_show_rects
                                                                                                                    ; Boolean to set when need to show screen
                                                                                                               wires_need_show_screen:
   POP R15
   POP R16
   POP R17
                                                                                                                                                    tft-registers.S
   The following is a list of the wires combinations (i.e. every possible scenario that could happen). The format for listing them is:
DB: <wire>, <screen number>, <screen rects..>
Note there should be 2**WIRES_COMBS here.
                                                                                                                            KEEP CODING AND NOBODY EXPLODES (KCANE)
                                                                                                                  Project for 66.09. Laboratorio de Microcomputadoras
                                                                                                                ; Faculty of Engineering, University of Buenos Aires
wires_combinations:
   By Ana Czarnitzki, Alejandro García & Juan Fresia
                                                                                                                                         TFT GENERAL DEFINITIONS
               LOW (WHITE)
                                                                                                                   .EQU TFT_WIDTH, 240
.EQU TFT_HEIGHT, 320
   DB WIRES_AMOUNT-1, 1, HIGH(BLUE_DARK), LOW(
BLUE_DARK), HIGH(RED_LIGHT), LOW(RED_LIGHT),
HIGH(BLUE_DARK), LOW(BLUE_DARK), HIGH(
GREEN_DARK), LOW(GREEN_DARK)
                                                                                                                   .EQU IL19341_SOFTRESET, 0x01
.EQU IL19341_SLEEPIN, 0x10
.EQU IL19341_SLEEPOUT, 0x11
```

EQU ILI9341_NORMALDISP, 0x13
.EQU ILI9341_INVERTOFF, 0x20
.EQU ILI9341_INVERTON, 0x21

DB 1, 1, HIGH(RED_LIGHT), LOW(RED_LIGHT), HIGH(
BLUE_DARK), LOW(BLUE_DARK), HIGH(RED_LIGHT),
LOW(RED_LIGHT), HIGH(GREEN_DARK), LOW(

GREEN_DARK)

```
EQU ILI9341_GAMMASET, 0x26
EQU ILI9341_DISPLAYOFF, 0x28
EQU ILI9341_DISPLAYON, 0x29
EQU ILI9341_COLADDRSET, 0x2A
EQU ILI9341_PAGEADDRSET, 0x2B
EQU ILI9341_MEMORYWRITE, 0x2C
                                                                                                       POP R16
                                                                                                       RET
                                                                                                                                        tft-writing.S
   .EQU ILI9341_MEMORYWRITE, 0x2C
.EQU ILI9341_PIXELFORMAT, 0x3A
.EQU ILI9341_FRAMECONTROL, 0xB1
.EQU ILI9341_DISPLAYFUNC, 0xB6
.EQU ILI9341_ENTRYMODE, 0xB7
.EQU ILI9341_POWERCONTROL1, 0xC0
.EQU ILI9341_POWERCONTROL2, 0xC1
.EQU ILI9341_VCOMCONTROL2, 0xC7
.EQU ILI9341_VCOMCONTROL2, 0xC7
.EQU ILI9341_MEMCONTROL2, 0x36
.EQU ILI9341_MADCTL, 0x36
                                                                                                       #include "avr-linux.h"
                                                                                                       ; TFT control pins : LCD_RD TFT_CONTROL_PORT.0
                                                                                                       ; LCD_WR
                                                                                                                             TFT_CONTROL_PORT.1
TFT_CONTROL_PORT.2
                                                                                                        ; LCD_CD
                                                                                                       ; LCD_CS TFT_CONTROL_PORT.3
; LCD_RESET TFT_CONTROL_PORT.4
                                                                                                       .EQU TFT_CONTROL_PORT, PORTF .EQU TFT_CONTROL_DDR, DDRF
    .EQU ILI9341_MADCTL_MY, 0x80
    .EQU ILI9341_MADCTL_MX, 0x40
                                                                                                       .EQU LCD_CONTROL_PORT_MASK, 0x1F
    EQU ILI9341_MADCTL_MV, 0x20
EQU ILI9341_MADCTL_ML, 0x10
EQU ILI9341_MADCTL_RGB, 0x00
EQU ILI9341_MADCTL_BGR, 0x08
EQU ILI9341_MADCTL_MH, 0x04
                                                                                                       ; All signals are active low, so use SBI to idle ; and CBI to active LCD_RD.

.MACRO RD_ACTIVE
CBI TFT_CONTROL_PORT, 0
                                                                                                        . FNDM
                                                                                                       .MACRO RD_IDLE
                                     tft-colors.S
                                                                                                          SBI TFT_CONTROL_PORT, 0
    .EQU MAGENTA, 0xF8F8
   .EQU MAGENTA, OxF8F8
.EQU ORANGE, OxE4E4
.EQU BLACK, Ox0000
.EQU WHITE, OxFFFF
.EQU BLUE_LIGHT, Ox1D1D
.EQU BLUE_DARK, Ox1818
.EQU RED_LIGHT, OxE0E0
.EQU RED_DARK, OxC0C0
.EQU GREEN_LIGHT, Ox4747
.EQU GREEN_DARK, Ox6666
                                                                                                        ENDM
                                                                                                       ; LCD_WR
.MACRO WR_ACTIVE
                                                                                                          CBI TFT_CONTROL_PORT, 1
                                                                                                       .MACRO WR_IDLE
                                                                                                          SBI TFT_CONTROL_PORT, 1
                                                                                                        .ENDM
                                                                                                          LCD CD
                                                                                                        .MACRO CD_COMMAND
                                      tft-delay.S
                                                                                                          CBI TFT_CONTROL_PORT, 2
                                                                                                        . F.NDM
; Delays for (R25 + 1) * 0.5 microseconds. Since R25; is an 8 bit register, the maximum possible delay
                                                                                                       .MACRO CD_DATA
SBI TFT_CONTROL_PORT, 2
       is
    128 microseconds.
                                                                                                        .ENDM
HALF_MICRO_DELAY:
                                                                                                       ; LCD_CS
.MACRO CS_ACTIVE
CBI TFT_CONTROL_PORT, 3
    CLR R18
                                                       ; 1 cycle
   NOP
                                                       ; 1 cycle
; 8 cycle loop = 500
half_micro_delay_loop:
    ns
CP R18, R25
                                                                                                        .ENDM
                                                       ; 1 cycle
; 1 cycle (non-taken)
                                                                                                       .MACRO CS_IDLE SBI TFT_CONTROL_PORT, 3
    BREQ half_micro_delay_end
    NUb
    NOP
                                                                                                       .ENDM
                                                       ; 3 nop cycles ; 1 cycle
    NOP
    INC R18
                                                                                                          LCD_RESET
                                                       ; 1 cycle
; 2 cycles
    RJMP half_micro_delay_loop
                                                                                                        .MACRO TFT_RESET_LOW
                                                                                                          CBI TFT_CONTROL_PORT, 4
half_micro_delay_end:
                                                                                                        . ENDM
                                                       ; 5 cycles
                                                                                                       .MACRO TFT_RESET_HIGH
; Delays for R25 miliseconds. Since R25 is an 8 bit
, register, the maximum possible delay is 256 ms. MILI_DELAY:
                                                                                                          SBI TFT_CONTROL_PORT, 4
   PUSH R16
                                                                                                        .MACRO WR_STROBE WR_ACTIVE
    PUSH R17
CLR R16
MOV R24, R25
mili_delay_loop:
CP R16, R24
BREQ mili_delay_end
                                                                                                       WR_IDLE . ENDM
                                                                                                       .EQU PORTH_MASK, 0x78
.EQU PORTG_MASK, 0x20
.EQU PORTE_MASK, 0x38
    INC R16
LDI R25, 200
LDI R17, 10
mili_delay_loop2:
CPI R17, 0
                                                                                                       .EQU DATA_MASK_76, 0xC0 ; to H4, H3 .EQU DATA_MASK_5, 0x20 ; to E3 .EQU DATA_MASK_4, 0x10 ; to G5 \,
    BREQ mili_delay_loop
                                                                                                       .EQU DATA_MASK_32, 0x0C; to E4, E4
.EQU DATA_MASK_10, 0x03; to H6, H5
    DEC R17
    PUSH R16
    CALL HALF_MICRO_DELAY ; 100 us
    POP R16
                                                                                                    CSEG
    RJMP mili_delay_loop2
mili_delay_end:
POP R17
                                                                                                      WRITE_8 writs register R25 into screen data.
                                                                                                      Rewrite this to re-map screen conections.
```

```
For Arduino Mega (2560) the mapping is: ScreenData: D7 D6 D5 D4 D3 D2 D1 D0 atmega2560: H4 H3 E3 G5 E5 E4 H6 H5
                                                                                                                       \mathtt{data}\ \mathtt{d}\ \mathtt{in}\ \mathtt{R24}
                                                                                                      ; It stores d in address a of the TFT screen. WRITE_REGISTER_8:
WRITE_8:
PUSH R16
                                                                                                          CD_COMMAND
                                                                                                          CALL WRITE_8
   PUSH R17
                                                                                                          CD_DATA
   ; Deal with PORTH
LDS R16, PORTH
ANDI R16, ~PORTH_MASK
                                                                                                          MOV R25, R24
CALL WRITE_8
                                                                                                          RET
      Н6.
                                                                                                      ; WRITE_REGISTER_16 takes two 16 bit parameters:
   MOV R17, R25
ANDI R17, DATA_MASK_10
                                                                                                           address a in R25:R24
data d in R23:R22
                                                                                                      ; It stores d in address a of the TFT screen.; It writes bytes in hi-lo order.
WRITE_REGISTER_16:
   LSL R17 ; H1 --> H6
   LSL R17
   LSL R17
   LSL R17
                                                                                                          CD_COMMAND
   LSL R17
                                                                                                          CALL WRITE_8
   OR R16, R17
                                                                                                          MOV R25, R24
                                                                                                          CALL WRITE_8
      H4. H3
   ; H4, n5
MOV R17, R25
ANDI R17, DATA_MASK_76
LSR R17; H7 --> H4
TCD P17
                                                                                                          CD DATA
                                                                                                          MOV R25, R23
CALL WRITE_8
   LSR R17
                                                                                                          MOV R25, R22
   LSR R17
OR R16, R17
                                                                                                          CALL WRITE_8
                                                                                                          RET
   STS PORTH, R16
                                                                                                      ; WRITE_REGISTER_32 takes a one byte addres and a 32
                                                                                                            bit data:
      Deal with PORTG (can use IN)
   INPUT R16, PORTG
ANDI R16, "PORTG_MASK
                                                                                                               address a in R25
                                                                                                                   data d in R23:R22:R21:R20
                                                                                                      ; It stores d in address a of the TFT screen.; It writes bytes in hi-lo order. WRITE_REGISTER_32:
   ; G5
MOV R17, R25
ANDI R17, DATA_MASK_4
LSL R17; G4 --> G5
OR R16, R17
OUTPUT PORTG, R16
                                                                                                         PUSH R25
                                                                                                          CS_ACTIVE
                                                                                                          CD COMMAND
                                                                                                         CALL WRITE_8
     Deal with PORTE (can use IN)
   INPUT R16, PORTE
ANDI R16, PORTE_MASK
                                                                                                          CD_DATA
                                                                                                          MOV R25, R23
    ; E5, E4
                                                                                                          CALL WRITE_8
   MOV R17, R25
ANDI R17, DATA_MASK_32
LSL R17; E3 --> E5
LSL R17
                                                                                                          MOV R25, R22
CALL WRITE 8
                                                                                                          MOV R25, R21
CALL WRITE_8
                                                                                                          MOV R25, R20
CALL WRITE_8
   OR R16, R17
   MOV R17, R25
ANDI R17, DATA_MASK_5
LSR R17; E5 --> E3
LSR R17
                                                                                                          CS IDLE
                                                                                                          POP R25
   OR R16, R17
   OUTPUT PORTE, R16
                                                                                                                                        tft-char-ROM.S
                                                                                                          .EQU TFT_CHAR_H, 8 .EQU TFT_CHAR_W, 6
   WR_STROBE
   POP R17
                                                                                                         ft_char_ROM:

DB 0x00, 0x00, 0x00, 0x00, 0x00

DB 0x3E, 0x5B, 0x4F, 0x5B, 0x3E

DB 0x3E, 0x6B, 0x4F, 0x6B, 0x3E

DB 0x1C, 0x3E, 0x7C, 0x3E, 0x1C

DB 0x18, 0x3C, 0x7E, 0x3C, 0x1B
   POP R16
   RET
   Sets screen data pins as output.
SET_WRITE_DIR:
   PUSH R17
                                                                                                          DB 0x1C, 0x57, 0x7D, 0x57, 0x1C
DB 0x1C, 0x5E, 0x7F, 0x5E, 0x1C
   INPUT R17, DDRH
ORI R17, PORTH_MASK
OUTPUT DDRH, R17
                                                                                                         DB 0x00, 0x18, 0x3C, 0x18, 0x00
DB 0xFF, 0xE7, 0xC3, 0xE7, 0xFF
DB 0x00, 0x18, 0x24, 0x18, 0x00
DB 0xFF, 0xE7, 0xDB, 0xE7, 0xFF
DB 0x30, 0x48, 0x3A, 0x06, 0x0E
   INPUT R17, DDRE
ORI R17, PORTE_MASK
OUTPUT DDRE, R17
                                                                                                          DB 0x26, 0x29, 0x79, 0x29, 0x26
                                                                                                         DB 0x40, 0x7F, 0x05, 0x05, 0x07

DB 0x40, 0x7F, 0x05, 0x25, 0x3F

DB 0x5A, 0x3C, 0xE7, 0x3C, 0x5A

DB 0x7F, 0x3E, 0x1C, 0x1C, 0x08
   INPUT R17, DDRG
ORI R17, PORTG_MASK
OUTPUT DDRG, R17
   POP R17
                                                                                                         DB 0x7F, 0x3E, 0x1C, 0x1C, 0x3E, 0x7F

DB 0x14, 0x22, 0x7F, 0x22, 0x14

DB 0x5F, 0x5F, 0x00, 0x5F, 0x5F

DB 0x06, 0x09, 0x7F, 0x01, 0x7F

DB 0x00, 0x66, 0x89, 0x95, 0x6A

DB 0x60, 0x60, 0x60, 0x60, 0x60, 0x60

DB 0x94, 0xA2, 0xFF, 0xA2, 0x94
; Mid-level routines to talk to the screen
; WRITE_REGISTER_8 takes two 8 bit parameters:
          address a in R25
```

```
DB 0x08, 0x04, 0x7E, 0x04, 0x08
                                                          DB 0x7C, 0x04, 0x78, 0x04, 0x78
DB 0x10,
                                                           DB 0x7C,
         0x20,
                0x7E. 0x20.
                             0x10
                                                                    0x08, 0x04,
                                                                                  0x04, 0x78
                                                          DB 0x38,
  0x08,
         0x08, 0x2A, 0x1C, 0x08
                                                                    0x44, 0x44, 0x44, 0x38
                0x2A,
                                                           DB OxFC,
   0x08,
                      0x08,
                             0x08
                                                                     0x18,
                                                                           0x24,
                                                                                  0x24, 0x18
          0x1C,
                0x10,
                                                           DB 0x18,
                                                                     0x24, 0x24,
                                                                                  0x18,
   0x1E
          0x10.
                      0x10, 0x10
DB
  0x0C
         0x1E,
                0x0C,
                      0x1E, 0x0C
                                                           DB 0x7C,
                                                                     0x08,
                                                                           0x04,
                                                                                  0x04, 0x08
                                                                     0x54,
DB 0x30
          0x38,
                0x3E,
                      0x38,
                             0x30
                                                           DB 0x48,
                                                                           0x54, 0x54, 0x24
         0x0E,
                                                           DB 0x04,
                                                                     0x04,
DB 0x06
                0x3E, 0x0E, 0x06
                                                                           0x3F, 0x44, 0x24
                                                                     0x40,
                0x00,
                                                           DB 0x3C,
                                                                           0x40, 0x20, 0x7C
DB 0x00.
          0x00,
                      0x00, 0x00
                                                           DB 0x1C,
                                                                     0x20,
                                                                           0x40, 0x20, 0x1C
0x30, 0x40, 0x3C
DB
   0x00,
         0x00,
                0x5F, 0x00, 0x00
   0x00,
          0x07,
                0x00,
                      0x07, 0x00
                                                           DB 0x3C,
                                                                     0x40,
                      0x7F,
                                                           DB 0x44,
                                                                     0x28,
                                                                           0x10,
                                                                                  0x28, 0x44
          0x7F,
                0x14,
                             0x14
DB 0x14
         0x2A,
                0x7F,
                      0x2A,
                                                           DB 0x4C,
                                                                     0x90,
                                                                           0x90,
DB 0x24
                             0 \times 12
                                                                                  0x90, 0x7C
                                                                           0x54,
DB 0x23,
                0x08,
                                                           DB 0x44,
                                                                     0x64,
         0x13,
                      0x64,
                             0x62
                                                                                  0x4C, 0x44
                0x56,
                                                           DB 0x00,
DB 0x36,
         0x49,
                      0x20, 0x50
                                                                     0x08,
                                                                           0x36,
                                                                                  0x41, 0x00
                                                                     0x00,
                                                                           0x77,
DB 0x00
                                                                                  0x00,
          0x08,
                0x07,
                      0x03, 0x00
                                                           DB 0x00,
                                                                                        0x00
                                                           DB 0x00,
                                                                     0x41,
                                                                           0x36, 0x08, 0x00
DB 0x00
          0x1C,
                0x22, 0x41, 0x00
DB
  0x00
          0x41,
                0x22,
                      0x1C, 0x00
                                                           DB 0x02,
                                                                     0x01,
                                                                           0x02,
                                                                                  0x04, 0x02
                                                                                  0x26,
DB 0x2A
                0x7F,
                      0x1C,
                                                                     0x26,
                                                                           0x23,
                                                                                        0x3C
          0x1C,
                             0x2A
                                                           DB 0x3C
         0x08,
                0x3E,
                      0x08, 0x08
0x30, 0x00
                                                           DB 0x1E,
                                                                     OxA1,
                                                                           OxA1,
DB
  0x08
                                                                                  0x61, 0x12
                                                           DB 0x3A,
                                                                     0x40,
DB 0x00,
         0x80,
                0x70,
                                                                           0x40, 0x20,
                                                                                        0x7A
DB 0x08,
         0x08, 0x08, 0x08, 0x08
                                                           DB 0x38,
                                                                     0x54,
                                                                           0x54, 0x55, 0x59
                                                                           0x55,
DB 0x00,
          0x00,
                0x60, 0x60, 0x00
                                                           DB 0x21,
                                                                     0x55,
                                                                                  0x79, 0x41
                                                                     0x54,
          0x10, 0x08, 0x04, 0x02
                                                           DB 0x22,
                                                                           0x54, 0x78, 0x42
DB 0x20
                                                                                             ; a-umlaut
DB 0x3E
         0x51,
                0x49,
                      0x45,
                            0x3E
                                                           DB 0x21,
                                                                     0x55,
                                                                           0x54,
                                                                                  0x78, 0x40
                                                                     0x54,
                                                                           0x55,
DB 0x00
          0x42,
                0x7F,
                      0x40,
                             0x00
                                                           DB 0x20,
                                                                                  0x79, 0x40
                                                           DB 0x0C,
         0x49,
                0x49,
                      0x49, 0x46
                                                                                  0x72, 0x12
DB 0x72.
                                                                     0x1E,
                                                                           0x52,
                      0x4D,
                             0x33
                                                                     0x55,
                                                                           0x55,
                                                                                  0x55,
DB 0x21,
          0x41,
                0x49,
                                                           DB 0x39,
                                                                                        0x59
                                                           DB 0x39,
DB 0x18
          0x14.
                0x12, 0x7F, 0x10
                                                                     0x54,
                                                                           0x54, 0x54, 0x59
          0x45,
                                                           DB 0x39,
DB
  0x27
                0x45, 0x45,
                             0x39
                                                                     0x55,
                                                                           0x54,
                                                                                  0x54, 0x58
                                                                                 0x7C, 0x41
0x7D, 0x42
                0x49,
                                                                     0x00,
                                                                           0x45,
DB 0x3C
                      0x49,
                             0x31
                                                           DB 0x00,
          0x4A,
                0x11,
                                                                           0x45,
         0x21,
                                                           DB 0x00,
DB 0x41,
                      0x09,
                             0 \times 07
                                                                     0x02,
                                                                           0x45,
                                                                                  0x7C,
         0x49,
                0x49,
                                                                     0x01,
DB 0x36,
                      0x49,
                             0x36
                                                           DB 0x00,
                                                                                        0x40
                                                           DB 0x7D,
                                                                     0x12,
   0x46,
                                                                                  0x12, 0x7D
                0x49, 0x29, 0x1E
DB
         0x49,
                                                                           0x11,
                                                                                             ; A-umlaut
DB 0x00,
          0x00,
                0x14, 0x00, 0x00
                                                           DB 0xF0,
                                                                     0x28,
                                                                           0x25,
                                                                                  0x28, 0xF0
                                                           DB 0x7C,
                                                                     0x54,
DB 0x00
          0x40, 0x34, 0x00, 0x00
                                                                           0x55, 0x45, 0x00
                                                                     0x54,
                                                                           0x54,
DB
   0x00
         0x08,
                0x14,
                      0x22, 0x41
                                                           DB 0x20,
                                                                                  0x7C,
                                                                                        0x54
                                                                                 0x7F,
0x49,
                                                                     OxOA,
                                                                           0x09,
          0x14,
                0x14,
                      0x14,
DB 0x14
                             0x14
                                                           DB 0x7C,
                                                                                        0x49
         0x41,
                0x22,
                      0x14,
DB 0x00,
                                                           DB 0x32,
                             0x08
                                                                     0x49,
                                                                           0x49,
                                                                                        0x32
                                                                           0x44,
                             0x06
                                                           DB Ox3A,
                                                                     0x44,
                                                                                  0x44,
DB 0x02.
                0x59, 0x09,
                                                                                        0x3A ; o-umlaut
          0x01,
                                                                     0x4A,
DB 0x3E,
                                                           DB 0x32,
                                                                           0x48, 0x48, 0x30
         0x41, 0x5D, 0x59, 0x4E
DB 0x7C
          0x12,
                0x11, 0x12,
                             0x7C
                                                           DB 0x3A,
                                                                     0x41,
                                                                           0x41,
                                                                                  0x21,
                                                                                        0x7A
DB 0x7F
          0x49,
                0x49, 0x49,
                             0x36
                                                           DB 0x3A,
                                                                     0x42,
                                                                           0x40,
                                                                                  0x20, 0x78
                                                                     0x9D,
DB 0x3E
         0x41,
                0x41, 0x41, 0x22
                                                           DB 0x00,
                                                                           0xA0, 0xA0, 0x7D
                                                                     0x42,
         0x41,
                                                                           0x42,
                                                                                  0x42, 0x3D
0x40, 0x3D
                                                                                             ; O-umlaut
DB 0x7F
                0x41,
                      0x41,
                             0x3E
                                                           DB 0x3D,
                                                                                        0 x 3 D
                                                           DB 0x3D,
                                                                           0x40,
                                                                     0x40,
DB 0x7F
          0x49,
                0x49, 0x49,
                            0 \times 41
                                                                           0xFF,
                                                                     0x24,
                                                                                  0x24,
                0x09, 0x09, 0x01
                                                           DB 0x3C,
                                                                                        0x24
DB 0x7F
          0x09,
                                                                     0x7E,
                                                                           0x49, 0x43, 0x66
                                                           DB 0x48,
DB 0x3E
          0x41, 0x41, 0x51, 0x73
                                                                     0x2F,
                                                                           OxFC,
DB 0x7F
          0x08.
                0x08,
                      0x08, 0x7F
                                                           DB 0x2B,
                                                                                  0x2F, 0x2B
                                                                     0x09,
                                                                                  0xF6,
DB 0x00
          0x41,
                0x7F,
                      0x41,
                             0x00
                                                           DB OxFF
                                                                           0x29,
                                                                                        0x20
                                                                     0x88,
                                                                           0x7E,
         0x40,
                      0x3F,
                                                           DB 0xC0,
DB 0x20
                0x41,
                             0x01
                                                                                  0x09, 0x03
         0x08,
                                                                     0x54,
                                                                           0x54,
                                                                                  0x79,
                0x14,
                      0x22,
                                                           DB 0x20,
DB 0x7F
                             0x41
                                                                                        0x41
                                                                     0x00,
                                                           DB 0x00,
                                                                           0x44,
                0x40,
DR
  0x7F
          0x40,
                      0x40, 0x40
                                                                                  0x7D, 0x41
                                                           DB 0x30,
                                                                     0x48,
DB 0x7F
          0x02,
                0x1C, 0x02, 0x7F
                                                                           0x48,
                                                                                  0x4A, 0x32
          0x04,
                                                                     0x40,
                                                                           0x40,
DB 0x7F
                0x08, 0x10, 0x7F
                                                           DB 0x38,
                                                                                  0x22, 0x7A
                      0x41,
                                                                     0x7A,
                                                                           OxOA,
DB 0x3E
          0x41,
                0x41,
                             0x3E
                                                           DB 0x00,
                                                                                  0x0A, 0x72
                                                                     OxOD,
                                                                                  0x31,
         0x09,
                0x09,
                                                           DB 0x7D,
DB 0x7F
                      0x09,
                             0 \times 06
                                                                           0x19,
                                                                                        0 \times 7D
                                                           DB 0x26,
DB
  0x3E
          0x41,
                0x51,
                      0x21, 0x5E
                                                                     0x29,
                                                                           0x29,
                                                                                  0x2F, 0x28
                                                                     0x29,
                                                                           0x29,
          0x09,
                0x19,
                      0x29, 0x46
                                                           DB 0x26,
                                                                                  0x29,
DB 0x7F
                                                                                        0x26
                                                           DB 0x30,
                                                                                 0x40, 0x20
0x08, 0x08
  0x26
                                                                     0x48,
                                                                           0x4D,
DΒ
          0x49, 0x49, 0x49, 0x32
                0x7F,
                                                           DB 0x38,
                                                                     0x08,
   0 \times 03
          0x01,
                      0x01,
                             0x03
                                                                           0x08,
                                                                     0x08,
DB 0x3F
          0x40,
                0x40, 0x40,
                             0x3F
                                                           DB 0x08,
                                                                           0x08, 0x08, 0x38
          0x20,
DB 0x1F
                0x40, 0x20,
                            0 x 1 F
                                                           DB 0x2F,
                                                                     0x10,
                                                                           0xC8,
                                                                                  OxAC, OxBA
                                                                                  0x34,
                                                                     0x10,
                                                                           0x28,
         0x40,
DB 0x3F
                0x38, 0x40,
                             0x3F
                                                           DB 0x2F,
                                                                                        0 x F A
                                                           DB 0x00,
                                                                    0x00,
                                                                           0x7B,
DB
  0x63,
         0x14,
                0x08, 0x14, 0x63
                                                                                  0x00, 0x00
DB 0x03,
          0x04,
                0x78, 0x04,
                             0x03
                                                           DB 0x08,
                                                                     0x14,
                                                                           0x2A,
                                                                                  0x14,
                                                                                        0x22
                                                          DB 0x22,
                                                                     0x14,
          0x59.
                0x49, 0x4D, 0x43
                                                                           0x2A, 0x14, 0x08
                                                          DB 0x55,
                                                                    0x00,
                0x41, 0x41, 0x41
                                                                          0x55, 0x00, 0x55; #176 (25% block)
DB 0x00
          0x7F,
DB 0x02
          0x04,
                0x08, 0x10,
                             0x20
                                                                missing in old code
                                                           DB 0xAA, 0x55, 0xAA, 0x55, 0xAA; 50% block
         0x41,
DB 0x00,
                0x41, 0x41, 0x7F
                                                                    0x55,
                                                                          OxFF,
                                                          DB OxFF,
                                                                                  0x55,
DB 0x04,
         0x02,
                0x01,
                      0x02,
                                                                                        0xFF ; 75% block
                             0 \times 04
                                                           DB 0x00,
                                                                    0x00, 0x00, 0xFF, 0x00
0x10, 0x10, 0xFF, 0x00
DR
   0x40
          0x40, 0x40, 0x40, 0x40
                                                           DB 0x10,
                                                                     0x10, 0x10,
  0x00
          0x03, 0x07, 0x08, 0x00
                0x54, 0x78, 0x40
                                                           DB 0x14,
                                                                     0x14,
                                                                           0x14,
                                                                                  0xFF, 0x00
DB 0x20
          0x54,
                0x44,
                                                                     0x10,
                                                                           0xFF,
   0x7F
          0x28,
                      0x44,
                             0x38
                                                           DB 0x10,
                                                                                  0x00, 0xFF
DB
DB 0x38,
         0x44,
                0x44,
                                                           DB 0x10,
                                                                     0x10,
                                                                           0xF0,
                      0x44,
                             0x28
                                                                                  0x10, 0xF0
                0x44,
                                                                     0x14,
         0x44,
                                                           DB 0x14,
                                                                           0x14,
   0x38,
DB
                      0x28, 0x7F
                                                                                  0xFC, 0x00
                                                                     0x14,
                                                                           0xF7,
                                                                                  0x00,
   0x38
                0x54,
                                                           DB 0x14,
                                                                                        0xFF
DB
          0x54.
                      0x54.
                             0x18
                                                                     0x00,
                                                                           OxFF,
   0.000
          0x08,
                0x7E, 0x09, 0x02
                                                           DB 0x00,
                                                                                  0x00, 0xFF
                0xA4,
                                                                           0xF4,
   0x18
          0xA4,
                      0x9C,
                             0x78
                                                           DB 0x14,
                                                                     0x14,
                                                                                  0x04, 0xFC
                                                                                  0x10,
                                                                     0x14,
                                                                           0x17,
DB
   0x7F
          0x08,
                0x04,
                      0x04,
                             0x78
                                                           DB 0x14
                                                                                        0 x 1 F
         0x44,
                                                           DB 0x10,
                0x7D,
                      0x40,
                                                                     0x10,
                                                                           0x1F,
                                                                                  0x10,
DR
   0 x 0 0
                             0x00
                                                                                        0 x 1 F
                0x40,
                                                           DB 0x14,
                                                                     0x14,
                                                                           0x14,
                                                                                  0x1F,
         0x40,
                      0x3D,
DR
   0×20
                             0x00
                                                                                        0 \times 00
                                                           DB 0x10, 0x10, 0x10, 0xF0, 0x00
DB
   0x7F,
         0x10, 0x28, 0x44, 0x00
DB 0x00, 0x41, 0x7F, 0x40, 0x00
                                                           DB 0x00, 0x00, 0x00, 0x1F, 0x10
```

```
DB 0xFC, 0x4A, 0x4A, 0x4A, 0x34 ; sharp-s or beta DB 0x7E, 0x02, 0x02, 0x06, 0x06 DB 0x02, 0x7E, 0x02, 0x7E, 0x02
DB 0x10, 0x10, 0x10, 0x1F, 0x10
DB 0x10, 0x10, 0x10, 0xF0, 0x10
   0x00, 0x00, 0x00, 0xFF, 0x10
DB
                                                                                 0x55,
                                                                                        0x49,
                                                                                                0x41, 0x63
           0x10,
                   0x10, 0x10,
                                                                     DB 0x63,
   0x10,
                                  0x10
           0x10, 0x10, 0xFF,
                                                                     DB 0x38,
                                                                                0x44, 0x44, 0x3C, 0x04
DB
   0x00,
           0x00, 0x00, 0xFF,
                                  0x14
                                                                     DB 0x40,
                                                                                 0x7E, 0x20,
                                                                                                0x1E, 0x20
DB 0x00,
           0x00, 0xFF, 0x00,
                                  0xFF
                                                                     DB 0x06,
                                                                                 0x02,
                                                                                        0x7E, 0x02, 0x02
                                                                                        0xE7,
   0x00,
                                                                                0xA5,
0x2A,
                                                                     DB 0x99,
DB
           0x00, 0x1F,
                           0x10, 0x17
                                                                                                0xA5, 0x99
                                                                                        0x49,
                                                                     DB Ox1C,
                                                                                                Ox2A,
DB 0x00,
           0x00, 0xFC, 0x04, 0xF4
                                                                                                        0 x 1 C
           0x14, 0x17, 0x10, 0x17
                                                                                0x72, 0x01, 0x72, 0x4C
0x4A, 0x4D, 0x4D, 0x30
   0x14,
                                                                     DB 0x4C,
DB
   0x14,
           0x14, 0xF4, 0x04, 0xF4
                                                                     DB 0x30,
   0x00,
                                                                                 0x48,
                                                                                        0x78, 0x48, 0x30
0x5A, 0x46, 0x3D
DΒ
           0x00, 0xFF, 0x00,
                                  0xF7
                                                                     DB 0x30,
   0x14,
DB
           0x14,
                   0x14,
                           0x14,
                                  0x14
                                                                     DB 0xBC,
                                                                                 0x62,
DB 0x14,
                                                                                 0x49,
           0x14, 0xF7, 0x00,
                                  0xF7
                                                                     DB 0x3E,
                                                                                        0x49, 0x49, 0x00
                                                                                0x01, 0x01, 0x01, 0x7E
0x2A, 0x2A, 0x2A, 0x2A
0x44, 0x5F, 0x44, 0x44
   0x14,
                                  0x14
                                                                     DB 0x7E,
DB 0x2A,
DB
           0x14, 0x14, 0x17,
           0x10, 0x1F, 0x10, 0x1F
0x14, 0x14, 0xF4, 0x14
DB
   0x10,
                                                                     DB 0x44,
DB 0x14,
   0x10,
           0x10, 0xF0, 0x10,
                                  0 x F 0
                                                                     DB 0x40,
                                                                                 0x51,
                                                                                         0x4A,
                                                                                                0x44, 0x40
                                                                                 0x44,
                                                                                         0x4A,
                                                                                                0x51, 0x40
0x01, 0x03
DB 0x00
           0x00, 0x1F, 0x10,
                                  0 x 1 F
                                                                     DB 0x40,
                                                                                0x00,
0x80,
                                                                                        0xFF,
DR
   00x0
           0x00,
                   0x00, 0x1F,
                                  0x14
                                                                     DB 0x00,
DB 0x00,
           0x00, 0x00, 0xFC,
                                                                     DB 0xE0,
                                                                                        0xFF, 0x00, 0x00
0x6B, 0x6B, 0x08
                                  0x14
                                                                     DB 0x08,
                                                                                0x08,
   0x00,
DB
           0x00, 0xF0, 0x10, 0xF0
                                                                                        0x36, 0x24, 0x36
0x09, 0x0F, 0x06
0x18, 0x18, 0x00
                                                                                0x12,
0x0F,
0x00,
           0x10, 0xFF, 0x10,
0x14, 0x14, 0xFF,
0x10, 0x10, 0x1F,
                                                                     DB 0x36,
DΒ
   0x10,
                                  0xFF
DB 0x14,
                                  0x14
                                                                     DB 0x06,
DB 0x10,
                                  0 x 0 0
                                                                     DB 0x00,
DB 0x00,
                                                                                 0x00,
                                                                                                0x10,
           0x00,
                   0x00, 0xF0,
                                  0 x 1 0
                                                                     DB 0x00,
                                                                                         0x10,
                                                                                                        0 x 0 0
           0xFF,
                                                                     DB 0x30,
                   OxFF, OxFF,
                                                                                0x40,
DB
   0xFF
                                  0 \times FF
                                                                                         OxFF,
                                                                                                0x01, 0x01
                                                                                0x1F,
           0xF0,
                   0xF0, 0xF0,
                                                                     DB 0x00,
                                                                                         0x01,
                                                                                                0x0<u>1</u>,
DB 0xF0,
                                  0xF0
                                                                                                        0 x 1 E
                                                                     DB 0x00, 0x19, 0x1D, 0x17, 0x12
DB 0x00, 0x3C, 0x3C, 0x3C, 0x3C
   0xFF, 0xFF, 0xFF, 0x00, 0x00
DB 0x00, 0x00, 0x00, 0xFF, 0xFF
DB 0x0F, 0x0F, 0x0F, 0x0F, 0x0F
DB 0x38, 0x44, 0x44, 0x38, 0x44
DB OxOF,
                                                                     DB 0x00, 0x00, 0x00, 0x00, 0x00; #255 NBSP
```