



Universidad tres culturas

Diseño Lógico

Circuito Lógico

Sistema Experto UTC

Integrantes De Equipo:

Juan José Solano Fragoso

David Ramírez Gómez

Jesús Pérez Monroy

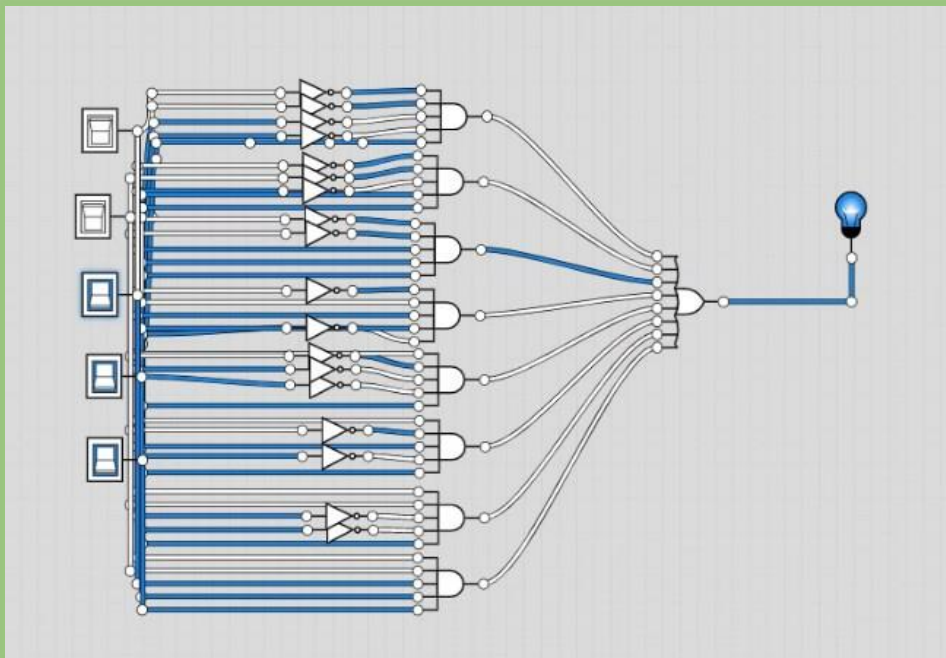
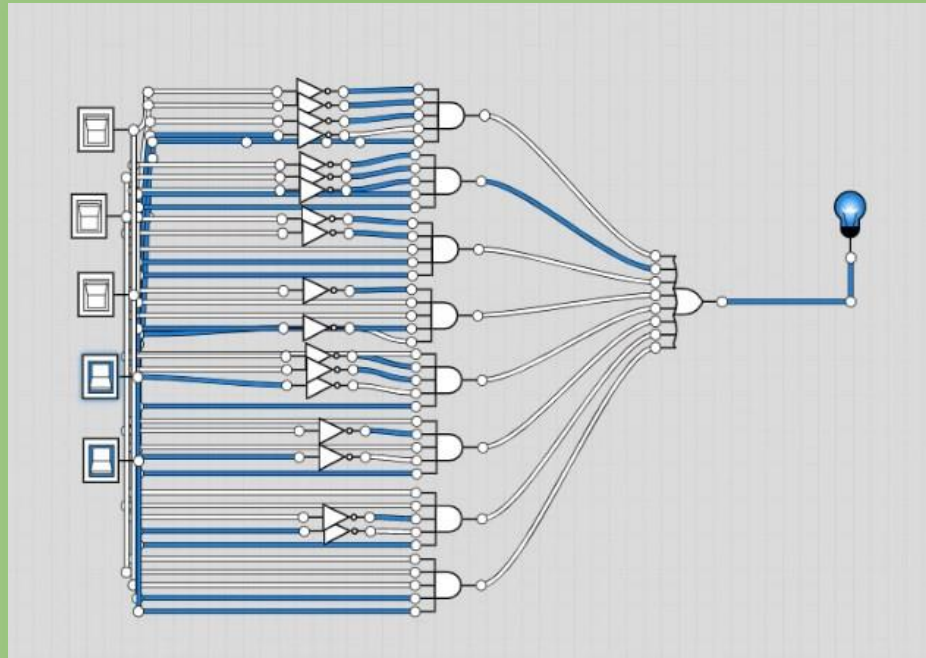
Profesor:

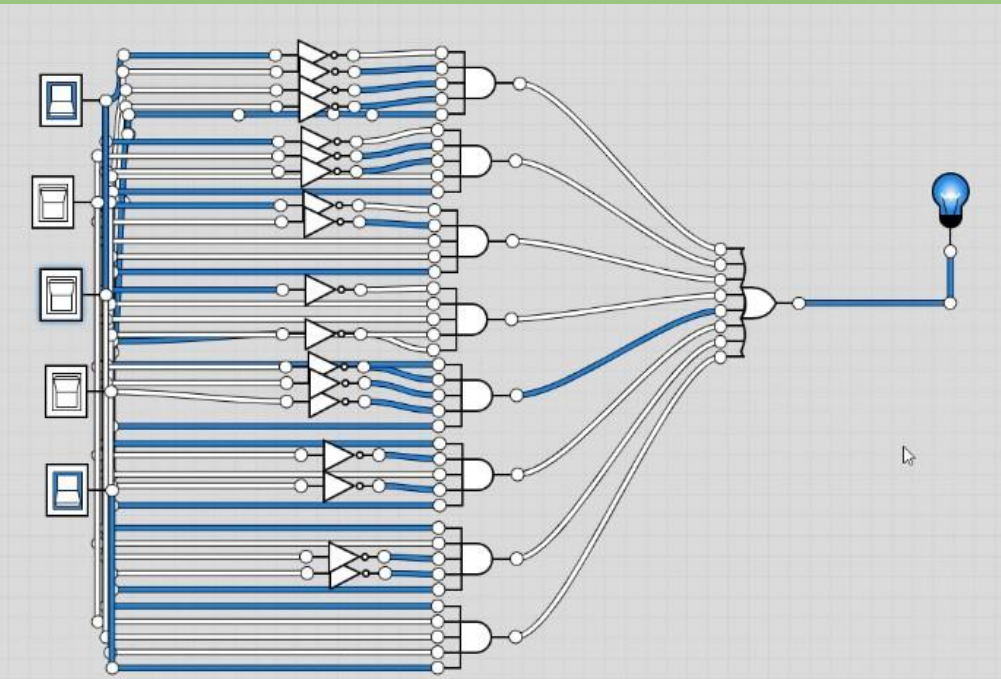
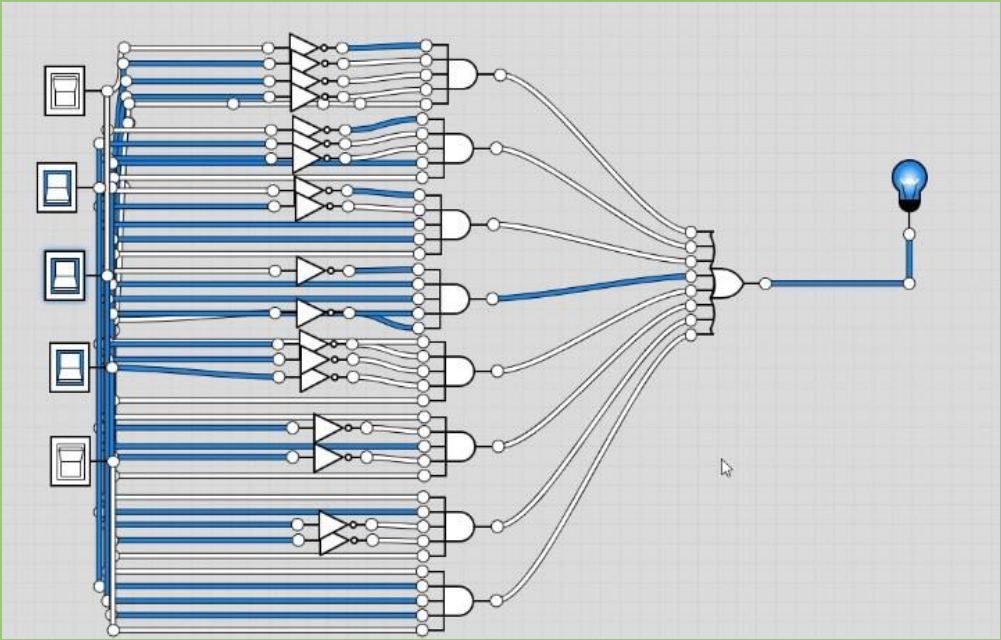
Edgar Calleros Aguilar

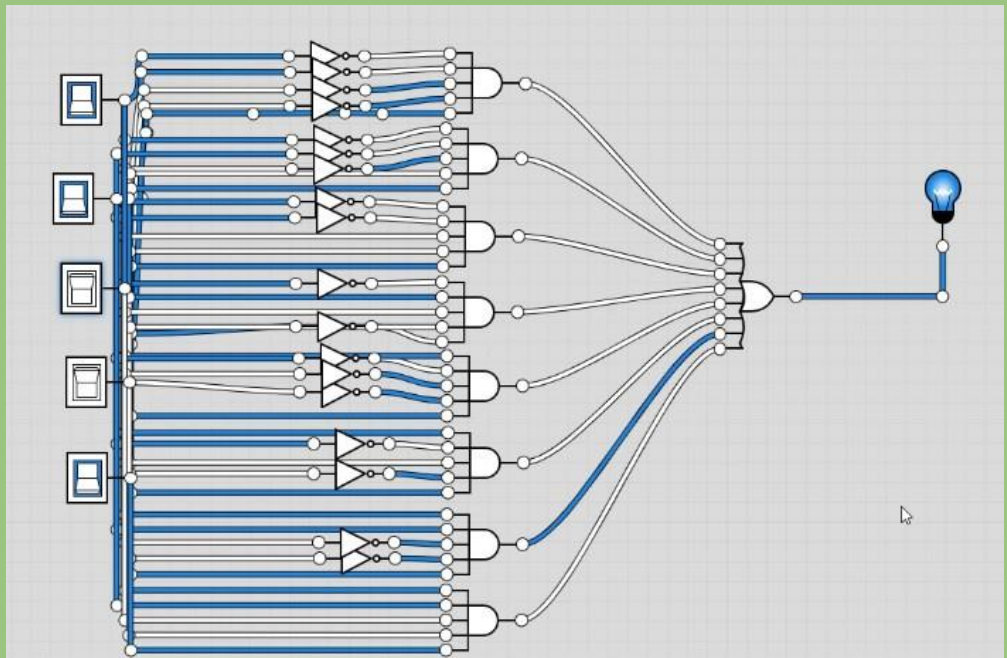
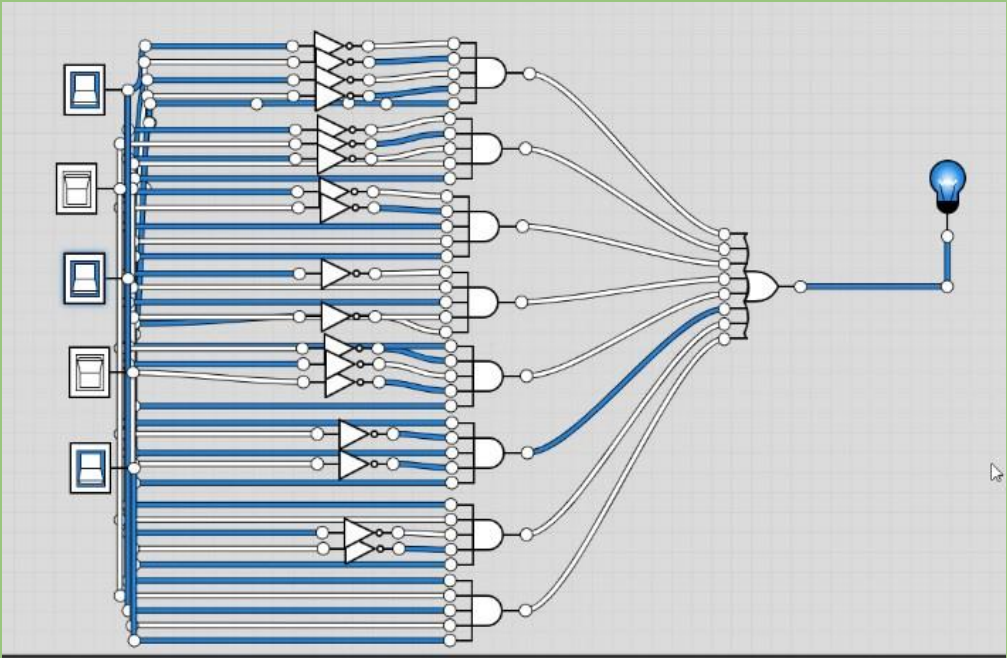
Grupo: 8EL SIS1A

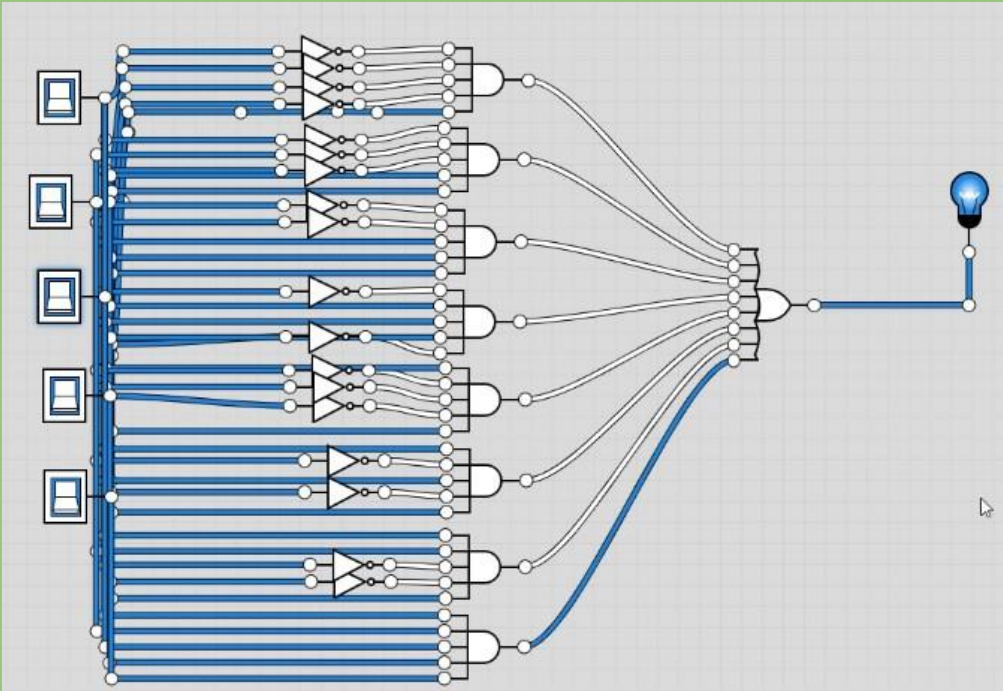
INGENIERÍA EN SISTEMAS COMPUTACIONALES

CIRCUITO LOGICO









TAMBLA DE VERDAD

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0	0	0	1	1	1
0	0	1	0	0	0
0	0	1	0	1	0
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MAPA DE KARNAUGHT

	000	001	011	010	110	111	101	100
00		1	1			1		
01					1			
11		1				1		
10			1				1	

Funcion

$A'B'C'D'E + A'B'C'DE + A'B'CDE + A'BCDE' + AB'C'D'E + AB'CD'E + ABC'D'E + ABCDE$

Funcion Reducida

$ABC'D'E + A'B'CDE + A'BCDE' + ABCDE + AB'CD'E + A'B'C'E + B'C'DE$