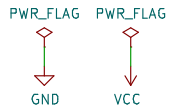
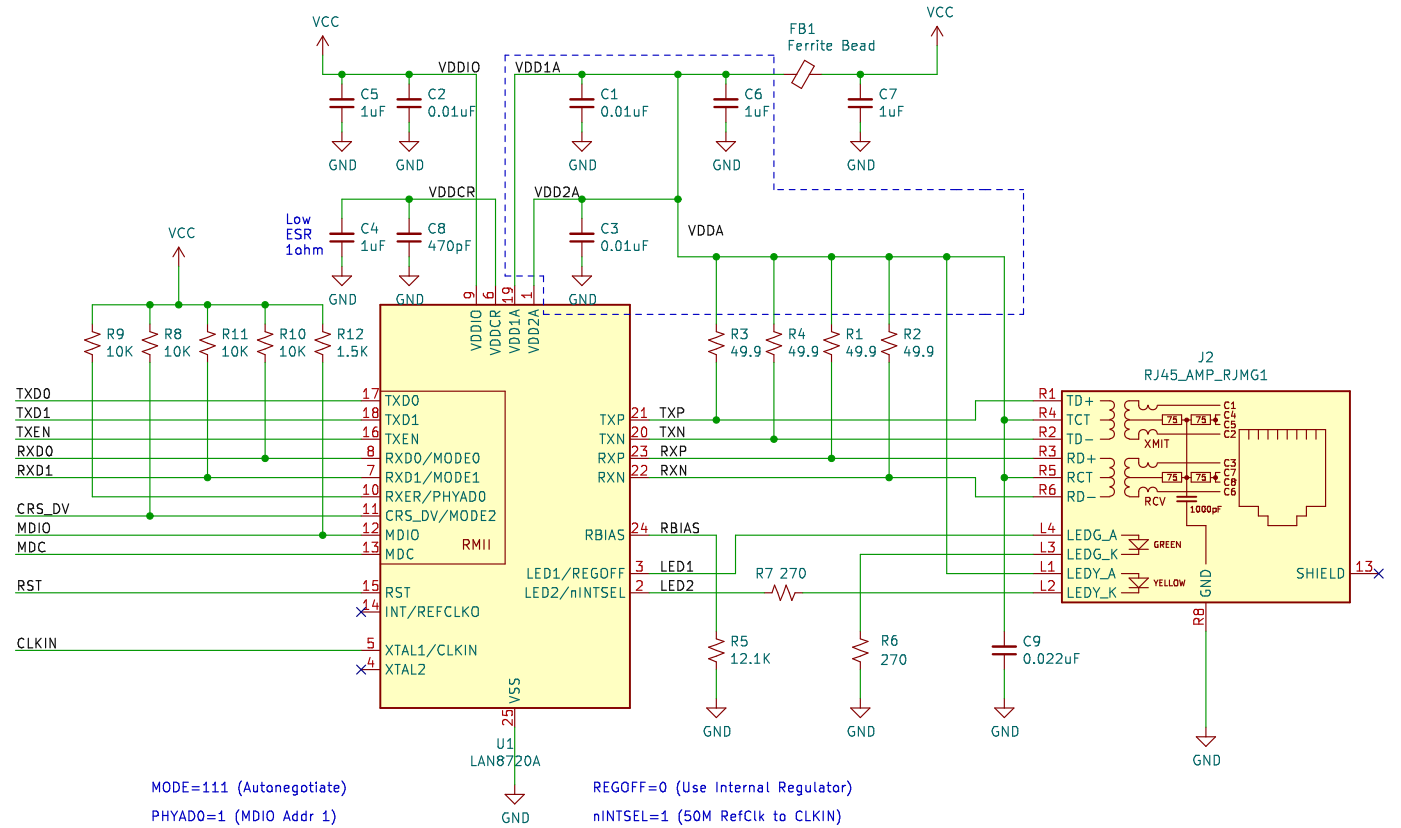
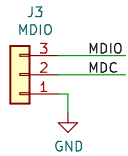
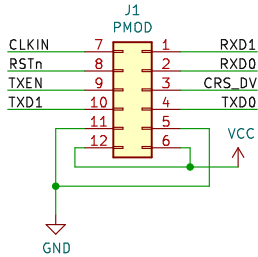


Pins are oriented so the 1–6 row is the top row when looking into the right-angle 2x6 header at the opposite side of the PCB from the RJ45 jack.



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Sheet: /  
File: ethernet-pmod.kicad\_sch  
**Title: Ethernet PHY PMOD**

Size: USLetter Date: 2022-05-16  
KiCad E.D.A. kicad 6.0.5-a6ca702e91~116-ubuntu20.04.1

**Rev: 2**  
Id: 1/1