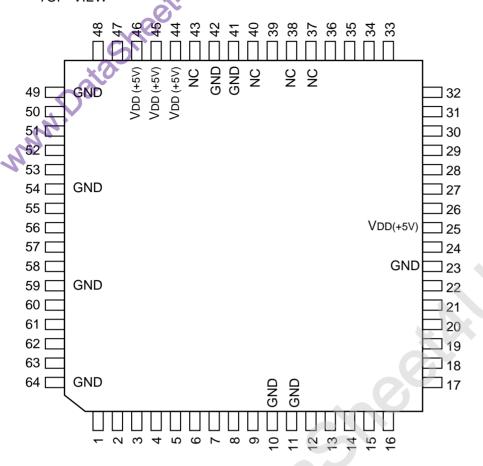

C-MOS FDC (FLOPPY DISK CONTROLLER) —TOP VIEW—

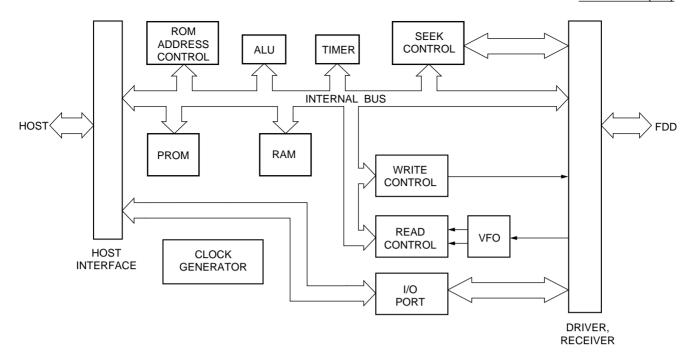


(VDD = +5V)

											(/
PIN No.	I/O	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	8" / 5 "	17	I/O	D5	33	I	TRK0	49	_	GND
2	ı	XTALSEL	18	1/0	D6	34	I	INDEX	50	0	STEP
3	ı	RESET	19	1/0	D7	35	I	RDATA	51	0	HDIR
4	I	E, \overline{RD}	20	0	DREQ	36	I	XTAL2	52	0	HLOAD
5	I	$R/\overline{W}, \overline{WR}$	21	0	ĪRQ	37	_	NC	53	0	HSEL
6	I	CS	22	I	DEND	38	_	NC	54	_	GND
7	ı	DACK	23	_	GND	39	I	XTAL1	55	0	DS0
8	I	RS0	24	0	1/2EX1	40	_	NC	56	0	DS1
9	I	RS1	25	_	Vdd	41	_	GND	57	0	DS2
10	_	GND	26	I	NUM1	42	_	GND	58	0	DS3
11	_	GND	27	I	NUM2	43	_	NC	59	_	GND
12	I/O	D0	28	I	IFS	44	_	Vdd	60	0	MON0
13	I/O	D1	29	ı	SFORM	45	_	Vdd	61	0	MON1
14	I/O	D2	30	I	ĪNP	46	_	Vdd	62	0	MON2
15	I/O	D3	31	I	READY	47	0	WGATE	63	0	MON3
16	I/O	D4	32	I	WPRT	48	0	WDATA	64	4	GND
16 I/O D4 32 I WPRT 48 O WDATA 64 GND											

INPUT		1			
8"/5" ; <u>CS</u> ; DACK ;	DATA TRANSFER RATE SELECT CHIP SELECT DMA ACKNOWLEDGE	12 13 14	D0 D1 D2	IRQ c	21
DEND ; E, RD ; IFS ;	DMA END ENABLE, READ INTERFACE SELECT	15 16	D3 D4	DREQ C	20
INDEX ;	INDEX INPUT PORT NOT USER MODE 1, 2 READ/WRITE, WRITE	17 18 19	D5 D6 D7	WDATA C	47 48 24
RDATA ; READY ; RESET ; RS0, 1 ;	READ DATA READY RESET REGISTER SELECT 0, 1	_4_	RESET E, RD	1/2 EX1 =	50
SFORM ; TRKO ; WPRT ; XTAL1, 2 ;	SELECT FORMAT DATA TRACK 00	5 6 8 9	R/W, WR CS RS0	-	<u>52</u>
XTALSEL ;	XTAL SELECT		RS1		<u>53</u>
OUTPUT 1/2EX1 DREQ DS0-DS3 HDIR HLOAD HSEL IRQ MON0-MON3 STEP WDATA WGATE 1/2EX1 1/2 MONO-MON3 STEP WDATA WGATE	1/2 EXTAL1 DMA REQUEST DRIVE SELECT 0-3 HEAD DIRECTION HEAD LOAD HEAD SELECT INTERRUPT REQUEST MOTOR ON 0-3 STEP WRITE DATA WRITE GATE	$ \begin{array}{r} \frac{7}{22} \\ \hline 22 \end{array} $ $ \begin{array}{r} 35 \\ \hline 34 \\ \hline 33 \end{array} $ $ \begin{array}{r} 2 \\ \hline 26 \\ \hline 27 \end{array} $	RDATA	DS2 C DS3 C MON0 C MON1 C MON2 C	56 57 58 60 61 62 63
INPUT/OUTPUT D0-D7 ;	DATA BUS 0-7	<u>30</u>	INP		
		32 —C	WPRT		
		39 36	XTAL1 XTAL2		
		1 28 29 31 _C	8"/5" IFS SFORM READY		

HD63266F (3/3)



ALU; ARITHMETIC LOGIC UNIT

VFO; VARIABLE FREQUENCY OSCILLATOR