



Spec No. :DS70-2008-0035 Effective Date: 10/17/2017

**Revision: D** 

**LITE-ON DCC** 

RELEASE

BNS-OD-FC001/A4



### 1. DESCRIPTION

The 6N137-L consists of a high efficient AlGaAs Light Emitting Diode and a high speed optical detector. This design provides excellent AC and DC isolation between the input and output sides of the Optocoupler. The output of the optical detector features an open collector Schottky clamped transistor. The enable function allows the optical detector to be strobed. A guaranteed common mode transient immunity is up to 10kV/µs at 3.3V.

The Optocoupler operational parameters are guaranteed over the temperature range from -40°C ~ +85°C.

### 1.1 Features

- 3.3V / 5V Dual Supply Voltages
- Low power consumption
- High speed 15MBd typical
- 10kV/µs minimum Common Mode Rejection (CMR) at V<sub>CM</sub> = 1000V
- Guaranteed AC and DC performance over temperature -40°C ~ +85°C.
- LVTTL/LVCMOS Compatible.
- Available in Dual-in-line, Wide lead spacing, Surface mounting package.
- Strobable output.
- Safety approval

UL/ cUL 1577, 5000 Vrms/1 min

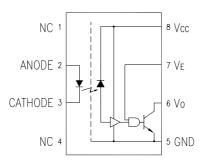
VDE DIN EN60747-5-5, V<sub>IORM</sub> = 567 Vpeak

## 1.2 Applications

- Isolation in line receivers
- Digital isolation for A/D, D/A conversion
- Ground loop elimination
- Feedback Element in Switching Mode Power Supplier
- Pulse transformer replacement
- Power transistor isolation in motor drives
- Interface between Microprocessor system, computer and their peripheral

### 1.3 Functional Diagram

Pin No. and Internal connection diagram



Truth Table (Positive Logic)

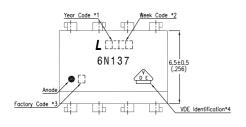
LED	ENABLE	OUT
ON	Н	L
OFF	Н	Н
ON	L	Н
OFF	L	Н
ON	NC	L
OFF	NC	Н

A  $0.1\mu F$  bypass Capacitor must be connected between Pin8 and Pin5

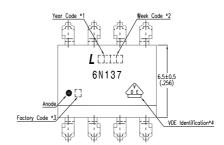


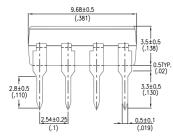
## 2. PACKAGE DIMENSIONS

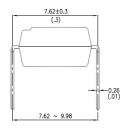
### 2.1 6N137-L

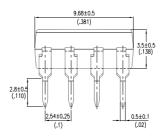


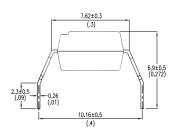
### 2.2 6N137M-L



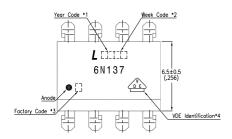


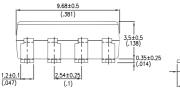


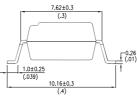




## 2.3 6N137S-L







### Notes:

- 1. Year date code.
- 2. 2-digit work week.
- 3. Factory identification mark shall be marked (W: China-CZ, Y: Thailand)
- 4. VDE option.

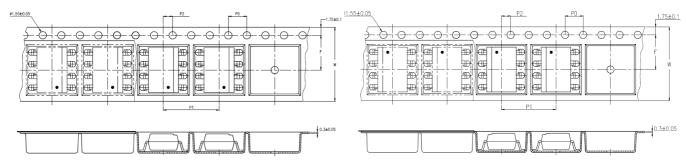
Dimensions in millimeters (inches).



## 3. TAPING DIMENSIONS

## 3.1:6N137S-TA-L





Description	Symbol	Dimension in mm (inch)
Tape wide	W	16±0.3 (0.63)
Pitch of sprocket holes	P <sub>0</sub>	4±0.1 (0.15)
Distance of compartment	F	7.5±0.1 (0.295)
Distance of compartment	$P_2$	2±0.1 (0.079)
Distance of compartment to compartment	P <sub>1</sub>	12±0.1 (0.472)

### 3.3 Quantities Per Reel

Package Type	TA/TA1
Quantities (pcs)	1000



## 4. RATING AND CHARACTERISTICS

## 4.1 Absolute Maximum Ratings at Ta=25℃ \*1

	Parameter	Symbol	Rating	Unit	Note
	Average Forward Input Current	I <sub>F</sub>	20	mA	2
	Reverse Input Voltage	$V_R$	5	V	
Input	Power Dissipation	Pı	40	mW	
	Enable Input Voltage	V <sub>E</sub>	Vcc+0.5	V	
	Enable Input current	Ι <sub>Ε</sub>	5	mA	
	Output Collector Current	Ιο	50	mA	
Output	Output Collector Voltage	Vo	7	V	
	Output Collector Power Dissipation	P <sub>o</sub>	85	mW	
	Isolation Voltage	$V_{iso}$	5000	$V_{rms}$	
	Supply Voltage	$V_{CC}$	7	V	
	Operating Temperature	$T_{opr}$	-40 ~ +85	°C	
	Storage Temperature	$T_{stg}$	-55 ~ +125	°C	
	Lead Solder Temperature *2	$T_{sol}$	260	°C	

- Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings
  can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time
  can adversely affect reliability.
- 2. 260°C for 10 seconds. Refer to Lead Free Reflow Profile.



## **4.2 Recommended Operating Conditions**

Parameter	Symbol	Min	Max	Unit
Operating Temperature	T <sub>A</sub>	-40	85	°C
Supply Voltage	W	2.7	3.6	V
Supply Voltage	V <sub>cc</sub>	4.5	5.5	V
Low Level Input Current	I <sub>FL</sub>	0	250	μΑ
High Level Input Current	I <sub>FH</sub>	5	15	mA
Low Level Enable Voltage	V <sub>EL</sub>	0	0.8	V
High Level Enable Voltage	$V_{EH}$	2	Vcc	V
Output Pull-up Resistor	$R_L$	330	4k	Ω
Fan Out (at R <sub>L</sub> =1kΩ per channel)	N	_	5	TTL Loads



### 4.3 ELECTRICAL OPTICAL CHARACTERISTICS at Ta = 25°C

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition
Input						
Input Forward Voltage	$V_{F}$	_	1.38	1.70	V	I <sub>F</sub> = 10mA
Input Forward Voltage Temperature Coefficient	$\Delta V_{F}/\Delta T$	_	-1.5	_	mV/ <sup>o</sup> C	I <sub>F</sub> = 10mA
Input Reverse Voltage	$BV_R$	5.0	_	_	V	I <sub>R</sub> = 10µA
Input Threshold Current	I <sub>TH</sub>	_	1.5	5	mA	$V_{E} = 2V, V_{CC} = 3.3V,$ $V_{O} = 0.6V$ $I_{OL} (sinking) = 13mA$
Input Capacitance	C <sub>IN</sub>	_	34	_	pF	$f = 1MHz, V_F = 0V$
Detector						
High Level Supply Current	I <sub>ссн</sub>	_	3.8	7	mA	$V_{\text{E}} = 0.5 \text{V}, \ V_{\text{CC}} = 3.3 \text{V},$ $I_{\text{F}} = 0 \text{mA}$
Low Level Supply Current	I <sub>CCL</sub>	_	5.8	10	mA	$V_E = 0.5V$ , $V_{CC} = 3.3V$ , $I_F = 10mA$
High Level Enable Current	I <sub>EH</sub>	_	-0.19	-1.6	mA	$V_{CC} = 3.3V, V_E = 2V$
Low Level Enable Current	I <sub>EL</sub>	_	-0.41	-1.6	mA	$V_{CC} = 3.3V, V_E = 0.5V$
High Level Enable Voltage	$V_{EH}$	2	_	_	V	
Low Level Enable Voltage	V <sub>EL</sub>		_	0.8	V	
High Level Output Current	Іон	_	5	100	μΑ	$V_E = 2V$ , $V_{CC} = 3.3V$ , $V_O = 3.3V$ , $I_F = 250\mu A$
Low Level Output Voltage	V <sub>OL</sub>	_	0.3	0.60	V	$V_E = 2V$ , $V_{CC} = 3.3V$ , $I_F = 5mA$ , $I_{OL}$ (sinking) = 13mA

Specified over recommended temperature ( $T_A$  = -40°C to +85°C, 2.7V  $\leq$  V<sub>CC</sub>  $\leq$  3.6V), I<sub>F</sub> = 7.5mA unless otherwise specified. All typicals at  $T_A$  = 25°C, V<sub>CC</sub> = 3.3V.



Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition
Input						
Input Forward Voltage	$V_F$	_	1.38	1.70	V	I <sub>F</sub> = 10mA
Input Forward Voltage Temperature Coefficient	$\Delta V_{F}/\Delta T$	_	-1.5	_	mV/ <sup>o</sup> C	I <sub>F</sub> = 10mA
Input Reverse Voltage	$BV_R$	5.0	_	_	V	I <sub>R</sub> = 10μA
Input Threshold Current	I <sub>TH</sub>	_	1.35	5	mA	$V_{CC}$ =5.5V, $V_{O}$ = 0.6V $I_{OL}$ > 13mA
Input Capacitance	C <sub>IN</sub>	_	34	_	pF	$f = 1MHz, V_F = 0V$
Detector						
High Level Supply Current	Іссн	_	6.1	10	mA	$V_E = 0.5V, V_{CC} = 5.5V, I_F = 0mA$
Low Level Supply Current	I <sub>CCL</sub>	_	8.3	13	mA	$V_E = 0.5V$ , $V_{CC} = 5.5V$ , $I_F = 10mA$
High Level Enable Current	I <sub>EH</sub>	_	-0.6	-1.6	mA	$V_{CC} = 5.5V, V_E = 2V$
Low Level Enable Current	I <sub>EL</sub>	_	-0.9	-1.6	mA	$V_{CC} = 5.5V, V_E = 0.5V$
High Level Enable Voltage	$V_{EH}$	2	_	_	V	
Low Level Enable Voltage	V <sub>EL</sub>		_	0.8	V	
High Level Output Current	Іон	_	0.9	100	μΑ	$V_E = 2V$ , $V_{CC} = 5.5V$ , $V_O = 5.5V$ , $I_F = 250\mu A$
Low Level Output Voltage	V <sub>OL</sub>	_	0.4	0.60	V	$V_{CC}$ =5.5V, $I_F$ = 5mA, $I_{OL}$ (sinking) = 13mA

Specified over recommended temperature ( $T_A$ = -40°C to +85°C, 4.5V  $\leq$  V<sub>CC</sub>  $\leq$  5.5V), I<sub>F</sub> = 7.5mA unless otherwise specified. All typicals at  $T_A = 25$ °C,  $V_{CC} = 5.0$ V.



## 5. SWITCHING SPECIFICATION

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition	Note
Propagation Delay Time to High Output Level	t <sub>PLH</sub>	25	48	90	ns		3
Propagation Delay Time to Low Output Level	t <sub>PHL</sub>	25	35	75	ns		4
Pulse Width Distortion	t <sub>PLH</sub> - t <sub>PHL</sub>	_	13	_	ns	$R_L = 350\Omega$ , $C_L = 15pF$	_
Propagation Delay Skew	t <sub>PSK</sub>	_	_	40			_
Output Rise Time (10 to 90%)	t <sub>r</sub>	_	21	_	ns		_
Output Fall Time (90 to 10%)	t <sub>f</sub>	_	6.6	_	ns		_
Propagation Delay Time of Enable from $V_{\rm EH}$ to $V_{\rm EL}$	t <sub>ELH</sub>	_	27	_	ns	$R_L = 350\Omega, C_L = 15pF,$ $V_{EL} = 0V, V_{EH} = 3V$	5
Propagation Delay Time of Enable from $V_{\text{EL}}$ to $V_{\text{EH}}$	t <sub>EHL</sub>	_	9	_	ns	$R_L = 350\Omega, C_L=15pF,$ $V_{EL} = 0V, V_{EH} = 3V$	6

Specified over recommended temperature ( $T_A$  = -40°C to +85°C, 2.7V  $\leq$  V<sub>CC</sub>  $\leq$  3.6V), I<sub>F</sub> = 7.5mA unless otherwise specified. All typicals at  $T_A$  = 25°C, V<sub>CC</sub> = 3.3V.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Co	ondition	Note
Propagation Delay Time to High	t <sub>PLH</sub>	25	40	75	ns	TA = 25°C	$R_L=350\Omega$ ,	3
Output Level	VPLH	_	_	100	113		C <sub>L</sub> =15pF	3
Propagation Delay Time to Low	4	25	32	75	no	TA = 25°C	$R_L$ =350 $\Omega$ ,	4
Output Level	t <sub>PHL</sub>	_	_	100	ns		C <sub>L</sub> =15pF	4
Pulse Width Distortion	t <sub>PLH</sub> - t <sub>PHL</sub>	_	8	_	ns			_
Propagation Delay Skew	t <sub>PSK</sub>	_	_	40		R <sub>L</sub> = 350Ω, C	. =15pF	_
Output Rise Time (10 to 90%)	t <sub>r</sub>	_	22	_	ns	R <sub>L</sub> = 350Ω2, G <sub>L</sub> = 15μF		_
Output Fall Time (90 to 10%)	t <sub>f</sub>	_	6.9	_	ns			_
Propagation Delay Time of Enable from $V_{\text{EH}}$ to $V_{\text{EL}}$	t <sub>ELH</sub>	_	28	_	ns	$R_L = 350\Omega$ , $C_{EL} = 0V$ , $V_{EH}$	- • •	5
Propagation Delay Time of Enable from V <sub>EL</sub> to V <sub>EH</sub>	t <sub>EHL</sub>	_	12	_	ns	$R_L = 350\Omega$ , $C_{EL} = 0V$ , $V_{EH}$		6

Specified over recommended temperature ( $T_A$  = -40°C to +85°C, 4.5V  $\leq$  V<sub>CC</sub>  $\leq$  5.5V), I<sub>F</sub> = 7.5mA unless otherwise specified. All typicals at  $T_A$  = 25°C, V<sub>CC</sub> = 5.0V.

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition	Note	
Logic High Common Mode	CM <sub>H</sub>	10	15	_	kV/uo	- kV/μs	$V_{CC} = 3.3V$ $V_{CM} = 1000V$ $R_{L} = 350\Omega$ $I_{F} = 0mA$ $T_{A} = 25^{\circ}C$	7
Transient Immunity	ЮМН	10	15	_	ΚV/μ3	$V_{CC} = 5V$ $V_{CM} = 1000V$ $R_L = 350\Omega$ $I_F = 0mA$ $T_A = 25^{\circ}C$	7	
Logic Low Common Mode	ICM I	10	15	_		$V_{CC} = 3.3V$ $V_{CM} = 1000V$ $R_{L} = 350\Omega$ $I_{F} = 10.0mA$ $T_{A} = 25^{\circ}C$	- 8	
Transient Immunity	CM <sub>L</sub>	kV/μs	$V_{CC} = 5V$ $V_{CM} = 1000V$ $R_L = 350\Omega$ $I_F = 10.0mA$ $T_A = 25^{\circ}C$	0				



### 6. ISOLATION CHARACTERISTIC

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition	Note
Input-Output Insulation Leakage				4.0		45% RH, t = 5s,	0
Current	I <sub>I-O</sub>	_	_	1.0	μA	$V_{I-O} = 3kV DC, T_A = 25^{\circ}C$	9
Withstand Insulation Test	M	5000			V	RH ≤ 50%, t = 1min,	0.40
Voltage	$V_{ISO}$	5000	_	_	$V_{RMS}$	T <sub>A</sub> = 25°C	9, 10
Input-Output Resistance	R <sub>I-O</sub>	_	10 <sup>12</sup>	_	Ω	V <sub>I-O</sub> = 500V DC	9,
Input-Output Capacitance	C <sub>I-O</sub>	_	1.0	_	р	f = 1MHz, T <sub>A</sub> = 25°C	9,

Specified over recommended temperature (T<sub>A</sub> = -40°C to +85°C) unless otherwise specified. Typical values applies to T<sub>A</sub> = 25°C

### Notes

- 1. A  $0.1\mu F$  or bigger bypass capacitor for  $V_{CC}$  is needed as shown in Fig.1
- 2. Peaking driving circuit may be used to speed up the LED. The peak drive current of LED may go up to 50mA and maximum pulse width 50ns, as long as average current doesn't exceed 20mA.
- 3. t<sub>PLH</sub> (propagation delay) is measured from the 3.75 mA point on the falling edge of the input pulse to the 1.5 V point on the rising edge of the output pulse.
- 4. t<sub>PHL</sub> (propagation delay) is measured from the 3.75 mA point on the rising edge of the input pulse to the 1.5 V point on the falling edge of the output pulse.
- 5. The t<sub>ELH</sub> enable propagation delay is measured from the 1.5 V point on the falling edge of the enable input pulse to the 1.5 V point on the rising edge of the output pulse.
- 6. The t<sub>EHL</sub> enable propagation delay is measured from the 1.5 V point on the rising edge of the enable input pulse to the 1.5 V point on the falling edge of the output pulse.
- 7. CM<sub>H</sub> is the maximum tolerable rate of rise of the common mode voltage to assure that the output will remain in a high logic state (i.e., VO > 2.0 V).
- 8. CM<sub>L</sub> is the maximum tolerable rate of fall of the common mode voltage to assure that the output will remain in a low logic state (i.e., VO < 0.8 V).
- 9. Device is considered a two-terminal device: pins 1, 2, 3 and 4 shorted together, and pins 5, 6, 7 and 8 shorted together.
- 10. In accordance with UL1577, each optocoupler is proof tested by applying an insulation test voltage 5250Vrms for one second (leakage current less than 10 μA). This test is performed before the 100% production test for partial discharge



## 7. SWITCHING TIME TEST CIRCUIT

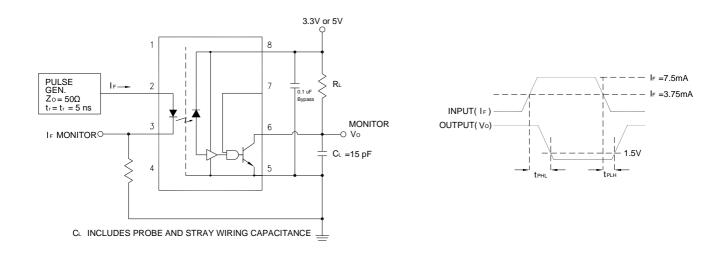


Figure 1: Test Circuit for tPHL and tPLH

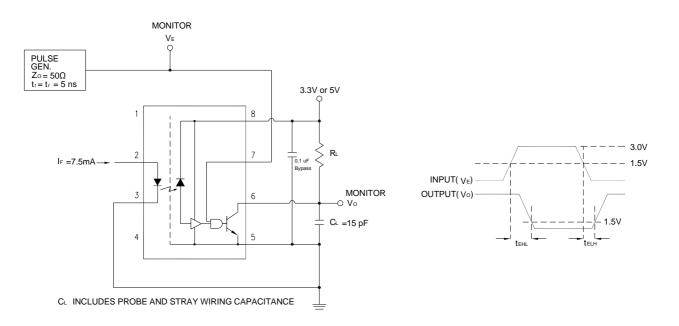


Figure 2: Single Channel Test Circuit for Common Mode Transient Immunity



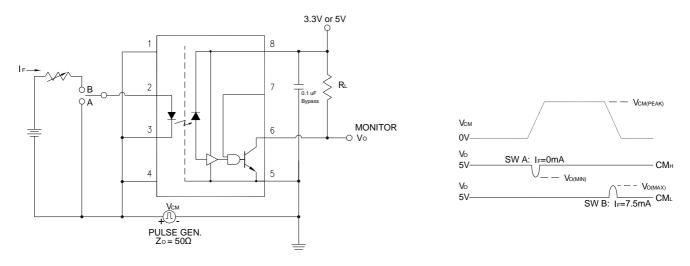
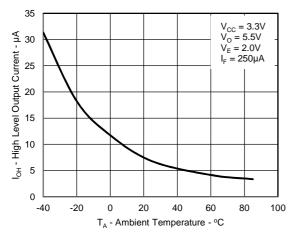


Figure 3: Single Channel Test Circuit for Common Mode Transient Immunity



## 8. CHARACTERISTIC CURVES



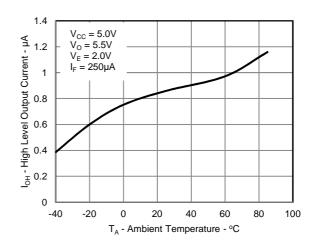
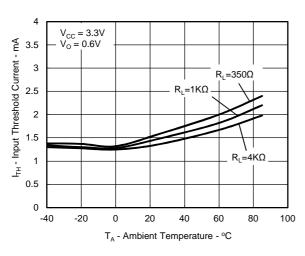


Figure 4: Typical High Level Output Current vs. Ambient Temperature



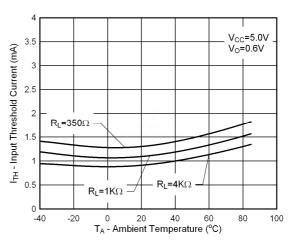
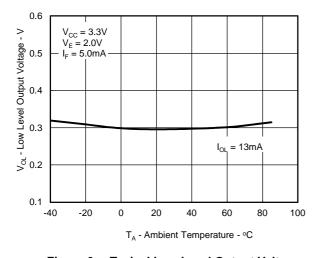


Figure 5: Typical Input Diode Threshold Current vs. Ambient Temperature



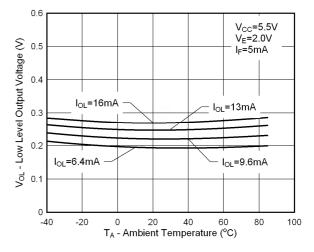
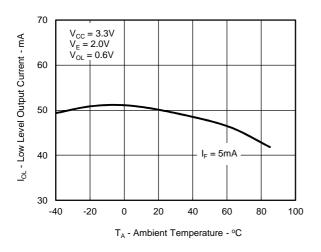


Figure 6: Typical Low Level Output Voltage vs. Ambient Temperature





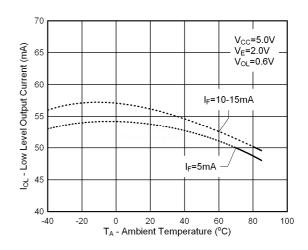
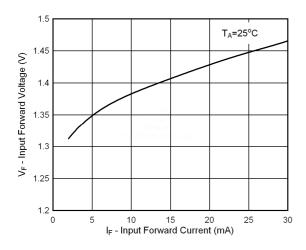


Figure 7: Typical Low Level Output Current vs. temperature



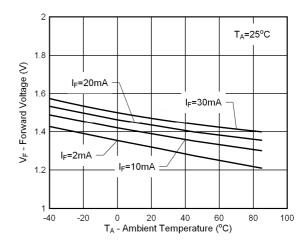
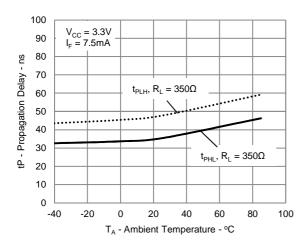


Figure 8: Typical Input Diode Forward Characteristic

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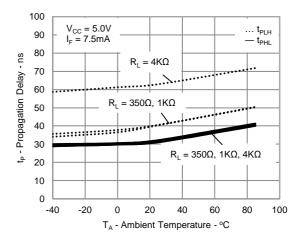
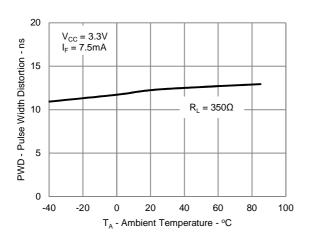


Figure 9: Typical Propagation Delay vs. Ambient Temperature



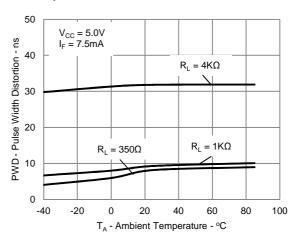


Figure 10: Typical Pulse Width Distortion vs. Ambient Temperature

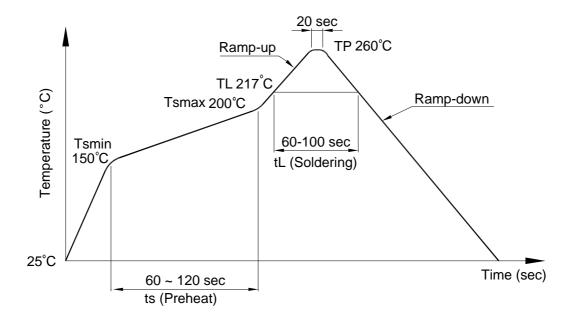


## 9. TEMPERATURE PROFILE OF SOLDERING

## 9.1 IR Reflow soldering (JEDEC-STD-020C compliant)

One time soldering reflow is recommended within the condition of temperature and time profile shown below. Do not solder more than three times.

Profile item	Conditions
Preheat	
- Temperature Min (T <sub>Smin</sub> )	150°C
- Temperature Max (T <sub>Smax</sub> )	200°C
- Time (min to max) (ts)	90±30 sec
Soldering zone	
- Temperature (T <sub>L</sub> )	217°C
- Time (t∟)	60 ~ 100 sec
Peak Temperature (T <sub>P</sub> )	260°C
Ramp-up rate	3°C / sec max.
Ramp-down rate	3~6°C / sec





## 9.2 Wave soldering (JEDEC22A111 compliant)

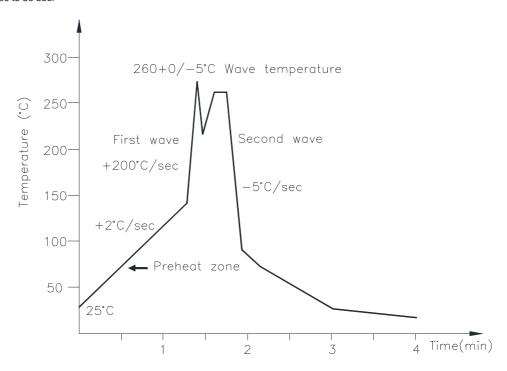
One time soldering is recommended within the condition of temperature.

Temperature: 260+0/-5°C

Time: 10 sec.

Preheat temperature:25 to 140°C

Preheat time: 30 to 80 sec.



## 9.3 Hand soldering by soldering iron

Allow single lead soldering in every single process. One time soldering is recommended.

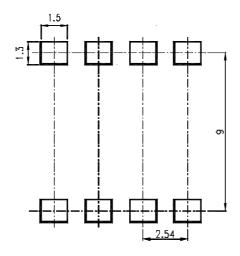
Temperature: 380+0/-5°C

Time: 3 sec max.

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## 10. RECOMMENDED FOOT PRINT PATTERNS (MOUNT PAD)



Note:

Dimensions in millimeters.



## 11. NAMING RULE

Part Number Options
6N13
6N137M
6N137S-TA
6N137S-TA1
6N137-V
6N137M-V
6N137STA-V
6N137TA1-V

Definition of Suffix	Remark
"6N137 "	LiteOn model name
"No Suffix"	Dual-in-Line package clearance distance 7 mm typical
"M"	Wide lead spacing package clearance distance 8 mm typical
"S"	Surface mounting package clearance distance 8 mm typical
"TA"	Pin 1 location at lower right of the tape
"TA1"	Pin 1 location at upper left of the tape
"V"	VDE approved option

## 12. NOTES

- LiteOn is continually improving the quality, reliability, function or design and LiteOn reserves the right to make changes without further notices.
- The products shown in this publication are designed for the general use in electronic applications such as office automation equipment, communications devices, audio/visual equipment, electrical application and instrumentation.
- For equipment/devices where high reliability or safety is required, such as space applications, nuclear power control equipment, medical equipment, etc, please contact our sales representatives.
- When requiring a device for any "specific" application, please contact our sales in advice.
- If there are any questions about the contents of this publication, please contact us at your convenience.
- The contents described herein are subject to change without prior notice.
- Immerge unit's body in solder paste is not recommended.