

Working Title - Hardware Acceleration: Inferring an Emergency Vehicle Classifier

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Abstract— Hardware accelerators offer a unique solution to the problem of object detection in computer vision applications. This research focuses on a machine learning application to recognize emergency vehicles on the road. As the number of distracted-related deaths continue to rise, driver safety features are of interest for automotive product companies.

Index terms—hardware acceleration, computer vision, machine learning, neural network

I. INTRODUCTION

The purpose of this project is to explore the field of computer vision to implement object tracking on a Gentex Full Display Mirror® (FDM®). This product offers an unobstructed rearward view of the road. At a high-level, a camera feeds video onto the rearview mirror device. The goal of this project is to implement a neural network capable of detecting object movement across a screen over time. This novel feature has the potential of alerting drivers to moving objects while reserving their vehicle or identifying emergency vehicles in the rearview mirror. This project has three objectives:

1. Develop and train a machine learning model to track objects over time (Python development)
2. Deploy the model onto hardware using a combination of microcontroller and FPGA (HLS/HDL development)
3. Assess the performance on a Gentex FDM® for viability

Computer vision is a powerful tool enabling a computer to mimic the way humans see. the modern implementation of computer vision relies on machine learning. An sensor device captures an image and processing is done on an interpreting device. A neural network algorithm performs the processing, providing useful information about the image [1]. There are four main categories of computer vision applications: image classification, object detection, object tracking, and optical character recognition [1]. For our purposes, we are focused on object tracking. First, an object

must be detected in a single image. Next, our object must be followed throughout subsequent images. Finally, our device provides useful information in order to perform specific tasks based on our object tracking data [2]. There are several ways of implementing a model onto an FPGA: using kernel managed by a CPU (HLS), or writing a model in VHDL (HDL). FPGA kernels are not just instructions, but digital circuits (hence why we have pragmas, subset of C/C++).

REFERENCES

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- [2] “What is Computer Vision? | IBM --- ibm.com,” 2023. (\url{https://www.ibm.com/topics/computer-vision})