



Juan Raul Carrasco

MECHATRONICS ENGINEER

Details

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NATIONALITY

Bolivien

DATE OF BIRTH

08/05/1991

Links

[LinkedIn](#)

Skills

C/C++

Python

Java

Languages

Spanish

English

French

Hobbies

Sports (Football, Tennis, Gym,
Crossfit, Jogging)

Profile

Mechatronics engineer with a Master's degree in Wireless Embedded Technologies. During my professional career, I contributed to the construction of automated machines, and connected sensors for the industry. Excellent technical and communication skills.

Education

MS in Wireless Embedded Technologies, Ecole Polytechnique, Nantes - France

SEPTEMBER 2019 – JULY 2020

Postgraduate course in GNU/Linux, Universidad Católica Boliviana (U.C.B), La Paz - Bolivia

APRIL 2018 – MARCH 2019

BEng in Mechatronics, Universidad Católica Boliviana (U.C.B), La Paz - Bolivia

FEBRUARY 2010 – MARCH 2016

Employment History

Developer, SII Laboratories, Nantes - France

SEPTEMBER 2019 – JULY 2020

Internship at an electronics laboratory. Integration of the NFC and Lora communications protocols on an electronic's development board based on Linux. Device driver development for the NFC chip integration. Development of the user-space applications on QT. And development of a Smartphone application on Android.

Developer, Multimec S.R.L., La Paz - Bolivia

JUNE 2015 – JUNE 2019

Construction of structures and automated machines for the industry. I contributed with the electric, electronics and automation part of the machine development. In parallel, I was also in charge of designing and developing connected sensors for different industries and laboratories.

Developer, SMC Automation and Control, La Paz - Bolivia

AUGUST 2014 – MARCH 2015

Internship at an automation and control enterprise. I was in charge of programming the machine's controllers (PLCs) and building the electric boards.

Programming Languages

Shell (Bash), Assembly, Html, CSS, JS, VHDL

Software

Microsoft Office, Latex, PSIM, Matlab, Proteus, Android Studio, NI LabView, Solidworks

Hardware

Arduino, Pic, Pycom, Raspberry Pi, Beaglebone, FPGA (Zynq 7000, Artix v-7)