

# Juan Agüero

Senior Design Engineer

## Contact

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juansta

## Programming

♥C++, C  
ASM ARM & AVR  
JavaScript  
Matlab, Latex  
VHDL (hobby)

## Tools

git, gtest,  
Qt, make, vim,  
Altium, Eagle  
gitlab, jenkins,  
apache, nginx  
HTML, PHP, CSS  
SQL

## Platforms

Cypress ARM,  
BeagleBone,  
rPi, Arduino,  
AVR USB,  
Cypress USB,  
Win, Linux

## Languages

English  
Spanish

## interests

To secure a role within a reputable organisation where I can apply my skills and experience in Electronics in a R&D environment. There is nothing more satisfying than being able to see a product from its initial conceptual stages through to manufacturing. The ideal role would also provide a challenging environment that allows both personal and professional growth and development.

## education

2013–2013	Laboratory Measurement Uncertainties <i>mention ISO here</i>	National Measurement Institute
1999–2007	Bachelor of Engineering <i>Software and Electronics</i>	The University of Technology, Sydney
1999–2007	Diploma of Engineering Practice <i>Professional</i>	The University of Technology, Sydney

## experience

2006–Now	Acoustic Research Labs <i>Senior Design Engineer</i>	Pennant Hills, NSW
<ul style="list-style-type: none"><li>• Responsible for the establishment and oversight of all engineering practices</li><li>• Advise management in the areas of Standards Adoption, Staff Development, Product Identification and Production</li><li>• Selection of Engineering Standards, Procedures and Workflows to be adopted</li><li>• Provide guidance and support to engineering staff</li><li>• Ensuring technical compliance with the Company's ISO-9001 based Quality Assurance system</li><li>• Produced product roadmaps and coordinated product development with Project and Product Managers through identification of potential commonalities and reuse between products and projects</li><li>• Advised on the standardisation of tools, utilities and infrastructure to support engineering and production activities</li></ul>		
2001–2006	Tyco Services - Wormald <i>Junior Systems Engineer</i>	Rydalmere, NSW

- System level safety critical design

## skills

- Excellent working knowledge of the Systems Engineering process as applied to electronic systems containing both hardware and software
- Skilled at policy and report writing
- Excellent communication, interpersonal and Negotiation skills
- Excellent Analytical/Problem Solving skills with great attention to detail