







1 2 3 4 5 6 7	8
ChangeLIST	
	A
V01	
- Inital version	
V02	
- Removed C45 (47uF) and C46(47uF)	
- Changed C12 from 0402 (10uF) to 0603(10uF)	
- Changed C1 and C2 from 15pf to 10pf	
- Changed X1 from X1A000061000200 to Q-SC20S0322070AAAF	
- Changed L1 from 0.47uH to 4.7uH	
- Routed the SYNC pin from DW1000 to a castellation - Routed the LIS3DH_INT2 pin from LIS3DH to a castellation	В
- Tie the DW1000 pin 31 and pin 42 internally in the module and Routed the VDDIO to a castellation	В
- Routed the MCU_BOOT0 to a castellation	
- Connected the PA4 to a castellation	
2018/11/15 - Changed U1 from STM32L031G6U6 to STM32L041G6U6STR	
- Removed C4(0.1uF)	
- Routed the PB4&PB5 to a castellation	
- Re-draw the Module Pinout	
V03	
- Changed C20&C21 from 12pf to 15pf	
- Swap pins PA2(LIS3DH_CS, pin 8) and PA15(LIS3DH_INT1, pin 23)	
- Power the Accelerometer via PA4	
20190426 C	c
- Removed C40	
- Changed the power name of DW1000 from VDD_3V3 to VDD_DW	
- Changed the power name of MCU from VDD to VDD_MCU	
- Separated power pins for MCU and DW1000	
- Connected ADC1, ADC2 and ADC3 inputs of LIS3DH to module pins	
- Re-draw the Module Pinout	
2019/05/14	
- Changed the DW1000 VDDPA pin and nets	
	D
PCB名称: PCB型号: LSD	9EV-DW1000T0
立项编号: PCB版本:	Cannot open file ClipboardPicture2014-4-17 上午 09:45:29
制图人员: 邱 满刚 制图日期: 2018/10/11 纸张型号: A3	上午 09:45:29
审核人员: 审核日期: 共 <u>5</u> 页 第 <u>5</u>	页