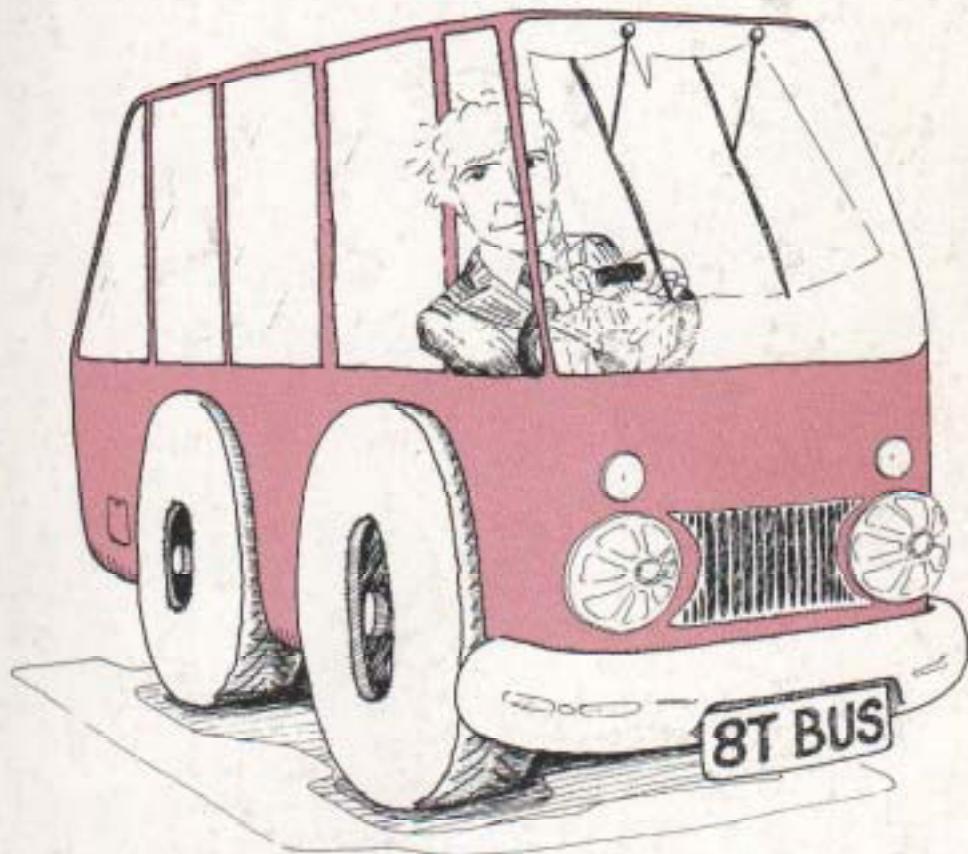


80-BUS NEWS

JANUARY–FEBRUARY 1984

VOL. 3 ISSUE 1

- NASCOM ASSEMBLERS REVIEWED
- POLYDOS FILE UPDATE PROGRAM
- CP/M DIS-ASSEMBLER REVIEWED
- LAWRENCE!



The Magazine for
NASCOM & GEMINI USERS

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80-BUS NEWS

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Letters to the Editor

Please note that occasionally there may be a considerable interval between the receipt of a letter and the time that it materialises in this column. Consequently events that have occurred in the meantime may make the letter out of date. If this is the case we apologise for any inconvenience thus caused.

MBASIC, 16 bit, & other bits

I was pleased to find that at least one 80-BUS user had read my article on disk MBASIC (Letters - 80-BUS News Vol.2, Iss.4).

The version of MBASIC that I use is 5.21 (July 1981) and it really is possible to include comments on the same line as program statements without, as Mr Stuckey suggests, the obligatory colon before the apostrophe. This appears in all manuals I have seen which have been issued after 1977 and applies to both Disk and 'Extended' versions only. It is also available in a number of mainframe BASICs (such as PDP and DEC - which I have used for more than 10 years).

It will be very interesting to see what transpires in the way of high level languages and operating system(s) as support for the promised 80-bus 16 bit CPU card (from Gemini, I understand, although they denied having any such revolutionary ideas a few months ago!). I suspect that Pascal will be available, and possibly Fortran as well (since Prospero Software already have a 16-bit PASCAL available and it would be useful to have a 16-bit version of their excellent implementation of FORTRAN when they get round to it); it would be a pity if some form of BASIC will not be supported, but it seems probable that its use will be confined to 8-bit machines, to die a lingering death. Will we need to spend lots of money on CP/M-86 or will MSDOS be the preferred operating system - and will our 8-bit CP/Ms be totally redundant? What about compatibility with our AVCs, MAP RAM cards and colour cards? I look forward to hearing more about it - price, estimated launch date, etc.

Does anyone actually use COMAL-80? I noticed that Atherton's book was mentioned in the last issue of 80-BUS News - and having used the aforementioned text, I rapidly came to the conclusion that it was better to learn PASCAL and forget that COMAL ever existed since it is, in reality, only a "souped-up" and rather poor subset of BASIC with vaguely Pascal-like structuring. A comparison between interpreted MBASIC and COMAL-80 using the PCW benchmark programs showed that COMAL was significantly slower on almost all the programs apart from BM8 (which involved some work with intrinsic functions) where it was twice as fast as the BASIC, taking 27 seconds to carry out a 1000 times loop with log, sine and exponentiation.

Yours sincerely, P D Coker, Farnborough, Kent

[Ed. - I note from a magazine that Prospero are advertising their Pascal and Fortran in both 8 and 16 bit forms - it is to be hoped that the 16 bit versions are not just code conversions of the 8 bit versions, if this is the case they are likely to be slower!]

Angry at Nascom

I am writing to you to air my anger and frustration at Lucas Logic. I have been a faithful supporter of Nascom for 5 years, but Lucas Logic's absence from the PCW Computer Show was the final straw.

With the launching of two new products - the LX Printer and the LX80 MicroComputer - it was the ideal platform to advertise. Obviously they couldn't give a damn!

Lucas Logic apparently have lost their commitment to Nascom Microcomputers and products. Their relationship with Dealers is appalling.

The sooner Nascom is bought by a decent company the better. In my opinion in their hands Nascom is dead.

Yours in extreme anger and disappointment,
Dr D Plews, MB ChB, Keighley, W Yorks

Rory a Sadist?

Whilst reading Rory O'Farrell's article "Happy Talk", I got the impression that he is some sort of sadist. The time taken to load a file into a disassembler, save it into 4K blocks as a hex file, then finally to transfer the file, must be at least 10 minutes per block.

For some time now, I have been working on a quick and cheap data transfer program for the home enthusiast. The transmitting of data is done via a BASIC program which reads any file type and transmits via the serial port. For reasons mentioned in Rory O'Farrell's article, the program cannot transmit pure binary object code, it is therefore necessary to convert the file to some form that may be recognised by PIP and not to send it Control Zs. The file is received on the second machine via the RDR: into a file. A small BASIC program is then required to convert the program back into its binary form.

Finally, a little tip for NASCOM CP/M users using the AVC board. When most people boot-up CP/M they type AVCTXT straight away to give an 80 column screen, or modify the BIOS via the config program to execute a file AUTO.SUB. You then create the file AUTO.SUB and enter the command AVCTXT.

Have you ever wondered why it takes 15-20 seconds to execute AVCTXT after boot-up? The reason is simple, when Auto load is specified, it sets a jump in the BIOS, the address jumped to is to load SUBMIT.COM, it is then the job of SUBMIT.COM to load AVCTXT and execute it, therefore, taking time loading two files to achieve one goal.

Pondering on this, I decided to delve into MOVCPM. I found the bytes which loaded SUBMIT.COM and changed these to AVCTXT and resaved MOVCPM. CP/M now boots-up AND executes AVCTXT in less than 5 seconds. The trick is:

DDT MOVCPM.COM

- D OA00

Notice that locations OA08 - OA17 contain SUBMIT AUTO.
Change these locations to the following:-

OA07	- This is the length of the command to be executed
OA08 41 A	
OA09 56 V	
OA0A 43 C	
OA0B 54 T	
OA0C 58 X	
OA0D 54 T	
OA0E 2E	

SAVE 41 MOVCPM.COM

MOVE 55 *

SYSGEN

SOURCE (press enter)

DESTINATION B

Yours faithfully, P.A. Dutton, Northfield, Birmingham.

Re:C.B. A.K.A. Dr.D

In a recent hard-copy interchange with that super-programmer, peerless hardware hack and expert wine connoisseur Chris Blackmore, the subject of software exchange loops cropped up. Chris suggested I write to you and deal directly "with the Mafiosi who have a firm grip on the throat of 80-BUS NEWS" concerning non-CP/M exchange loops. Right, here goes.

My N-1 (yes, there are still a few around!) is fitted with a GM805 disk drive and runs D-DOS (boo, hiss, boo), DCS-DOS and POLYDOS. I am currently in disk-to-disk contact with one Alan Wood Esq of St Mellons, Cardiff, using both DCS-DOS and POLYDOS. If an exchange loop exists for either or both of these DOS formats, would you put me (us) in contact with the organisers. Alternatively, if said loops do not exist you could direct any interested parties my way and I'll have a bash at setting a loop (or loops) going, 'like wot yer doc. did for CP/M'. Ah, fame at last.

I will start an exchange loop, and I will also name it unto you. And it will be called TOROID FERRUGINOUS, the greatest software exchange in the history of time and space (or, the biggest anticlimax EVER).

Another point raised in conversation was the inordinately long time taken by 80-BUS to cough up the readies for articles published. Now I know that 99% of the contributors who write to you do so just to see their names in print and/or to help other readers. But please take note, C.B. and D.G.R. DO IT FOR VER MONEY!! Why do you think Chris Blackmore makes home-brew wine? It's because he's too poor to visit the local wine bar, okay, ya. And my Nascom rusts in the corner 'cos, 'cos, I can't afford to pay yer 'lectric bill. And all this just because you employ a disslexic, er, dislexic, um, diss-lexik accountant who doesn't know how to sighn, er sign a cheque!. I'm seriously thinking of trading in my Nascom for an I.B.M. 370!

Happy hacking, D.G. Richards, 29 Martin Crescent, Tonyrefail,
Mid. Glamorgan, South Wales, CF39 8NT.

Re. Easicomp Board

With regard to Mr A Brown's information on the Easicomp Sound Board, in part 2 of your I/O map (Vol.2, Iss.4), please note that, although the manual states that ports 10 and 11 (decimal) are used if NASIO is provided, I find that the board actually decodes ports 130 and 131 (82H and 83H). I assume from this that Easicomp changed the design of their board at some point, and hence boards of both types exist. Incidentally, in common with the PSG design published in INMC 80 News, Issue 5, I now use ports 8 and 9!

Many thanks for printing the "Nascom ROM BASIC Dis-assembled" articles! Although I had dis-assembled this previously, the added documentation is very helpful.

Regarding the contents of your magazine, I feel you are leaving many of your readers behind by printing lengthy articles on very specific topics. This is an unfortunate side-effect of the flexibility of an 80-BUS system. What about including a short questionnaire as part of your subscription form, to get a better idea of what equipment people own?

On a different note, I would be interested in Doctor Dark's program exchange idea, assuming you have had replies from anyone else interested in using tapes. While on the subject of software, I notice that you have been publishing very little software and even fewer software reviews. Is this because no-one is writing programs for the Nascom anymore?

Yours sincerely, Kevin Smith, Aberdeen.

Dealing with RFI

I am glad to see that the problem of RFI is being considered seriously (80-BUS News Vol.2, Iss.5) and delighted to learn that we may get an issue devoted to Amateur Radio. For a starter, may I suggest the following additional hints on the problem of RFI.

- 1) A metal case to enclose your computer is vital. Mine came from a Radio Rally, custom-made out of 16swg steel except for a 1/8th aluminium front panel for #15. This vast box measures 19x14.5x9 inches and leaves a lot of room for all the expansion boards.
- 2) Multi-core screened cable for keyboard, printer and etc, may be obtained from Maplins whose catalogue may be read (and purchased) at W.H.Smiths. Screened cable is a must for stopping RF radiating from lovely aerials dangling from your computer.
- 3) Ferrite rings (try Ambit International) are effective. All leads (power, cassette, tv/monitor) emerging from your case to be wound a few times around the rings, as near as the case as practicable.
- 4) A mains filter (Ambit). This is effecitve both ways. It stops RF escaping into the mains where the house wiring acts as a massive aerial and prevents the mains crud getting at your computer.
- 5) Desperate measure. In Wireless World, September 1983, G3NRW recommends a 150 pF capacitor between the +12 V rail and ground.
- 6) Another desperate measure. Try earthing all your bits (computer proper, tv/monitor, cassette, etc) at only one point.

RFI appears worst for amateurs at 70 MHz. I achieved a measure of success for that band with some of the above, plus critical computer/radio spacing and complete success by executing HALT! ON 144 MHz, I have a few S1 whistles, but presently the lid is off the box and a Hobbit mechanism dangling on an unscreened lead.

By the way, has anyone made Hisoft Pascal talk to the Hobbit?

Yours, G. Orford, Bristol.

A Lunatic Writes

Dear Dirtbags,

Oldsters, who are, like, total vacuum-heads and do not understand the problems of a young person of today, often get the wrong idea about Waldo 'D.R.' Dobbs, who is like me, man. They think that I have no romance in my soul, which is, like, this incredibly strange thing that I have inside my body. To them I say: "You are totally incorrect, man". It is, like, the total lack of 80-BUS NEWS that causes these sauzzball manifestations, man! Why, when I see these utterly horrible PLASTIC BOXES, I am, like, totally overcome with emotion. There are these worthless diseased MOLLUSCS all over the magazine stands in W.H. Nasties, reading about these disturbingly repulsive micros, man.

Degenerate reptile, man, I ask you to stop this senseless deprivation of REAL COMPUTERS that you are, like, doing to me, man.

Monstrously, hazardously, like, yours, man.

Waldo 'D.R.' Dobbs [No address given.]

Utters from the gutters

By Mick Waters

Before getting down to the meaty bits, I should say that I am one of Dr. Dark's dodos in that I have never before submitted anything for publication. In the past I haven't subscribed regularly to either the INMC or 80-BUS News and used to pick up my copies about once a year on my rare excursions to one of the London dealers. This year I made a resolution to persuade (con) a relative into buying me a subscription as a Christmas present and so now I should be able to read the latest scandal almost as it happens. I don't pretend to be an authority on Nascom hardware but have managed over the past five years or so to become reasonably acquainted with the way they work. Mind you, being an RAF technician, I am an expert on modern electronic techniques (circa 1960) and can write volumes on pentodes, triodes and other glass encapsulated transistors with heaters. Since I have been about 12 months behind everyone else, this article may be too late for solving the problems of a couple of readers who asked for advice as far back as the March/April 83 issue but here goes regardless.

Nascom/SIMON

The first problem(s) of interest came from S. Willmott and from the Sept/Oct 83 issue of Dr. Darks diary. First Dr. Dark, your problem with SIMON getting in the way when verifying a page-mode style RAM-DISK appears to me to be due to the fact that the Nascom computers do not support page mode and SIMON will be present (and generating a RAMDIS signal) no matter what page of RAM is selected. The answer? Read on.

Mr Willmott asked how SIMON may be removed from the system once its job is done. I did (yet) another mod to my N2 as I objected to losing 4k of RAM and also to the prospect of buying a page mode EPROM board for one chip. My solution is not very elegant but it works. Below is a table of advantages and disadvantages which should be weighed up before going any further:

For it

1. Its CHEAP. (Free in fact)
2. It doesn't involve hacking your faithful Nascom around.
3. It works.

Against it

1. The tape drive may no longer be used for anything else.
2. SIMON doesn't work apart from booting the system up as it effectively switches itself out.
3. You must power up with a disk in drive A.
4. Pressing RESET doesn't work any more. The only way to reset is to switch off then switch back on. This creates problems when a program goes into a loop as you can't then RESET and examine the memory.
5. This mod. may only be used if an IVC is fitted as chips are borrowed from the redundant Nascom video circuitry.

In my opinion, the advantages outweigh the disadvantages, you may not think so. If you decide to try this mod, proceed as follows:

1. Pull out the chips used for the video circuitry except IC8.
2. On LKS1 remove all links except for the link between pins 3 and 14.

3. Connect a wire from TP10 to LKS1 pin 4.
4. Remove IC8 and bend pins 1, 2 and 3 (carefully) out straight so that they will clear the socket and replace IC8.
5. Remove IC71 and do the same with pin 8.
6. Connect a wire from IC71/8 to IC8/2.
7. Touch solder a wire onto IC24/12 and connect the other end to IC8/1.
8. Touch solder a wire onto IC18//13 and connect the other end to IC8/3.

The effect of this mod is only to provide a chip select and a RAMDIS signal when the tape drive is on. Certainly on my machine, it is always on when powered up so SIMON is connected in and any RAM in the system occupying those addresses is disabled. Once SIMON is entered other than for booting up or once CP/M is loaded and started, port 0 is reset and SIMON disappears from the memory map. Plug in and power up and all should be as before (on a 60k max CP/M) provided SIMON is not used as a monitor. If the system is powered up without a disk in drive A, your machine will crash once port 0 is reset by SIMON.

Before a full 64k system can be installed, the cold boot loader must be modified to switch out SIMON before loading the CP/M system. To do this, perform the following sequence of operations:

1. Use MOVCPM to generate a 64k system. Allow room for SYS if you use it. Save the result as CPM64.COM.
 2. Load CPM64.COM under DDT or ZSID.
 3. Use the S command to modify the addresses given below to the values indicated.
- | | |
|---------|---------|
| 090B 18 | 097C 3E |
| 090C 6C | 097D 01 |
| 0979 AF | 097E 18 |
| 097A D3 | 097F 8D |
| 097B 00 | |
4. Use ^C or G0 and save the modified CPM64.COM.
 5. Use SYSGEN to put the file on drive A. This can be done by typing:
SYSGEN CPM64.COM
after which, specify drive A as the destination.
 6. Unplug & plug in again with the new system in the A drive and Hey Presto, you should have a 64K system up and running with no SIMON in the way. As an added bonus, it won't get in the way when paging RAM either.

Faulty RAM Board

My second bit of first-aid is probably of no use to Kevin Weatherhead who has, by now, probably cured his faulty RAM board himself. If not then here goes.

Kevin doesn't say which type of RAM board he is using, so I shall go into the Nascom RAM A & B boards and the Gemini GM802 64k RAM card. The problem is this. When Kevin powers up, his RAM card is full of rubbish as expected. He can write zeros to the card but not ones. Fortunately, all three RAM cards use the same principle so only a brief "how it works" is necessary.

When the Z80 wants to read data, the address lines contain the required address. This address is decoded by the RAM cards to select the appropriate RAM chips. The /WR signal from the Nasbus/80-bus provides the /WR signal to the RAM chips so that they accept data rather than send it. This signal will be a '1' when reading and this side appears to work. As Kevin can also write to the chips, albeit in a limited fashion, this appears to be switching correctly. Data to be written to the RAM chips is gated into the RAM after

buffering by a 74LS244 whose enables are always tied to 0v. When reading, the /RD signal is used to enable a similar LS244 for buffering the data leaving the card. Assuming that the LS244 is faulty, swapping the two buffers should give an indication of whether this is so. If on power up, all of the RAM appears to contain either all ones or all zeros and no attempt to change them succeeds then replace the LS244. A table of chip numbers for all three RAM cards is given below.

LS244's used as write buffers:

RAM A card
IC1

RAM B card
IC26

Gemini G802
IC44

If changing the chip specified doesn't cure the problem then I can only suggest that the board is returned to the manufacturers via your dealer.

CCPZ

The last bit of what appears to be a takeover bid by me concerns three bits of software that should by this time, be available from Henrys. The first item is about CCPZ. As stated by Dave in a previous issue, there is a bug in all versions (with the possible exception of the Gemini release version) up to V4.1. After this version, CCPZ was made available in Macro-80 source code (hooray) and had the bug fix in it together with an implementation of the "." command. Since then, another bug has come to light when using SUBMIT files with the GET command. Try typing:

```
GET ADDR FILENAME.TYP
DIR A:
DIR B:
```

If you get the first command repeated three times then you have the bug. This is because CCPZ changes the default DMA address when getting the requested file and forgets to change it back. The result is that further SUBMIT commands are read to memory above the last file read. CCPZ then checks the default buffer and of course finds the last command which is repeated. With version 4.3, this bug has been fixed and there is no reason why everyone shouldn't be using CCPZ now. Order yours tomorrow! (No I don't work for Henry's).

BDOSZ

My second bit of software news concerns a Z80 implementation of the CP/M BDOS, by some strange coincidence, called BDOSZ. In true CCPZ fashion, the space saved by doing the conversion has made room for extra goodies while still retaining CP/M 2.2 compatibility. This package came about as a result of the good fortune of Chris Bellingham of Canterbury, who found a part completed source version of something which would behave rather like the CP/M 2.2 BDOS (author unknown) in an ancient file tucked away in the memory banks of the VAX mainframe at the University of Kent. Chris was at that time running CP/M 1.4 with a second hand Henelec FDC and drives. Being on a students grant, he had never considered the change to CP/M 2.2 as being economically viable. Having found the BDOS and typed the source into his Nascom, there followed a good deal of hard work to finish the BDOS and debug it. Once completed and armed with CCPZ and a home brew BIOS, he had CP/M 2.2 for next to nothing.

At about this time, I obtained a copy for interest value. The error reporting was as Chris originally found it and if possible was worse than that in DRI's BDOS. Also, at the end of the BDOS was a large unused area which we thought might be able to provide some extra goodies. As Chris was busy, I got the job of upgrading BDOSZ to something that would cure the limitations placed on users by the standard CP/M BDOS.

After the usual amount of swearing and cursing, it was finished and working, the results of this are now available at a very modest price.

Now for what it does:

Users of CP/M will have realised by now that the BDOS's error trapping and reporting leave a lot to be desired. How many times have you tried to erase or change a file only to find that either the disk or file is read only? Worse still, if after 6 hours processing, your latest masterpiece tries to update its data file and finds it read only. Unless you have the facility to change the attributes, all of that time has been wasted because you can only reboot, set the file to R/W and start again. Finally, have you got any source files that you would like to assemble, producing listing files to disk, but the PRN file is larger than the capacity of your disk? Infuriating isn't it that you can't just change the disk and carry on?

BDOSZ attempts to overcome these and other problems so that your valuable time is not wasted and your trusty Nascom or Gemini isn't kicked around the floor in temper. An example of each of the BDOS error types and BDOSZ's actions in these cases are given below:

BDOS Err On x: Select

In this case, BDOSZ outputs the message "Drive x: select error" followed on a new line by "Enter valid drive or ^C". The BDOS will then wait for a key press and either log in the selected drive or perform a warm boot as appropriate. This facility is useful if a program being debugged causes a select error and the user wishes to continue testing it.

BDOS Err On x: R/O

In the case of a R/O disk, BDOSZ will print the message "Disk x: is set R/O" followed by "Do it anyway? (Y/N/^C)". If the user types "Y" then the disk will be reset and the function completed as though the error had never existed.

BDOS Err On x: File R/O

Under CP/M, if the command "ERA *.*" is given and one of the files to be deleted is R/O then the above message will be displayed. The CP/M BDOS doesn't tell you which file it is referring to. Under BDOSZ, the message "File FILENAME.TYP is set R/O" followed by the message "Do it anyway? (Y/N/^C)". If the user types "Y", the files R/O attribute bit is cleared and the file will be deleted, renamed or written to as requested as though the file had been R/W. If the user types "N" then the queried file will be left unchanged and still R/O. If, as in the above example, the command ERA *.* was issued, this facility allows the deletion of all R/W files and selected R/O files in the same command.

NOTE. BDOSZ will not query a file unless it is R/O.

BDOS Err On x: Bad Sector

This error is not considered recoverable and most BIOS's these days contain facilities to re-try. If your BIOS can't sort out the problem, then the BDOS has no chance. However, in an attempt to provide meaningful error messages, BDOSZ will display either "Disk x: read error" or "Disk x: write error". In either case, if ^C is pressed, a warm boot will be performed. Pressing any other key will cause the error to be ignored.

Other Errors

Two other errors of a potentially disastrous nature are treated as recoverable by BDOSZ. These are disk directory full and disk full. In the

former case, the BDOS will print the message "Disk x: directory full" and in the latter "Disk x: full". In both cases, the message "Change disks? (Y/N/^C)" will be displayed. If the user types ^C, the system will perform a warm boot. If "N" is typed, a standard "disk full" or "directory full" code will be returned to the CCP or calling program. If "Y" is typed, in the case of a full disk, the currently addressed file will be closed. In either case, when the BDOS is ready to proceed, it will display the message "Change disks then hit any key or ^C". If the user types ^C, the system will be rebooted. If not then a new disk should be in the current drive and BDOSZ will reset the new disk to make it R/W, log it in, create a new file on this disk with the same name as before and continue with the write. The calling program will not be aware of the disk change. BDOSZ will erase any file of the same name occurring on the replacement disk unless it is R/O. In this case, BDOSZ will query before deleting.

As standard, BDOSZ sends a BEL character (ASCII 7) to the console device for those users with a bleep facility to warn the user that an error has occurred.

BDOSZ has been in use now for about 12 months on three different machines. No bugs have been found to date and the extra facilities provided have proved invaluable.

[Ed. - a warning. Owners of Gemini systems with BIOSs of version 2.8 or greater beware! These BIOSs do a check on power-up to determine if, and how many (up to 4) Gemini GM833 'RAM-DISK' boards are present. If any are present the BIOS copies the CCP and BDOS to drive 'M' and modifies the BDOS to use this for warm boots. Whether this will work with BDOSZ or not, who knows?]

MDIS

The last piece of this software trilogy is for those of you tempted by Henrys adverts for MDIS. For those who haven't seen the ad., MDIS is a CP/M disassembler with differences. It includes all of the excellent features found in David Parkinson's NAS-DIS and then some. The last version of MDIS to be released was 2.1, version 2.2 was substituted after a small unexpected "feature" was discovered. But now, version 2.3 is available with oodles of extras. [Ed. - Since the time of writing, vers. 2.6 has appeared.] To those who haven't yet obtained a copy, you were possibly right to wait. For those who have, never mind, upgrades are available at the cost of a copy charge. Simply return it to the dealer you bought it from. Being somewhat biassed in favour of MDIS, I wouldn't attempt to review it but will provide a list of its features (note that features in this case isn't in quotes).

1. MDIS produces either Z80 or 8080 mnemonics as requested by the user but will default to the mnemonics used by the CPU in use.
2. Assembler source files may be produced. Current versions provide 100% compatibility with the Microsoft Macro-80 assembler.
3. Listing files may be directed to the CON:, PUN: or LST: devices or may be sent to a disk file.
4. Labels are produced automatically. Labels are a four digit hex number related to the address where the label is to be inserted. To make the labels assembler compatible, they are given an alphabetic prefix. One of four prefixes will be used for each label depending on whether MDIS thinks the label refers to code, data, both or doesn't know.
5. A cross-reference listing may be supplied which lists each label and each address where the label is used.
6. Allows data areas to be specified as either hex bytes, ASCII, an address table (with labels substituted for addresses) or a look-up table in the form:

byte
address
byte
address

Again, labels will be substituted for addresses in look-up tables.

7. On-screen editing using the cursor controls is available on all line-inputs to MDIS.
8. MDIS works with XSUB. (One in the eye for DISZILOG).
9. Allows tables of data area addresses and types to be redirected to come from a text file on disk. This facility may be combined with input under XSUB.
10. Screen paging is available if required but not to SYS standards. (Who needs both?)
11. MDIS allows printed listings to be stopped after each page so that single sheet paper may be changed.
12. Margins may be specified on all output listings so that mounting in ring binders is possible. A unique feature enables alternate pages to have margins so that if single sheet paper is in use, both sides may be used.
13. Listings are formatted so that data areas are separated from code by a blank line to improve readability. Additionally, blank lines are also inserted after JP (ss) instructions and any unconditional jump, jump relative or return instructions.
14. MDIS decodes NAS-SYS restarts correctly.
15. Disassembles right up to FFFFH and will not overflow to 0, unlike many other disassemblers.
16. MDIS allows selected portions of the object program to be disassembled (a useful subroutine for example) and will produce a stand alone program for reassembly.
17. MDIS permits the user to enter titles and subtitles which will be used on page headings or after the appropriate pseudo-ops on Macro-80 source files.
18. The user is allowed to define his own edit keys (even on systems without user defineable keys on the keyboard).
19. MDIS will accept a printer initialisation string to be entered for ease of use.
20. A user subroutine area has been included for those special initialisation jobs (like redefining the numeric keypad as cursor controls on the Rotec keyboard - a routine to do this is provided free).
21. MDIS works on ANY system running CP/M-80 [properly!!! Superbrains don't like it. - Ed.].

At #50, MDIS surpasses all other CP/M disassemblers in both performance and price. Too good to be true? Most of the Microvalue Dealers will provide a free demonstration to callers on request. Finally, those of you who have versions 2.1 or 2.2 will know that MDIS was protected against disassembling itself and against copying to other systems. In spite of the possibility of rip-offs, version 2.3 has had these checks removed for two reasons. Firstly, it was causing problems for some dealers where some computer manufacturers didn't serialise their CP/M systems properly and second, I was feeling hypocritical as how do you think I obtain those pieces of software that I don't write for myself.

DOCTOR DARK'S DIARY - EPISODE 20.

Well, I never thought all those centuries ago that I would end up writing this many articles for the magazine! I think I owe you all thanks for putting up with me for so long, and to show you that you should not have done, here is my latest load of waffle.

I read the other day, somewhere or other, that the Nascom 1 is alive and well, and costs \$50 in kit form from Lucas. You can't keep a good machine down, and that's a fact. You tell the kids of today you had to pay over \$200 for one, and they'll laugh in your face...

Another thing I chanced upon, in a weekly publication called "Computing", was a review of Boris Allan's book about Logo. At the bottom of the page was a picture of lots of school children in a room with lots of Nascom 3's. It was nice to see that all the claims made by Acron about their monopoly are not 100% true after all. Apparently, Nasnet works. I have tried to use Acron's Ekonet (the names have been changed so that I won't be sued by the guilty!) but it is slower than first class post, when more than two people are using files on the same day!

And now, down to serious stuff, probably not a moment too soon! The Pluto graphics board is short of decent software, unless the people who are writing it don't want it reviewed, and are advertising it in something I don't read. Before you can get round to writing any really big programs for the thing, you need some sort of standard software interface to it. The one that follows is not quite complete, and one day I will get round to writing the definitive version. I have tried several times to write something like this, and have learned several interesting things from my failures along the way. My first versions were in assembler, and very effective, but I wanted something on a higher level, for ease of programming. The first attempt in Pascal was going to have a separate procedure or function for each of the functions of the Pluto board itself. As the set of routines grew longer, I kept compiling them, to check for errors. At a certain stage, the Hisoft Pascal 4 compiler became angry, and would not compile the end of the program. It works well enough with longer programs made up of fewer routines, so I can only assume that a stack somewhere is filling up, or maybe it is a table of addresses that gets too full? Anyway, it was a very ugly program, so I began again with a different approach.

What I really wanted to be able to do was write something along the lines of "pluto(plot,42,42)", in which the name of the required routine and the necessary parameters were passed in the manner shown. But not all of the routines take the same number of parameters. And some return values, while others do not. Pascal does not provide a construction that will let you do anything so variable. So the version that follows is given only the routine name in the call, and a set of global variables that mimic the Pluto board's internal variables must be set to the values you would have passed as parameters, had that been possible in Pascal. All clear? I thought not. If you want to set the current colour to red, you have to write:

```
ccol := red;
pluto(ccol)
```

This is more or less friendly to use, but not by any means perfect! If anyone has any good ideas about how to improve matters, I for one would be glad to see them.

I know it is not as pretty as it should be, but it does work. At least, those parts that don't say "not yet written" work! Any comments will be read with interest, and offers of Pluto Palette boards to test the next version will be met with considerable grovelling.

```

PROGRAM pluto;
CONST
  {Pluto port addresses.}
  status = 160; data = 161;
  {Pluto routine numbers.}
  allocp = 163; arc = 193; bfill = 176; bfilis = 177;
  ffils = 179; clrcwp = 172; copy = 133; copyts = 132;
  ffill = 130; ffillp = 174; ffilis = 173; ffils = 175;
  ibcol = 144; iccol = 141; icp = 150; icdp = 148;
  fcsp = 146; fcwp = 147; ifcol = 143; ipat = 183;
  ipcol = 187; irsel = 183; istat = 134; istyle = 142;
  itcol = 145; twprot = 149; limage = 159; limsgc = 188;
  liner = 157; liners = 158; lineto = 128; laym = 160;
  lsymc = 189; mover = 152; movers = 153; movevo = 151;
  pinit = 166; plot = 154; plotr = 155; plotrs = 156;
  rfill = 129; rimage = 161; rimagc = 190; rpx = 167;
  rpxr = 168; rpxrs = 169; rsym = 162; rsymc = 191;
  shcol = 135; scol = 137; scdp = 131; scap = 164;
  scwp = 165; scfcol = 136; sfpatr = 180; sfpatr = 181;
  shires = 170; stores = 171; spat = 182; spcol = 186;
  srsel = 188; ssstyle = 138; stcol = 139; swprot = 140;
{Pluto colours.}
black = 0; green = 1; blue = 2; cyan = 3;
red = 4; yellow = 5; magenta = 6; white = 7;
{Pluto pixel block maximum sizes, to set maximum size
of the array used by pixel load and save routines.}
pixwide = 10; {Or other value.}
pixhigh = 10; {Or other value.}
TYPE
  {Pluto colour type.}
  plutocol = black..white;
  {Pluto subrange types.}
  byte = 0..255;
  xcoord = 0..639;
  ycoord = 0..287;
  xinc = -639..639;
  yinc = -287..287;
  xincsh = -127..128;
  yincsh = -127..128;
  horiz = 1..640;
  vert = 1..288;
VAR
  {Pluto colour variables.}
  ccol, bcol, fcol, tcol, pcol : plutocol;
  {Pluto partition and workspace variables.}
  cwp : 1..2; {Current working partition.}
  csp : 0..255; {Current symbol partition.}
  cdp : 1..2; {Current display partition.}
  cpx : 0..639; {Current position, X value.}
  cpy : 0..287; {Current position, Y value.}
  {Pluto miscellaneous variables.}
  stat : byte; {Pluto status variable.}
  pat : 0..255; {Line and arc pattern.}
  {Single colour plane operators.}
  wprot : 0..7; {Write protect mask.}

rsel : 0..7; {Read select mask.}
  {The style variable.}
  style : 0..255;
  {Pluto parameter variables.}
  x : xcoord; {Absolute X, Y coordinates.}
  y : ycoord; {16 bit X, Y displacements.}
  dx : xinc; {8 bit X, Y displacements.}
  dy : yincsh; {8 bit X, Y displacements.}
  width : horiz; {Width and height.}
  height : vert; {General 8 bit number.}
  n : byte; {Partition identifier.}
  p : byte; {General colour returned.}
  c : plutocol; {Parameters for the copy routine.}
  {Parameters for the arc routine.}
  xfrom, xto : xcoord;
  yfrom, yto : ycoord;
  pfrom, pto : byte;
  {Parameters for the arc routine.}
  arcx, arcyc : INTEGER;
  arcxe : xinc;
  arcye : yinc;
  {Parameter block for the pixel block commands.}
  pixels : ARRAY [1..pixhigh,1..pixwide] OF plutocolour;
  PROCEDURE sendword(wl, w2 : INTEGER);
  {This sends two 16 bit values to the data port
  address of the Pluto board. As I am using the
  HSA-88B board and Hisoft Pascal 5, it has to do
  a bit of messing round to get the 16 bits we
  want from the 32 actually stored!}
  BEGIN
    OUT(data, PEEK(ADDR(wl)+4, CHAR));
    OUT(data, PEEK(ADDR(w1)+3, CHAR));
    OUT(data, PEEK(ADDR(w1)+2, CHAR));
    OUT(data, PEEK(ADDR(w2)+4, CHAR));
    OUT(data, PEEK(ADDR(w2)+3, CHAR));
  END;
  PROCEDURE senddxdy;
  BEGIN
    OUT(data, CHR(dx)); OUT(data, CHR(dy)));
  END;
  PROCEDURE setdefaults;
  BEGIN
    ccol := 7; bcol := 0; fcol := 7; tcol := 7;
    pcol := 7; cwp := 1; csp := 255; cdp := 1;
    X := 0; Y := 0; pat := 240; wprot := 0;
    rsel := 7; style := 128;
  END;
  PROCEDURE pwait;
  {This waits until the Pluto board is ready.}
  BEGIN
    WHILE INP(status) < CHR(128) DO {nothing}
    END;
    PROCEDURE notyet;
    BEGIN

```

```

WRITELN('Called routine is not yet written');
HALT;
END;
{
  And now the actual routine!
}

PROCEDURE pluto(com : byte);
VAR
  i, j : INTEGER;
BEGIN
  pwatt; {Wait until Pluto is ready.}
  {Send Pluto command.}
  OUT(data,CHR(com));
  {Now send parameters, if any.}
CASE com OF
  allocp : BEGIN pwait; sendword(width,height);
    OUT(data,CHR(n)) END;
    arc : BEGIN pwait; sendword(arxc,arcyc);
      sendword(arcx,arcy) END;
    copy : BEGIN pwait; sendword(width,height);
      OUT(data,CHR(prfrom));
      sendword(xfrom,yfrom);
      OUT(data,CHR(pto));
      sendword(xto,yto) END;
    copyts : BEGIN pwait; OUT(data,CHR(n)) END;
    limage : BEGIN pwait; sendword(width,height);
      FOR i := 1 TO height DO
        FOR j := 1 TO width DO
          OUT(data,CHR(pixels[i,j])) END;
    limagec : BEGIN pwait; notyet END;
    liner : BEGIN pwait; sendword(dx,dy) END;
    liners : BEGIN pwait; sendxdy END;
    lineto : BEGIN pwait; sendword(x,y) END;
    lsym : BEGIN pwait; OUT(data,CHR(n));
      FOR i := 1 TO height DO
        FOR j := 1 TO width DO
          OUT(data,CHR(pixels[i,j])) END;
    lsymc : BEGIN pwait; notyet END;
    mover : BEGIN pwait; sendword(dx,dy) END;
    movers : BEGIN pwait; sendxdy END;
    moveto : BEGIN pwait; sendword(x,y) END;
    pinit : BEGIN pwait; setdefaults END;
    plot : BEGIN pwait; sendword(x,y) END;
    plotr : BEGIN pwait; sendword(dx,dy) END;
    plots : BEGIN pwait; sendxdy END;
    rfill : BEGIN pwait; sendword(width,height) END;
    rimage : BEGIN pwait; sendword(width,height) END;
    rimagec : BEGIN pwait; sendword(dx,dy) END;
    rpx : BEGIN pwait; sendword(x,y) END;
    rpxr : BEGIN pwait; sendxdy END;
    rsym : BEGIN pwait; notyet END;
    rsync : BEGIN pwait; OUT(data,CHR(bc0)) END;
    sbcol : BEGIN pwait; OUT(data,CHR(bc0)) END;

```

```

sc0l : BEGIN pwait; OUT(data,CHR(cc0l)) END;
scdp : BEGIN pwait; OUT(data,CHR(cdp)) END;
scsp : BEGIN pwait; OUT(data,CHR(csp)) END;
scwp : BEGIN pwait; OUT(data,CHR(cwp)) END;
scf0l : BEGIN pwait; OUT(data,CHR(fcc0l)) END;
sfptr : BEGIN pwait; sendword(width,height);
      OUT(data,CHR(p));
      sendword(x,y) END;
      OUT(data,CHR(p));
      sfptrs : BEGIN pwait; OUT(data,CHR(p));
      OUT(data,CHR(n)) END;
      spat : BEGIN pwait; OUT(data,CHR(pat)) END;
      spcol : BEGIN pwait; OUT(data,CHR(pc0l)) END;
      srsel : BEGIN pwait; OUT(data,CHR(rsell)) END;
      sstyle : BEGIN pwait; OUT(data,CHR(style)) END;
      stcol : BEGIN pwait; OUT(data,CHR(fc0l)) END;
      swprot : BEGIN pwait; OUT(data,CHR(wprot)) END
      END;
      {Now get return values if any.}
CASE com OF
  allocp : BEGIN pwait; p := ORD(INP(data)) END;
  icol : BEGIN pwait; bc0l := ORD(INP(data)) END;
  fcol : BEGIN pwait; cc0l := ORD(INP(data)) END;
  lcp : BEGIN pwait; x := ORD(INP(data))+256*ORD(INP(data));
  icdp : BEGIN pwait; cdp := ORD(INP(data)) END;
  icsp : BEGIN pwait; csp := ORD(INP(data)) END;
  icvp : BEGIN pwait; cvp := ORD(INP(data)) END;
  ifcol : BEGIN pwait; fc0l := ORD(INP(data)) END;
  ipat : BEGIN pwait; pat := ORD(INP(data)) END;
  ipcol : BEGIN pwait; pc0l := ORD(INP(data)) END;
  irsel : BEGIN pwait; rsell := ORD(INP(data)) END;
  istat : BEGIN pwait; stat := ORD(INP(data)) END;
  istyle : BEGIN pwait; style := ORD(INP(data)) END;
  itcol : BEGIN pwait; tc0l := ORD(INP(data)) END;
  iwprot : BEGIN pwait; wprot := ORD(INP(data)) END;
  rimage : BEGIN pwait;
    FOR i := 1 TO height DO
      FOR j := 1 TO width DO
        pixels[i,j] := ORD(INP(data)) END;
    rimagec : BEGIN pwait; notyet END;
    rpx : BEGIN pwait; c := ORD(INP(data)) END;
    rpxr : BEGIN pwait; c := ORD(INP(data)) END;
    rpxrs : BEGIN pwait; c := ORD(INP(data)) END;
    rsym : BEGIN pwait;
      FOR i := 1 TO height DO
        FOR j := 1 TO width DO
          pixels[i,j] := ORD(INP(data)) END;
      rsync : BEGIN pwait; notyet END
      END;
      BEGIN {MAIN PROGRAM}
      {You have to write this part for yourself, of
       course, but it should be a lot easier now!}
      END.

```

CP/M File Transfer Program Via The IEEE488 Bus.**By S. Wood**

Below is a simple program written in BASIC-80 which, if present on two machines fitted with the EV Computing IEEE488 interface, will allow data files to be transferred between them at a reasonable rate. The speed loss is due to the amount of data conversion done in BASIC, but it is still faster than serial interface methods previously used. Shortly a machine code version with more features will be made available which will be able to transfer files very fast indeed (approaching 200 K bytes a second). The speed limitation then will be in the disk drives and not in the software or interface.

```

10 REM 488IO, Written by S.Wood. March 1984
20 WIDTH 255
30 B$=CHR$(2)
40 REM SENDER IS ADDRESS 1 & SYSTEM CONTROLLER
50 REM RECEIVER IS ADDRESS 2
60 INPUT "S)END OR R)ECEIVE";A$
70 IF A$="R" OR A$="r" THEN 390
80 IF A$="s" OR A$="S" THEN 110
90 IF A$<>"" THEN PRINT CHR$(7);:GOTO 60
100 END

```

```

110 REM SEND SECTION
120 PRINT B$;"pon 1/s":REM Initialise Sender as system controller
130 INPUT "File name";FSS
140 PRINT B$;"wrt 2 ";FSS:REM Send filename to be loaded across
150 OPEN "R",1,FSS,128
160 R=1:FIELD 1,128 AS X$ 
170 GET 1,1:REM Force dummy file read otherwise EOF function does not work
180 WHILE NOT EOF(1):REM Main sending loop
190 GET 1,R
200 R=R+1
210 S1$="":S2$="":REM Generate two ASCII-Hex strings 128 and 129 bytes long
220 FOR A=1 TO 64:REM the second string has an EOF marker attached.
230 S$=HEX$(ASC(MID$(X$,A,1))):IF LEN(S$)=1 THEN S$="0":S$=
240 S1$=S1$+S$ 
250 NEXT A
260 FOR A=65 TO 128
270 S$=HEX$(ASC(MID$(X$,A,1))):IF LEN(S$)=1 THEN S$="0"+S$ 
280 S2$=S2$+S$ 
290 NEXT A
300 IF EOF(1) THEN S$="A" ELSE S$="M"
310 S2$=S2$+S$ 
320 PRINT B$;"wrt,2 ";S1$ 
330 PRINT B$;"wrt,2 ";S2$ 
340 WEND
350 PRINT"Complete."
360 INPUT "Any more files to send";A$
370 IF A$="y" OR A$="Y" THEN RUN
380 END
390 REM RECEIVE SECTION
400 PRINT B$;"pon 2":REM Power-on the receiver as address 2
410 REM RE-ENTER HERE
420 PRINT"File ";"FR$," is being received."
430 R=1
440 OPEN "R",1,FSS,128
450 FIELD 1,128 AS X$ 
460 REM RE-ENTER HERE
470 TEMP$=""
480 PRINT B$;INPUT "red,1";S1$ 
490 PRINT B$;INPUT "red,1";S2$ :TEMP$="""
500 IF RIGHTS$(S2$,1)="A" AND LEN(S2$)=129 THEN 610:REM END OF FILE
510 FOR A=1 TO 128 STEP 2
520 TEMP$=TEMP$+CHR$(VAL("6H"+MID$(S1$,A,2)))
530 NEXT A
540 FOR A=1 TO 128 STEP 2
550 TEMP$=TEMP$+CHR$(VAL("6H"+MID$(S2$,A,2)))
560 NEXT A
570 LSFT X$=TEMP$ 
580 PUT 1,R,R=R+1
590 IF RIGHTS$(S2$,1)="M" THEN 460:REM Not EOF yet
600 IF RIGHTS$(S2$,1)<>"A" THEN PRINT"error":STOP
610 CLOSE 1
620 PRINT B$;"rdy":REM Read last line-feed from Bus
630 RUN

```

Dave Hunt's Bits

So 80-BUS is on the move again, and has caught me by totally by surprise as I've nothing ready to print. Do I hear cries of shame!!! [Ed. - No.]

They say that behind every great man is a woman calling the shots, and in the case of our editor, this seems to be the case. Now he's too modest and/or shy to make any comment, but if I say that shortly before the Christmas issue was due to be put to bed, a rather nice young lady entered his life suffice to say that the demands on his spare time (usually devoted to magazine preparation) was diverted, or, should I say, diverted elsewhere. Now, this being the case, if you assume that the reappearance of the mag. is indicative of a departure in his life, you would be wrong, perhaps he's feeling guilty having left his readership magazine-less for so long, or something; whatever, it has created a great spurt of energy, and not only is this issue in the last stages of preparation as I write, but the next issue as well.

The radio bits first....

Firstly AMTOR, I am now in possession of a number of reports on the development of the commercial system upon which AMTOR is based and also the various specifications which comprise the working details of AMTOR. My grateful thanks to those readers (several anonymous) who sent me the details. I confess that I'm somewhat intrigued by the two copies addressed directly to my home, as at the time my address hadn't been published anywhere (now QTHR in the 1984 callbook). Sadly, I'm not sure that I will be able to make use of the information (although all information is ultimately useful at sometime). As you may have guessed I have been investigating 'PACKET RADIO', or at least the bastardized form that the UK amateur radio regulations allow.

Literally a couple of days ago I was presented with the PACKET RADIO program by G8WJL and G6GIX, for the BBC computer. I swiped a BEEB and had a go, it certainly works well. I particularly liked the fact that no additional hardware was required, just a lead which plugs from the BEEB tape I/O socket into the mic, ptt and extension speaker sockets of the rig.

Despite the ease of setting up, I feel the program suffers from a number of minor deficiencies, niggles really. If the program is active, then it will read any packet information that it sees, so if there are two or three QSO's going on on the same frequency (don't forget the idea of packet is to allow just this), then the program writes the packets to the screen. This is fine if you are 'earwigging' and just want to know who's around. The problem is that if you start a QSO with a station, then (as far as I can see) this reporting of other QSO's continues, making it difficult to see who is talking to who. Worse, because of this reporting, it suggests that multiway QSO's are possible, fine until you try it. As a data anti-collision protocol is used with random timing between packets, there is no knowing which station is going to send first, so unless 'to' -> 'from' callsigns are included in each packet text then multiway QSO's becomes extremely confusing. A simple software toggle to turn the 'blurb' off once contact is established would be a very good idea.

A further off-shoot of the 'blurb' reporting is the ability to 'sort of' send a CQ, something which packet does not normally allow. If you program the originating callsign as your own and the destination callsign as CQCQ rather

than a legit callsign, followed by the message 'DE G6MFR', then any station in the 'earwigging' mode will see:

G6MFR: CQCQ DE G6MFR

The receiving station, say G2XXX, can then open a communications channel to you simply by programming the destination callsign as the callsign just received. So his channel becomes G2XXX -> G6MFR. G2XXX can then send a message (or even no message), say KN DE G2XXX, where upon you will see:

G2XXX: KN DE G2XXX

What's the problem? Well, as you were sending to a mythical station called CQCQ you can't go back to G2XXX until the packet program gives up trying to contact the station CQCQ (16 tries over the space of a couple of minutes), or without first having escaped from the program and then reRUNning it. This takes time and clears the screen so if you've got a bad memory like me, then you've forgotten who was calling in the first place.

The program needs to know when no-one else is transmitting to allow the anti-collision logic to work. This is done by manually opening the squelch and the consequent white noise is detected by the tape I/O (probably as random numbers) indicating that the program is free to transmit. When a strong blank carrier is up, or data is being received, the program inhibits transmit and goes to sleep for a random period before trying again. Now this has snags. A noisy incoming signal which doesn't quite trigger the tape I/O could be ignored or interpreted as a free channel allowing transmit (this sort of error occurs on signals worse than about 4 by 5). Secondly, white noise must be present to indicate the channel is free. This precludes any phase lock signal shaping circuits which could be used to reconstitute a noisy signal, thereby much improving the sensitivity of the system. Now the rig squelch circuit is much better at differentiating between signal and no signal than the computer, so a logical development would be to return the squelch line to the computer to detect a channel free condition. For sideband use, a phase lock shaper could be used and the out-of-lock line from this may be used to indicate a free channel.

... and BBC BASIC

It was to investigate my niggles and with a mind to convert the program to my machine that I started to poke around inside the program. Now I haven't paid much attention to the BEEB machine in the past, preferring my machine which I consider superior and, at least, I understand. Some people tell me the BEEB is the most fantastic machine since the invention of the rocket propelled roller skate, whilst others tell me that all BEEBs should be collected, along with their owners, put in a large box and dumped at the bottom of the Marianas Trench as an insidious danger to mankind. I know of one gentleman who made a considerable loss by trading in an almost new Gemini MultiBoard machine for a BEEB, whilst I know of another who can't wait to trade his BEEB for a Galaxy when he can rake up the necessary. So overall, on a statistical sample of two, 50% of BEEB owners are loonies whilst the other 50% have seen the light and should be helped as much as possible. Certainly the BEEB seems to me to be overpriced and has gained a sort of cult acceptance which it does not deserve, but then the same could be said for some of the other machines around, so ... there you go!!

Anyway on to BEEB 'BASIC', the quotes are deliberate, as whatever it is, it bears little relation to the original Dartmouth College Basic. In fact its closer relations would seem to be some sort of cross between Pascal and C with BASIC syntax. Perhaps a better name for it might be BEEB C-BasPas, anything, but not BASIC. As a language, it's very powerful, but encourages 'opaque programming' under the guise of 'structured programming'. In other words if the programmer cares to do the job properly with lashings of comment and everything laid out in a nice orderly fashion, then anything written in C-BasPas should be beautifully clear; on the other hand, if the programmer wanted make his program difficult to read (or is lazy like most of us), all he has to do is remove all the comment, jumble the order of the PROCedures and function calls, and the program becomes as clear as mud. The latter is more likely to be the case as the available memory in the BEEB is quite small (having subtracted the screen memory and sundry workspaces (a lot of which don't seem to do much)), as it's quite difficult to squeeze a program of any size into the available memory space without resorting to 'dirty' short cuts.

The BEEB is also provided with a large number of indirectly addressed operating system calls (driven from a number table, something like NAS-SYS) which the user is encouraged to use from the C-BasPas. The OS calls are reasonably documented in the manual, but rely on an understanding of the hardware and devices used, information which is sadly not provided. (I understand the Advanced User's manual guide goes into detail, but as the BEEB machine I have is on borrow, I don't feel inclined to buy this just to see what's happening).

The program uses the BEEB CUTS tape I/O but seems to address it directly, as the data sent appears to be in the form of a bit stream (as it should be) rather than ASCII characters with start and stop bits using the UART as I had hoped. Shades of nasty undocumented things going on in the ULA. All this will prove an interesting exercise on a Gemini, as I guess a couple of bits from a port are going to have to be hard wired to the tape I/O and a software UART written, either that or I'm going to have to design an SIO piggyback board.

So my original plan of quickly rewriting the PACKET program into something more generalized has had to go by the board. It's not so much an exercise in rewriting, but having to learn C-BasPas and the detail innards of the BEEB operating system and hardware, a time wasting exercise that I'm not too keen on. Oh well, I will carry on (unless someone else wants to have a go). Instead of something I judged would take a few evenings to do, it looks like it's going to take weeks. Still when (if) I've done it we'll publish it here so that Gemini's and Nascom's can take to the air in the PACKET mode.

As to my opinion of the BEEB after a couple of days acquaintance ... well let's say I wouldn't give more than fifty quid for one if someone was rash enough to pass one my way!! Torch have the right idea, demote the BEEB to a terminal for use on a CP/M system, it's about all it's good for. Pity Torch had to do their own thing with a non-standard CP/M system. Perhaps I might have a go at doing the same with a MultiBoard system, because overpriced as the BEEB is, it might just be cheaper than buying an SVC and keyboard; it could then replace these two, with high res. colour thrown in. Now all this brands me as a heretic with the BEEB cult. So I'd better keep quiet otherwise some dark night, someone might find me lying in a dark alley with a BEEB in my back.

BIOS 3 ... Where is it?

Some time ago (in fact about this time last year, early June) I wrote a piece called 'SYS is dead, long live ...', where I stated that due to alleged piracy of the SYS disk drive source for commercial purposes Gemini were likely to withdraw their permission for the publication of their disk drivers in the SYS source. At about the same time Gemini started to charge very high prices for their source code to bona fide users. The point of my piece was that Gemini were pricing their source code at prices which only pirates, who could take commercial advantage of their source code, could afford. This accusation on my part quite understandably upset Gemini, who it was aimed at, to the point that they have threatened to produce a product called BIOS 3 which will allow the Gemini user to update his BIOS to the latest spec. and include all Gemini permutations of drive/controller/video for himself. As the majority of SYS users were using SYS for just this purpose, the need for SYS would disappear. So far BIOS 3 has not appeared, so a gentle nudge is required. How about it Gemini!!!

An apology

Much to my surprise, my piece 'SYS is dead, long live ...' provoked a much more vehement reaction from an unexpected quarter. Due to the tendency of 80-BUS magazine to appear in two's, my published comments in Vol.2 Iss.4 could be linked with an unattributed editorial piece (which I hasten to add I did not write) in Vol.2 Iss.5, the following issue, which appeared about the same time. MAP80 Systems of Chertsey have taken very strong objection to these two pieces and claim that these have damaged their business. If MAP80 Systems think that my piece was directed at them and in consequence, damaged their business, then I can only apologise and unreservedly withdraw any such imputation. My piece made no allusions direct or indirect to any specific concern.

As far as the second piece, the editorial, is concerned, this was merely a statement of fact, nothing more nothing less. The details of the litigation were sub-judice at the time and as far as I know, still are. The fact that MAP80 Systems and Gemini are in litigation has no bearing on my piece, as this litigation started sometime after I wrote the piece, which after all, as stated above, was intended as a 'side-swipe' at Gemini.

The SVC Board

The new Gemini GM832 Super Video Controller Board has at long last escaped from Amersham. I'm not sure whether this was a mistake or not, as up to a week or two ago, these cards were rare-er than rocking horse manure (very good for plastic rose trees I'm told). Anyway one of these cards fell into my clutches for a couple of hours before being whipped away by the customer who had paid for it months before.

You don't get much time to do a full evaluation in two hours, but first impressions were good. There was none of the video patterning characteristic of the GM812 when in inverse video. The most immediate impression is speed. Using the TYPE command under CP/M revealed a staggering increase in speed with text hurtling up the screen at a phenomenal rate. A lot more than the 50% increase directly attributable to the use of a 6MHz Z80B. The screen/workspace RAM contention logic has been removed from software and placed in hardware, and this is one other factor increasing the speed. The nett result looks more like 150% faster, although I didn't set to with stop watch to time it or to read the documentation to see if any actual figure is quoted. The only problem

with this improved display speed is that fingers 2 and 4 on my left hand had to increase their reaction time by a proportional amount, as there was barely time to bash ^S before the thing you wanted to see disappeared off the screen. Perhaps a ZZzzz... command could be built into CCPZ to slow it up when not actually executing a program.

The next feature investigated (for its novelty) was the clock display. Once enabled, a changing clock display is written at a user predetermined cursor position on the screen. The default cursor position is the extreme top right hand corner of the screen, and this suits most software I had a chance to try it with. The internal clock generator is interrupt driven from the frame sync., which is divided by 50 to update the clock counter and redisplay the clock once a second. As the system clock is crystal controlled, it is accurate enough for most purposes although I think reprogramming the 6845 video controller could upset the clock accuracy. A very simple routine was written which read the time from the GM822 RTC and loaded the clock on the SVC. At the end of the two hour period there was a discrepancy of 7 seconds between the RTC and the SVC clock.

A number of bits of software which were known to be to greater or lesser extent video card dependant were tried to see if the compatibility between the GM832 and the now discontinued GM812 were as claimed, and all were found to work as before. DISKPEN/GEMPEN required adjustment to the cursor speed patch byte, because the cursor flash rate went berserk, but in all other respects except the 48 wide mode (the new SVCs second screen mode is now 40 wide) DISKPEN/GEMPEN worked well. The increased speed of the card showed to advantage with WORDSTAR, screen rewrites being accomplished with very acceptable speed. My old CHARGEN program for reprogramming the character sets worked, but of course needs rewriting in the light of the SVC as there are now two programmable character sets (total 256 characters) instead of one.

The SVC has a number of new features including 256 by 256 pixel graphics with built in line and circle drawing software, selectable attributes for flashing characters with low, half and inverse intensities, inputs for serial keyboards, and many other things. As I said, I only laid hands on one for a very short period, so it is very difficult to be objective. I didn't find any nasty quirks and the board behaved impeccably. Perhaps now Gemini will loan me one to play with, and I'll be able to find out what it can really do. The only thing that worries me though is the very high degree of sophistication of this card, commensurate with its elevated price. In my experience, 99% of users were unaware of the potential of the old GM812 card, so is all this cleverness really necessary?

The Climax colour card

The Climax colour card is to be re-introduced. Gemini have acquired the rights to manufacture the card from Climax, and deliveries are to commence very shortly. This card was very popular with those owning it, and some very clever displays have been seen. But as the card has been unavailable for the best part of a year, interest in it has waned. With its welcome reappearance perhaps some new and clever software will start to be written. Certainly as the video format of the Climax card is the same as the high-res mode of the new SVC, there will be demand for a piece of software which will act as an interface between the two. Here is an opportunity for CC-SOFT, who have written some rather nice graphics software for Gemini gear in the past to come up with something. It would be nice to see two interfaces with common inputs

which would then either drive the SVC or the Climax from the same graphics programs.

With the advent of either black and white high-res or colour graphics perhaps some enterprising software writers will have a go at writing some arcade type games for the Gemini and Nascom hybrid machines. These would have to be written for fun as there would be little money in it, but it is surprising how often we get asked about games for the Gemini. There are a number of the more intellectual type, Adventure, Chess, Planetfall, etc., but the arcade types tend to get overlooked. I know I personally have no patience with arcade games, both lacking in the necessary co-ordination to play them and also in the necessary patience to practice. I also realise for the cost of a Climax card (now Gemini GM837) I could buy a Spectrum and a whole bag full of Space Invaders tapes. But some people seem to want them and where there is a need someone will usually try to fill it.

Qwikdraw

Whilst still on the subject of colour graphics, the expensive Pluto card (now only available in the full 8MHz, extended monitor form) has gained a very versatile graphics package shortly to be made available by Gemini. Called Qwikdraw, and written primarily for use on the Gemini networks installed in the Manpower Services YTS training scheme, Qwikdraw is an easy to use graphics package with some very novel features. Input is either from the keyboard (using the cursor keys with selectable step rates) or, ultimately, from a bit pad. Drawing of graphical displays is quite easy with automatic circle and smoothed curve creation. Block and complex shape fills and colour floods are also catered for. An optional colour mixing package will allow up to 32 colours to be displayed, and a 'picture compiler' which can convert the stored picture format into a .COM file for immediate execution. Limited animation is also possible. Display is normally to a high resolution colour monitor or to a dot matrix printer or to a multicolour plotter. Very extensive 'help' facilities are provided, which are an education in themselves with animated graphics demonstrating the points queried. One interesting use demonstrated was the preparation of the cels used for overhead projectors in an educational environment. The cels being drawn on the plotter using oil based pens. Whilst not as versatile as the Nascom Lotti, it is extremely quick and easy to use with a powerful editor, it should find lots of applications outside the YTS scheme particularly in the education field.

Printer. RS232 interface. 180 cps. Bidirectional. Buffered. True Descenders.
#80. Crawthorne (0344) 776894.

Nascom 2 with Nas-Sys 3, Naspen, 48K RAM and Castle interface - #300; additional 48K RAM B - #75; self-contained keyboard with lead - #35; graphics chip - #10; PSU - #25; Bricomp Real Time Clock/Calendar - #17; Nasbus EPROM/ROM card with Naspen, Debug, ZEAP - #45. 0532 740921

Colour card (Holmes/R&EW) - 16 colour + sprites. Also has 2 sound generators, 2 clock chips, 8 port A-D, CMOS RAM, CTC. Fully built, working with hardware and software. All ic's (socketed) except 1 off AY-3-8910. All xtals and backup battery. Also interface card to convert colour diff. signals to RGB, offers around #100. Tel C. Bowden 0209 860480 for details.

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Dis-assembly of NASCOM ROM BASIC Ver 4.7 PAGE 54
      ; Save code string address
F012 E5 ARLD$V: PUSH HL      ; A = 00 , Flags set = Z,N
F013 F5          PUSH AF      ; Start of arrays
F014 2AD810      LD HL,(YAREND) ; Skip "ADD HI,DE"
F017 3E          DEFB (LD A,n) ; Move to next array start
F018 19          FDARY: ADD HL,DE
F019 EB          EX DE,HL
F01A 2ADA10      LD HL,(ARREND) ; End of arrays
F01D EB          EX DE,HL      ; Current array pointer
F01E CDBAE6      CALL CRDEHL ; End of arrays found?
F021 CAA4FO      JP Z,CREARY ; Yes - Create array
F024 7E          LD A,(HL)    ; Get second byte of name
F025 B9          CP C        ; Compare with name given
F026 25          INC HL      ; Move on
F027 C22CFO      JP NZ,NXTARY ; Different - Find next array
F02A 7E          LD A,(HL)    ; Get first byte of name
F02B B8          CP B        ; Compare with name given
F02C 25          INC HL      ; Move on
F02D 5E          INC E,(HL)   ; Get ISSB of next array address
F02E 25          INC HL      ; Get MSB of next array address
F02F 56          LD D,(HL)
F030 25          INC HL
F031 C218FO      JP NZ,FIDARY ; Not found - Keep looking
F034 3AAC10      LD A,(LCRFLG) ; Found - Locate or Create it?
F037 B7          OR A
F038 C2B6E3      JP NZ,DDERR ; Create - 2DD Error
F03B F1          POP AF      ; Locate - Get number of dim's
F03C 44          LD B,H      ; BC Points to array dim's
F03D 4D          LD C,L
F03E CA54F7      JP Z,POPHRT ; Jump if array load/save
F041 96          SUB HL      ; Same number of dimensions?
F042 CAA8FO      JP Z,FINDEL ; Yes - Find element
F045 1E10          BSERR: LD E,BS    ; ?BS Error
F047 C3C1E3      JP ERROR   ; Output error

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NASCOM ROM

BASIC

DIS-ASSEMBLED

PART 5

BY CARL LLOYD-PARKER

Dis-assembly of NASCOM ROM BASIC Ver 4.7

Dis-assembly of NASCOM ROM BASIC Ver 4.7

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F04A 110400 CREAMY: LD DE,4 ; 4 Bytes per entry
 F04D F1 POP AF ; Array to save or 0 dim's?
 F04E CA0E9 JP Z,FERR ; Yes - ?FC Error
 F051 71 LD (HL),C ; Save second byte of name
 F052 23 INC HL ; Save first byte of name
 F053 70 LD (HL),B ;
 F054 23 INC HL ; Number of dimensions to C
 F055 4F LD C,A ; Check if enough memory
 F056 CDAE3 CALL CHKSTK ; Point to number of dimensions
 F059 23 INC HL ; Save address of pointer
 F05A 23 INC HL ; Set number of dimensions
 F05B 220510 LD (CUROPR),HL ;
 F05E 71 LD (HL),C ;
 F05F 23 INC HL ; Locate of Create?
 F060 3AAC10 LD A,(LCRFLG) ; Carry set = Create
 F063 17 RLA ; Get number of dimensions
 F064 79 LD A,C ; Default dimension size 10
 F065 010B00 CRARLP: LD BC,10+1 ; Locate - Set default size
 F068 D267FO JP NC,DEFSIZ ; Get specified dimension size
 F06B C1 POP BC ; Include zero element
 F06C 03 INC BC ; Save LSB of dimension size
 F06D 71 DEFSIZ: LD (HL),C ;
 F06E 23 INC HL ; Save MSB of dimension size
 F06F 70 LD (HL),B ; Save name' of dim'n an status
 F070 23 INC HL ; Save address of dim'n size
 F071 F5 PUSH AF ; Restore address of dim'n size
 F072 E5 PUSH HL ; Restore number of dimensions
 F073 CDFFF8 CALL MLDEBC ; Count them
 F076 EB PUSH AF ; Do next dimension if more
 F077 E1 POP AF ; Save locate/create flag
 F078 F1 POP AF ; MSB of memory needed
 F079 3D DEC A ; LSB of memory needed
 F07A C265FO JP NZ,CRARLP ; Add bytes to array start
 F07D F5 PUSH AF ; Too big - Error
 F07E 42 LD B,D ; See if enough memory
 F07F 4B LD C,E ; Save new end of array
 F080 EB EX DE,HL ;
 F081 19 ADD HI,DE ;
 F082 DAA2E3 JP C,OMERR ;
 F085 CD93E3 CALL ENTFMEM ;
 F088 22DA10 LD (AREND),HL ;

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; Array to save or 0 dim's?
 ; Yes - ?FC Error
 ; Save second byte of name
 ; Save first byte of name
 ; Number of dimensions to C
 ; Check if enough memory
 ; Point to number of dimensions
 ; Save address of pointer
 ; Set number of dimensions
 ; Locate of Create?
 ; Carry set = Create
 ; Get number of dimensions
 ; Default dimension size 10
 ; Locate - Set default size
 ; Get specified dimension size
 ; Include zero element
 ; Save LSB of dimension size
 ; Save MSB of dimension size
 ; Save name' of dim'n an status
 ; Save address of dim'n size
 ; Restore address of dim'n size
 ; Restore number of dimensions
 ; Count them
 ; Do next dimension if more
 ; Save locate/create flag
 ; MSB of memory needed
 ; LSB of memory needed
 ; Add bytes to array start
 ; Too big - Error
 ; See if enough memory
 ; Save new end of array
 ; Back through array data
 ; Set array element to zero
 ; All elements zeroed?
 ; No - Keep on going
 ; Number of bytes + 1
 ; A=0
 ; Get address of array
 ; Number of dimensions
 ; To HL
 ; Two bytes per dimension size
 ; Add number of bytes
 ; Bytes needed to DE
 ; Save LSB of bytes needed
 ; Save MSB of bytes needed
 ; Locate / Create?
 ; A is 0 , End if create
 ; Find array element
 ; Number of dimensions
 ; Skip "POP HL"
 ; Address of next dim' size
 ; Get LSB of dim'n size
 ; Get MSB of dim'n size
 ; Save index
 ; Multiply previous by size
 ; Index supplied to DE
 ; Add index to pointer
 ; Number of dimensions
 ; Count them
 ; MSB of pointer
 ; LSB of pointer
 ; More - Keep going
 ; 4 Bytes per element
 ; Start of array
 ; Point to element
 ; Address of element to DE
 ; Get code string address
 ; RET

Dis-assembly of NASCOM ROM BASIC Ver 4.7

F0D0 2ADA10	FRE:	LD HL,(ARPEND)	Start of free memory
F0D3 EB		EX DE,HL	To DE
F0D4 210000		LD HL,O	End of free memory
F0D7 39		ADD HL,SP	Current stack value
F0D8 3AAC10		LD A,(TYPE)	Dummy argument type.
F0DB B7	OR A		
F0DC CAECF0	Z,FRENUM	JP Z,FRENUM	Numeric - Free variable space
F0DF CD53FF3	GSTRCU	CALL GSTRCU	Current string to pool
F0E2 CD52FF2	GARBGE	CALL GARBGE	Garbage collection
F0E5 2A5A10	HI,(STRSPC)	LD HI,(STRSPC)	Bottom of string space in use
F0E8 EB	DE,HL	EX DE,HL	To DE
F0E9 ZAC310	LD HI,(STRBOT)		Bottom of string space
F0ECD 7D	FRENUM:	LD A,L	Get LSB of end
F0F0 95	SUB	LD E	Subtract LSB of beginning
F0F4 F	LD C,A	LD C,A	Save difference if C
F0F7 7C	LD A,H	LD A,H	Get MSB of end
F0F9 9A	SBC	LD A,D	Subtract MSB of beginning
F0F9 41	ACPASS:	LD B,C	Return integer AC
F0F9 50	ABPASS:	LD D,B	Return integer AB
F0F9 1E00	LD E,0		
F0F9 21AD10	LD HL,TYPE		Point to type
F0F8 73	LD (HL),E		Set type to numeric
F0F9 0690	LD B,8OH+16		16 bit integer
F0F9 C52AF8	JP RETINT		Return the integr
F0F9 3AA10	LD A,(CURPOS)		Get cursor position
F101 47	LD B,A	LD B,A	Put A into AB
F102 AF	XOR A	LD A	Zero A
F103 C5F2FO	JP ABPASS		Return integer AB
F106 CD89F1	DEF:	CALL CHEKFN	Get "FN" and name
F109 CDTBF1		LD TTEST	Test for illegal direct
F10C 0170EA		LD BC,DATA	To get next statement
F10F C5	PUSH BC	LD DE	Save address of RETurn
F110 D5	PUSH DE	CALL CHKSYN	Save address of function ptr
F111 CD90E6	"("	CALL DEFB	Make sure "(" follows
F114 28	DEFB	CALL GETVAR	Get argument variable name
F115 CD2DEF	PUSH HL	CALL PUSH	Save code string address
F118 E5	EX DE,HL	EX DE,HL	Argument address to HL
F119 EB	DEC HL	D,(HL)	Get first byte of arg name
F11A 2B	LD D,(HL)		
F11B 56	DEC HL	E,(HL)	Get second byte of arg name
F11C 2B	LD E,(HL)		
F11D 5E	POP HL	TSTNUM	Restore code string address
F11E E1		CALL CHKSYN	Make sure ")" follows
F11F CD44ED	CALL DEF3	CALL CHKSYN	Make sure "=" follows
F122 CD90E6	CALL DEF3	ZEQUAL	"=" token
F125 29	LD (HL),C	LD B,H	Code string address to BC
F126 CD90E6	INC HL	C,L	Save code str , Get FN ptr
F129 B4	LD (HL),B	(SP),HL	Save LSB of FN code string
F12A 44		(HL),C	Save MSB of FN code string
F12B 4D			...
F12C E3			
F12D 71			
F12E 23			
F12F 70			

Dis-assembly of NASCOM ROM BASIC Ver 4.7

F133 CD89F1	DOFN:	CALL CHEKFN	Make sure FN follows
F136 D5		PUSH DE	Save function pointer address
F137 CD00EE		CALL EVLPAR	Evaluate expression in "("
F13A CD44ED		CALL TSTNUM	Make sure numeric result
F13D E3		EX (SP),HL	Save code str , Get FN ptr
F13E 5E		LD E,(HL)	Get LSB of FN code string
F13F 25		INC HL	
F140 56		LD D,(HL)	Get MSB of FN code string
F141 25		INC HL	
F142 7A		LD A,D	And function DEFINED?
F143 B3		OR E	No - ?UF Error
F144 CAB9E3		JP Z,UFERR	Get LSB of argument address
F147 7E		LD A,(HL)	
F148 25		INC HL	
F149 66		LD H,(HL)	Get MSB of argument address
F14A 6F		LD L,A	HL = Arg variable address
F14B E5		INC HL	
F14C 21DE10		LD HL,(FNRRNM)	
F14F E5		EX (SP),HL	
F150 22DE10		LD (FNRRNM),HL	
F153 21E210		LD HL,(FNARG+2)	
F156 E5		PUSH HL	Save it
F157 21E010		LD HL,(FNARG)	Get old argument name
F15A E5		LD HL,(FNARG)	Save old , Get new
F15B 21E010		LD HL,FMARG	Set new argument name
F15E D5		PUSH DE	Get LSB,NLSB of old arg value
F15F CD6BF8		CALL FPTHL	Save it
F162 E1		POP HL	Move FPREG to argument
F163 CD41ED		CALL GETNUM	Get FN code string address
F166 2B		DEC HL	DEC cos GTTCHR INCs
F167 CD56E8		PUSH GETCHR	Get next character
F16A C2ADE3		CALL NZ,SNERR	Bad character in FN - Error
F16D E1		POP HL	Get MSB,EXP of old arg
F16E 22E010		CALL (FNARG),HL	Restore it
F171 E1		POP HL	Get LSE,NLSB of old arg
F172 22E210		LD (FNARG+2),HL	Restore it
F175 E1		POP HL	Get name of old arg
F176 22DE10		LD (FNRRNM),HL	Restore it
F179 E1		POP HL	Restore code string address
F17A C9		RET	
F17B E5		PUSH HL	Save code string address
F17C 2A5C10		LD HL,(LINEAT)	Get current line number
F17F 23		INC HL	-1 means direct statement
F180 7C		LD A,H	
F181 B5		OR OR	
F182 E1		POP HL	Restore code string address
F183 C0		RET NZ	Return if in program
F184 E16		LD E,TD	?ID Error
F186 C5C1E3		JP ERROR	

Dis-assembly of NASCOM ROM BASIC Ver 4.7

Dis-assembly of NASCOM ROM BASIC Ver 4.7

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F189 CD90E6 CHEKFN: CALL CHKSYN
 F18C A7 ZFN DEF3 LD A,80H
 F18D 3E80 (FORFLG),A LD (HL)
 F18F 32CB10 OR B,A
 F192 B6 LD B,A
 F193 47 CALL QTFNAM
 F194 CD32EF JP TSTNUM
 F197 C344ED STR: CALL TSTNUM
 F19A CD44ED CALL NUMASC
 F19D CDB8F9 CALL ORTST
 F1A0 CDCBFF1 CALL GSTRCU
 F1A3 CD55F3 CALL BC,TOPPOOL
 F1A6 01AEP7 LD BC
 F1A9 C5 PUSH HL
 F1AA 7E SAVSTR: LD A,(HL)
 F1AB 23 INC HL
 F1AC 23 INC HL
 F1AD B5 PUSH HL
 F1AE CD29F2 CALL TESTR
 F1B1 E1 POP HL
 F1B2 4B LD C,(HL)
 F1B3 23 INC B,(HL)
 F1B4 46 CALL CRMST
 F1B5 CDC2F1 PUSH HL
 F1B8 E5 LD L,A
 F1B9 6F CALL TOSTRA
 F1BA CD46F3 POP DE
 F1BD D1 RETP
 F1BE C9 MKTMST: CALL TESTR
 F1C2 21BF10 CRTMST:
 F1C5 E5 PUSH HL
 F1C6 77 LD (HL),A
 F1C7 25 INC HL
 F1C8 23 SVSTAD: INC HL
 F1C9 73 INC (HL),E
 F1CA 23 INC (HL),D
 F1CB 72 POP HL
 F1CC E1 RET
 F1CD C9

; Make sure FN follows
 ; "FN" token
 ; FN name to find
 ; FN name has bit 7 set
 ; in first byte of name
 ; Get FN name
 ; Make sure numeric function
 ; Make sure it's a number
 ; Turn number into text
 ; Create string entry for it
 ; Current string to pool
 ; Save in string pool
 ; Save address on stack
 ; Get string length
 ; Save pointer to string
 ; See if enough string space
 ; Restore pointer to string
 ; Get LSB of address
 ; Get MSB of address
 ; Create string entry
 ; Save pointer to MSB of addr
 ; Length of string
 ; Move to string area
 ; Restore pointer to MSB
 ; See if enough string space
 ; Temporary string
 ; Save it
 ; Save length of string
 ; Save LSB of address
 ; Save MSB of address
 ; Restore pointer

F1CE 2B CRTST: DEC
 F1CF 0622 QISTR: LD HL,
 F1D1 50 LD D,B
 F1D2 E5 DSSTR: PUSH HL
 F1D3 0EFF LD C,-1
 F1D5 23 QTSLP: INC HL
 F1D6 7E LD A,(HL)
 F1D7 OC INC C
 F1D8 B7 OR A
 F1D9 CAE4F1 JP Z,CRTSTE
 F1DC BA CP D
 F1DD CAE4F1 JP Z,CRTSTE
 F1E0 B8 CP B
 F1E1 C2D5F1 JP NZ,QTSLP
 F1E4 FE22 CRTSTE: CP "",
 F1E6 CC3688 CALL Z,GETCHR
 F1E9 E5 EX (SP),HL
 F1EA 23 INC HL
 F1EB EB EX HL,HL
 F1EC 79 LD A,C
 F1ED CDC2F1 CALL CRMST
 F1F0 11BF10 TSTOPL: LD DE,TMPSTR
 F1F3 2AB110 LD HL,(TMSTOPT)
 F1F6 22E410 LD (PREG),HL
 F1F9 3E01 LD (TYPE),A
 F1FB 32AD10 LD DEHL4
 F1FE CD6EFS CALL CRMSTP,HL
 F201 CD8AE6 CALL CPDEHL
 F204 22B110 LD (INSTRU),HL
 F207 E1 POP HL
 F208 7E LD A,(HL)
 F209 C0 RET NZ
 F20A 1E1E LD E,ST
 F20C 03C1E3 JP ERROR
 F20F 25 PRNMS: INC HL
 F210 CDCEP1 PRS: CALL CRMST
 F213 CD53F3 PRS1: CALL GSTRCU
 F216 CD62F8 CALL LOADFP
 F219 1C INC E
 F21A 1D PRSLP: CALL DBC
 F21B C8 RET Z
 F21C OA LD A,(BC)
 F21D CD9BE6 CALL OUTC
 F220 FE0D CP CR
 F222 C086EB CALL Z,DONULL
 F225 03 INC BC
 F226 C31AF2 JP PRSLP

; DEC - INCed after
 ; Terminating quote
 ; Quote to D
 ; Save start
 ; Set counter to -1
 ; Move on
 ; Get byte
 ; Count bytes
 ; End of line?
 ; Yes - Create string entry
 ; Terminator D found?
 ; Yes - Create string entry
 ; Terminator B found?
 ; No - Keep looking
 ; End with ""?
 ; Yes - Get next character
 ; Starting quote
 ; First byte of string
 ; To DE
 ; Get length
 ; Create string entry
 ; Temporary string
 ; Temporary string pool pointer
 ; Save address of string ptr
 ; Set type to string
 ; More string to pool
 ; Out of string pool?
 ; Restore code string address
 ; Get next code byte
 ; Return if pool OK
 ; ?ST Error
 ; String pool overflow
 ; Skip leading space
 ; Create string entry for it
 ; Current string to pool
 ; Move string block to BCDE
 ; Length + 1
 ; Count characters
 ; End of string
 ; Get byte to output
 ; Output character in A
 ; Return?
 ; Yes - Do nulls
 ; Next byte in string
 ; More characters to output

Dis-assembly of NASCOM ROM BASIC Ver 4.7

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Dis-assembly of NASCOM ROM BASIC Ver 4.7

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F229 B7 TESTR: OR A
 F22A OE DEFB (LD C, n)
 F22B F1 GREDON: POP AF
 F22C F5 PUSH AF
 F22D 2A5A10 LD HL, (STRSPC)
 F230 EB EX DE, HL
 F231 2AC310 LD HL, (STRBOT)
 F234 2F CPL
 F235 4F LD C, A
 F236 06FF LD B, -1
 F238 09 ADD HL, BC
 F239 23 INC HL
 F23A CDBA6 CALL CPDEHL
 F23D DA47F2 JP C, TESTOS
 F240 22C310 LD (STRBOT), HL
 F243 25 INC HL
 F244 EB EX DE, HL
 F245 F1 POPAF: POP AF
 F246 C9 RET

F247 F1 TESTOS: POP AF
 F248 1E1A LD E, OS
 F24A CAC1E3 JP Z, ERROR
 F24D BF CP A
 F24E F5 PUSH AF
 F24F O12BF2 LD BC, GRBDON
 F252 C5 PUSH HL, (LSTRAM)
 F253 2AAF10 GARBEE: LD (STRBOT), HL
 F256 22C310 GARBUP: LD HL, O
 F259 210000 PUSH HL
 F25C E5 LD (STRSPC)
 F25D 2A5A10 PUSH HL
 F260 E5 LD HL, TMSTPL
 F261 21B310 GRBLP: EX DE, HL
 F264 EB GRBLP: LD HL, (TMSPTR)
 F265 2AB110 PUSH DE, HL
 F268 EB EX CPDEHL
 F269 CDBA6 CALL BC, GRBLP
 F26C 0164F2 LD NZ, STPOOL
 F26F C2BB8F2 LD (PROGND)
 F272 2AD610 SMPVAR: EX DE, HL
 F275 EB LD HL, (VARENDR)
 F276 2AD810 EX DE, HL
 F279 EB CALL CPDEHL
 F27A CDBA6 Z, ARRLP
 F27D C48BF2 LD A, (HL)
 F280 7E INC HL
 F281 23 INC HL
 F282 23 OR A
 F283 B7 STRADD CALL SMPVAR
 F284 CDBBF2 JP

; Test if enough room
; No garbage collection done
; Garbage collection done
; Save status
; Bottom of string space in use
; To DE
; Bottom of string area
; Negate length (Top down)
; Length to BC
; BC = -ve length of string
; Add to bottom of space in use
; Plus one for 2's complement
; Below string RAM area?
; Tidy up if not done else err
; Save new bottom of area
; Point to first byte of string
; Address to DE
; Throw away status push
; ; Garbage collect been done?
; 705 Error
; Yes - Not enough string space
; Flag garbage collect done
; Save status
; Garbage collection done
; Save for RETurn
; Get end of RAM pointer
; Reset string pointer
; Flag no string found
; Get bottom of string space
; Save bottom of string space
; Temporary string pool
; Temporary string pool pointer
; Temporary string pool done?
; Loop until string pool done
; No - See if in string area
; Start of simple variables
; End of simple variables
; All simple strings done?
; Yes - Do string arrays
; Get type of variable
; "S" flag set if string
; See if string in string area
; loop until simple ones done

F28A C1 GNARY: POP EX
 F28B EB ARRLP: LD DE, HL
 F28C 2ADA10 LD HL, (ARREND) ; End of string arrays
 F28F EB EX DE, HL
 F290 CDAE6 CALL CPDEHL
 F293 C4E1F2 JP Z, SCEND ; All string arrays done?
 F296 CD62F8 CALL LOADFP
 F299 7B LD A, E ; Yes - Move string if found
 F29A E5 PUSH HL ; Get array name to BCDE
 F29B 09 ADD HL, BC ; Save address of num of dim'n's
 F29C B7 OR A ; Start of next array
 F29D F2B8F2 P, GMARY ; Test type of array
 F2A0 22C510 LD (CUROS), HL ; Numeric array - Ignore it
 F2A3 E1 POP HL ; Save address of next array
 F2A4 4E LD C, (HL) ; Get address of num of dim'n's
 F2A5 0600 LD B, O ; BC = Number of dimensions
 F2A7 09 ADD HL, BC ; Two bytes per dimension size
 F2A8 09 ADD HL, BC ; Plus one for number of dim'n's
 F2A9 25 INC HL
 F2AA EB GRBARY: EX DE, HL ; HL, (CUROPR) ; Get address of next array
 F2AB 2AC510 LD DE, HL ; HL, (CUROPR) ; Is this array finished?
 F2AE EB EX CPDEHL
 F2AF CDBA6 CALL Z, ARRLP
 F2B2 C48BF2 JP BC, GRBARY ; Yes - Get next one
 F2B5 01AAF2 LD BC, GRBARY ; Loop until array all done
 F2B8 05 STPOOL: PUSH BC ; Save return address
 F2B9 F680 OR 80H ; Flag string type
 F2BC 7B STRADD: LD A, (HL) ; Get string length
 F2BD 25 INC HL
 F2BE 5E LD E, (HL) ; Get LSB of string address
 F2BF 23 INC D, (HL) ; Get MSB of string address
 F2C1 25 INC HL
 F2C2 F0 RETP P ; Not a string - Return
 F2C3 B7 OR A ; Set flags on string length
 F2C4 C8 REPZ ; Null string - Return
 F2C5 4D INC L, H ; Save variable pointer
 F2C7 2AC310 LD C, L ; Bottom of new area
 F2CA CDBA6 CALL HL, (STRBOT) ; Lowest available string area?
 F2CD 60 LD H, B ; String been done?
 F2CE 69 LD L, C ; Restore variable pointer
 F2CF DB RETC ; String done - Ignore
 F2D0 E1 POP HL ; Return address
 F2D1 E5 EX (SP), HL ; Lowest available string area?
 F2D2 CDBA6 CALL CPDEHL ; String within string area?
 F2D5 E5 EX (SP), HL ; Re-save return address
 F2D6 E5 PUSH HL ; Restore variable pointer
 F2D7 60 LD H, B ; Outside string area - Ignore
 F2D8 69 LD L, C ; Get return, Throw 2 away
 F2D9 D0 POP BC, AF, AF ; Save variable pointer
 F2DD E5 PUSH HL ; Save address of current
 F2DE D5 PUSH DE ; Put back return address
 F2DF C5 PUSH BC ; Go to it
 F2EO C9 RET

```

F2E1 D1E1 SCNEND: POP DE,HL ; Addresses of strings
F2E3 7D LD A,L ; HL = 0 iff no more to do
F2E4 B4 OR H ; Get string block, save return
F2E5 C8 RET Z ; Get length of string
F2E6 2B DEC HL ; No more to do - Return
F2E7 46 LD B,(HL) ; MSB of address of string
F2E8 2B DEC HL ; LSB of address of string
F2E9 4E PUSH HL ; Save variable address
F2EA E5 F2EB 2B DEC HL ; MSB of string address
F2EC 2B DEC HL ; LSB of string address
F2ED 6E F2EE 2600 DEC HL ; HL = Length of string
F2F0 09 ADD HL,BC ; Address of end of string+1
F2F1 50 INC D,B ; String address to DE
F2F2 59 LD E,C ; Next destination
F2F3 2B DEC HL ; Last byte in string
F2F4 44 LD B,H ; Address to BC
F2F5 4D LD C,L ; Current bottom of string area
F2F6 2A CD310 CALL HL,(STRBOT) ; Move string to new address
F2F9 CD7CE3 POP HL ; Restore variable address
F2FC E1 F2FD 71 LD (HL),C ; Save new LSB of address
F2FE 23 INC HL ; Save new MSB of address
F2FF 70 LD L,C ; Next string area+1 to HL
F300 69 F301 60 LD H,B ; Next string area address
F302 2B DEC HL ; Look for more strings
F303 C356F2 JP GARBLP ; Save prec' opr & code string
F306 C5E5 CONCAT: PUSH BC,HI ; Get first string
F308 2AE410 LD HL,(FPREG) ; Save first string
F30B E3 EX (SP),HL ; Get second string
F30C CD1IED CALL (OPRND) ; Restore first string
F30F E3 EX (SP),HL ; Make sure it's a string
F310 CD45ED CALL TSTSTR ; Get length of second string
F313 7E LD A,(HL) ; Save first string
F314 E5 PUSH HL ; Get second string
F315 2AE410 LD HL,(FPREG) ; Save second string
F318 E5 PUSH HL ; Add length of second string
F319 86 ADD A,(HL) ; ?LS Error
F31A 1E1C LD E,LS ; String too long - Error
F31C DAG1E3 JP C,ERROR ; Make temporary string
F31F CD BFFF1 CALL MCTMST ; Get second string to DE
F322 D1 POP DE ; Move to string pool if needed
F323 CD57F3 CALL GSTRDE ; Get first string
F326 E3 EX (SP),HL ; Move to string pool if needed
F327 CD56F3 CALL GSTRHL ; Save first string
F32A E5 LD HL,(TMPSTR+2) ; Temporary string address
F32B 2AC110 EX DE,HL ; To DE
F32E EB CALL SSTSAA ; First string to string area
F32F CD3DF3 CALL SSTSAA ; Second string to string area
F332 CD3DF3 LD HL,EVAL2 ; Return to evaluation loop
F335 2166ED EX (SP),HL ; Save return, get code string
F338 E3 PUSH HL ; Save code string address
F339 E5 JP TSTOPL ; To temporary string to pool
F33A C3FOF1

```

Dis-assembly of NASCOM ROM BASIC Ver 4.7

```

F382 0101F1 LEN: LD BC, PASSA          ; To return integer A
F385 C5 PUSH BC                         ; Save address
F386 CD50F3 GETLEN: CALL GETSTR        ; Get string and its length
F389 AF XOR A, A                         ; Clear D
F38A 57 LD D,A                          ; Set type to numeric
F38B 32AD10 LD (TYPE),A                 ; Set length of string
F38E 7E LD A,(HL)                      ; Set status flags
F38F B7 OR A                           ; Set status flags
F390 C9 RET                           ; Return

```

```

F391 0101F1 ASC: LD BC, PASSA          ; To return integer A
F394 C5 PUSH BC                         ; Save address
F395 CD86F3 GETFLNM: CALL GETLEN       ; Get length of string
F398 CAA0E9 Z, FCERR                   ; Null string - Error
F39B 23 INC HL                         ; Restore string block address
F39C 23 INC HL                         ; And re-save it
F39D 5E LD E,(HL)                      ; Get LSB of address
F39E 23 INC HL                         ; Get MSB of address
F39F 56 LD D,(HL)                      ; Get first byte of string
F3A0 1A LD A,(DE)                      ; Get first byte of string
F3A1 G9 RET                           ; Return

```

```

F3A2 3E01 CHR: LD A,1                  ; One character string
F3A4 CDBFFF1 MKTMST                   ; Make a temporary string
F3A7 CD887F4 CALL MAKINT                ; Make it integer A
F3AA 2AC110 LD HL,(TMPSTR+2)           ; Get address of string
F3AD 73 LD (HL),E                      ; Save character
F3AE C1 TOPOOL: POP BC                 ; Clean up stack
F3AF C5F0F1 TSTOPL: JP TSTOPL          ; Temporary string to pool

```

Dis-assembly of NASCOM ROM BASIC Ver 4.7

```

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F3B2 CD37F4 LEFT: CALL LFRGNM          ; Get number and ending ")"
F3B5 AF XOR A, (SP), HL               ; Start at first byte in string
F3B6 E3 RIGHT1: EX LD C,A             ; Save code string, Get string
F3B7 4F MID1: PUSH HL                ; Starting position in string
F3B8 E5 LD A,(HL)                   ; Save string block address
F3B9 7E CP B                         ; Get length of string
F3BA B8 CP B                         ; Compare with number given
F3BB DAC0F3 JP C, ALLFOL            ; All following bytes required
F3BE 78 LD A,B                       ; Get new length
F3BF 11 DEFB (LD DE, nn)             ; Skip "LD C, 0"
F3C0 0P00 LD C,0                     ; First byte of string
F3C2 C5 PUSH BC                   ; Save position in string
F3C3 CD29F2 CALL TESTR              ; See if enough string space
F3C6 C1 POP BC                      ; Get position in string
F3C7 E1 POP HL                      ; Restore string block address
F3C8 E5 PUSH HL                   ; And re-save it
F3C9 23 INC HL                     ; Get LSB of address
F3CA 23 INC HL                     ; Get MSB of address
F3CB 46 INC HL                     ; Get first byte of string
F3CC 23 INC HL                     ; Get first byte of string
F3CD 66 LD H,(HL)                  ; Get MSB of address
F3CE 68 LD L,B                      ; HL = address of string
F3CF 0600 LD B,0                    ; BC = starting address
F3D1 09 ADD HL, BC                 ; Point to that byte
F3D2 44 LD B,H                      ; BC = source string
F3D3 4D LD C,L                      ; Create a string entry
F3D4 CDC2F1 CALL CRTMST             ; Length of new string
F3D7 6F CALL TOSTRA                ; Move string to string area
F3D8 CD46F3 POP DE                 ; Clear stack
F3DB D1 CALL GSTRDE                ; Move to string pool if needed
F3DC CD57F3 CALL TSTOPL              ; Temporary string to pool
F3DF C3F0F1 JP TSTOPL

```

```

PAGE 66
F3E2 CD37F4 RIGHT: CALL LFRGNM          ; Get number and ending ")"
F3E5 D1 POP DE                      ; Get string length
F3E6 D5 PUSH DE                   ; And re-save
F3E7 1A LD A,(DE)                  ; Get length
F3E8 90 SUB B                      ; Move back N bytes
F3E9 C3B6F3 JP RIGHT1              ; Go and get sub-string

```

Dis-assembly of NASCOM ROM BASIC Ver 4.7

DISCUSSIONS OF VARIOUS DOCUMENTS

Dis-assembly of NASCOM ROM BASIC Ver 4.7

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Dis-assembly of NASCOM ROM BASIC Ver 4.7

```

; << NO REFERENCE TO THIS SECTION OF CODE >>
; << Set up another program area (can be in ROM) >>

F495 2A5E10 LD HL,(BASTXT) ; Get start of program text
F498 22D610 LD (PROGEND),HL ; Set more variable space
F49B 210080 LD HL,8000H ; Address of new program
F49E 5E LD E,(HL) ; Set LSB of new RAM end
F49F 23 INC HL ; D,(HL) ; Get MSB of new RAM end
F4A0 56 INC HL ; INC HL ; Null at start of program
F4A1 23 INC HL ; (BASTXT),HL ; New program text area 8003H
F4A2 23 LD DE,HL ; New RAM end to HL
F4A3 225E10 EX LD (LSTRAM),HL ; Set new RAM end
F4A6 EB PUSH HL ; (STRSPC),HL ; Clear string space
F4A7 22AF10 LD BC,RUNINT ; Execution driver loop
F4AA 225A10 LD BC,RUNINT ; Save for return
F4AD 01FF7 PUSH HL ; Clear variables and continue
F4B0 C5 JP RUNFST ; Get a byte from UART
F4B1 C3C5E4 GUART: JP GUART ; Send 2 Bytes to UART
F4B4 C356FD RUART: JP RUART ; Save byte
F4B7 CDBAFA4 WUART2: CALL WUART ; Save BC
F4BA F5 WUART: PUSH BC ; Save BC
F4BB C5 PUSH C,A ; Byte to C
F4BC 4F LD STAART ; Send byte to UART
F4BD CD68FD CALL POP BC ; Restore BC
F4C0 C1 POP AF ; Restore byte
F4C1 F1 RET ; Restore byt
F4C2 C9 CSAVE: LD B,1 ; File "CSAVE"
F4C3 0601 CSAVE: LD B,1 ; "CSAVE" token? ("CSAVE*")
F4C5 FEEA ZTINES ; Yes - Array save
F4C7 CABBE8 Z,ARRSV1 ; Evaluate expression
F4CA CD5AED EVAL ; Save code string address
F4CD E5 HL ; Get file name
F4CE CD95F3 CALL GTFLNM ; Save file name
F4D1 D5 PUSH DE ; Turn on motor and wait
F4D2 CDC8FC CALL CASPFW ; Restore file name
F4D5 D1 DE ; Header byte
F4D6 3ED3 LD A,11010011B ; Send byte to UART
F4D8 CDBAFA4 CALL WUART2 ; Send byte twice more
F4DB CDB7F4 LD A,(DE) ; Get file name
F4DE IA CALL WUART ; Send it to UART
F4DF CDBAFA4 NOP ; Start of program information
F4E2 00 NOP ; Save for monitor save routine
F4E4 00 NOP ; End of program information
F4E5 21D610 HL,PROGND ; Save for monitor save routine
F4F8 22C0C0C (ARG1),HL ; (PROGND)
F4EB 2AD610 LD HL,(PROGND) ; Save for monitor save routine
F4F4 22D80C ARG2),HL ; Save program to tape
F4F7 E1 HL ; Not much there!
F4F8 C9 RESTORE ; Restore code string address

```

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Dis-assembly of NASCOM ROM BASIC Ver 4.7

```

CLOAD: LD A,(HL) ; Get byte after "CLOAD"
        CP ZTIMES ; "/*" token? ("CLOAD*")
        JP Z,ARRLD1 ; Yes - Array load
        CALL SMOTOR ; Start motor and get "?"
        SUB ZPRINT ; "??" ("PRINT" token) Verify?
        JP Z,FLGVER ; Yes - Flg "verify"
        XOR A ; Flag "load"
        CALL (LD BC,mn) ; Skip "CPL" and "INC HL"
        FLGVER: CPL ; Flag "verify"
        INC HL ; Skip over "?"
        PUSH AF ; Save verify flag
        DEC 'cos GETCHR INCS ; DEC 'cos GETCHR INCS
        DECB HL ; GETCHR
        CALL GETCHR ; Get next character
        LD A,0 ; Any file will do
        JP Z,ANYNAM ; No name given - Any will do
        CALL EVAL ; Evaluate expression
        LD A,(DE) ; Get file name
        POP AF ; Get first byte of name
        LD L,A ; Save name to find
        CALL GTFLNM ; Get verify flag
        LD A,(DE) ; And re-save
        PUSH AF ; Verify of load?
        OR A ; Verify of load?
        LD H,A ; Save name of file to find
        CALL (FPREG),HL ; Load - Clear pointers
        CALL Z,CURPTR ; Get name of program to find
        LD HL,(FPREG) ; Name to DE
        CALL DE,HL ; 3 Header bytes
        CALL RUART ; Get a byte from UART
        SUB 11010011B ; Header byte?
        CALL NZ,CLOAD1 ; Look for header
        DEC B ; Count header bytes
        JP NZ,CLOAD2 ; More to find?
        CALL RUART ; Get name of file
        CALL FILFIND ; Display "file X found"
        INC E ; Any file name given?
        DEC E ; No - This file will do
        JP Z,THSFIL ; Has file been found?
        CALL CP E ; No - Look for another
        CALL NZ,CLOAD1 ; Verify program
        NOP ; Use monitor to verify program
        NOP ; Load or verify?
        AF ; Verify program
        OR A ; Use monitor to load program
        CALL MONVIE ; Get end of program
        LD HL,(PROGND) ; See if enough memory
        CALL ENFMEM ; "Ok" and set up pointers
        CALL CLOADV ; Set up line pointers
        CALL CLOAD ; Use monitor to verify program
        LD HL,OKMSG ; "Ok" message
        CALL PRS ; Output string
        CALL ARET ; Not a lot there!
        JP SETPR ; Set up line pointers

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Dis-assembly of NASCOM ROM BASIC Ver 4.7

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```

F56B 219DF5 OUTBAD: LD HL,BAD ; "Bad" message
F56E CD10F2 PRS ; Output string
F571 C3E1E3 ERRIN ; In line message

F574 C5 FILEND: PUSH BC ; <- Save
F575 E5 PUSH HL ; <- all
F576 D5 PUSH DE ; <- the
F577 F5 PUSH AF ; <- registers
F578 218EF5 LD HL,FILE ; "File" message
F57B CD10F2 CALL PRS ; Output string
F57E F1 POP AF ; Get file name
F57F F5 PUSH AF ; And re-save
F580 CDD9FC CALL COMMON ; Output file name to screen
F583 2194F5 LD HL,FOUND ; "Found" message
F586 CD10F2 CALL PRS ; Output string
F589 F1 POP AF ; <- Restore
F58A D1 POP DE ; <- all
F58B E1 POP HL ; <- the
F58C C1 POP BC ; <- registers
F58D C9 RETN

F58E 466966C5 FILE: DEFB "File ",0 ; "File ",0
F594 20465F75 FOUND: DEFB " Found",CR,LF,0 ; " Found",CR,LF,0
F59D 42616400 BAD: DEFB "Bad",0,0,0 ; "Bad",0,0,0

F5A3 GD8BE9 PEEK: CALL DEINT ; Get memory address
F5A6 1A LD A,(DE) ; Get byte in memory
F5A7 C301F1 JP PASSA ; Return integer A

F5AA CD41ED POKE: CALL GETNUM ; Get memory address
F5AD CD8BE9 CALL DEINT ; Get integer -32768 to 3276
F5B0 D5 PUSH DE ; Save memory address
F5B1 CD90E6 CALL CHKSYN ; Make sure "," follows
F5B4 2C DEFB "," ; Get integer 0-255
F5B5 CD84F4 CALL GETINT ; Restore memory address
F5B8 D1 POP DE ; Load it into memory
F5B9 12 LD (DE),A ; Load it into memory
F5BA C9 RETN

F5BB 2191FA ROUND: LD HL,HALF ; Add 0.5 to FPREG
F5BE CD62F8 ADDPHL: CALL JP ; Load FP at (HL) to BCDE
F5C1 C3CDF5 SUBPHL: CALL F5C4 CD62F8 ; FPREG = -FPREG + number at HL
F5C7 21 DEFBSUB: CALL F5C8 C1 PSUB: POP BC ; Skip "POP BC" and "POP DE"
F5C9 D1 POP DE ; Get FP number from stack
F5CA CD3CF8 SUBCDE: CALL F5CD 78 FPADD: LD A,B ; Negate FPREG
F5CE B7 OR A ; Get FP exponent
F5CF C8 RET Z ; Is number zero?
F5D0 3AE710 LD A,(FPEXP) ; Yes - Nothing to add
F5D3 B7 OR A ; Get FPREG exponent
F5D4 CA54F8 LD A,(BCDE) ; Is this number zero?
F5D7 90 SUB B ; Yes - Move BCDE to FPREG
F5D8 D2E7F5 JP NC,NOSWAP ; BCDE number larger?
F5DC 3C CPL ; No - Don't swap them
F5DD 2F INC A ; Two's complement
F5DC 3C EX DE,HL ; FP exponent
F5DE CD44F8 CALL STAFCP ; Put FPREG on stack
F5E1 EB EX DE,HL ; Move BCDE to FPREG
F5E2 CD54F8 CALL FPBCDE ; Restore number from stack
F5E5 C1 POP BC
F5E6 D1 POP DE
F5E7 FE19 NOSWAP: CP 24+1 ; Second number insignificant?
F5E9 D0 RET NC ; Yes - First number is result
F5EA F5 PUSH AF ; Save number of bits to scale
F5EB CD79F8 CALL SIGNS ; Set MSBs & sign of result
F5EE 67 LD H,A ; Save sign of result
F5FF F1 POP AF ; Restore scaling factor
F5F0 CD92F6 CALL SCALE ; Scale BCDE to same exponent
F5F3 B4 OR H ; Result to be positive?
F5F4 21E410 LD HL,FPREG ; Point to FPREG
F5F7 F20DF6 JP P,MINCDE ; No - Subtract FPREG from CDE
F5FA CD72F6 CALL PLUCDE ; Add FPREG to CDE
F5FD D253F6 JP NC,RONDUP ; No overflow - Round it up
F600 23 INC HL ; Point to exponent
F601 34 INC (HL) ; Increment it
F602 CABCE3 JP Z,OVERR ; Number overflowed - Error
F605 2E01 LD L,1 ; 1 bit to shift right
F607 CDA8F6 CALL SHRT1 ; Shift result right
F60A C353F6 JP RONDUP ; Round it up

F60D AF MINCDE: XOR A ; Clear A and carry
F60E 90 SUB B ; Negate exponent
F60F 47 LD B,A ; Re-save exponent
F610 7E LD A,(HL) ; Get LSB of FPREG
F611 9B SBC A,E ; Subtract LSB of BCDE
F612 5F LD E,A ; Save LSB of BCDE
F613 23 INC HL ; Get NMSB of FPREG
F614 7E LD A,(HL) ; Subtract NMSB of BCDE
F615 9A SBC A,D ; Save NMSB of BCDE
F616 57 LD D,A ; Get MSB of FPREG
F617 23 INC HL ; Subtract MSB of BCDE
F618 7E LD A,(HL) ; Save MSB of BCDE
F619 99 SBC A,C ; Overflow - Make it positive
F61A 4F LD C,A ; COMPL
F61B DC7EF6 CONPOS: CALL C,COMPL

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F61E 68 BNORM: LD L,B ; L = Exponent
 F61F 63 LD H,E ; H = LSB
 F620 AF XOR A ;
 F621 47 BNRMPLP: LD B,A ; Save bit count
 F622 79 LD A,C ; Get MSB
 F623 B7 OR A ; Is it zero?
 F624 C240F6 JP NZ,PNORM ; No - Do it bit at a time
 F627 4A LD C,D ; MSB == NMSB
 F628 54 LD D,H ; Increment exponent
 F629 65 LD H,I ; Round if ok
 F62A 6F LD I,A ; Round NMSB
 F62B 78 LD A,B ; Return if ok
 F62C D608 SUB 8 ; Save bit count
 F62E FEE0 CP -2E-8 ; Get MSB
 F630 C221F6 JP NZ,BNRMLP ; Set normal value
 F633 AF RESZR: XOR A ; Return if ok
 F634 32E710 SAVEXP: LD (FPEXP),A ; Move BCDE to FPREG
 F637 C9 RET ; More bits to shift

F638 05 NORMAL: DEC B ; Count bits
 F639 29 ADD HL,HL ; Shift HL left
 F63A 7A LD A,D ; Get NMSB
 F63B 17 RLA ; Shift left with last bit
 F63C 57 LD D,A ; Save NMSB
 F63D 79 LD A,C ; Get MSB
 F63E 8F ADC A,A ; Shift left with last bit
 F63F 4F LD C,A ; Save MSB
 F640 F238F6 PNORM: LD P,NORMAL ; Not done - Keep going
 F643 78 LD A,B ; Number of bits shifted
 F644 5C LD E,H ; Save HL in EB
 F645 45 LD B,L ; Any shifting done?
 F646 B7 OR A ; No - Round it up
 F647 CA53F6 Z,RONDUP ; Point to exponent
 F648 21E710 JP HL,FPEXP ; Add shifted bits
 F64D 86 ADD A,(HL) ; Re-save exponent
 F64E 77 LD (HL),A ; Underflow - Result is zero
 F64F D233F6 JP NC,RESZR ; Result is zero
 F652 C8 RET ; Get VLSB of number
 F653 78 RONDUP: LD A,B ; Point to exponent
 F654 21E710 RONDIB: LD HL,FPEXP ; Any rounding?
 F657 B7 OR A ; Yes - Round number up
 F658 FC65F6 CALL M,FPTRND ; B = Exponent

F65B 46 LD B,(HL) ;
 F65C 23 INC HL ; Get sign of result
 F65D 7E LD A,(HL) ; Only bit 7 needed
 F65E E80 AND 1000000B ; Set correct sign
 F660 A9 XOR C ; Save correct sign in number
 F661 4F LD C,A ; Move BCDE to FPREG
 F662 C354F8 FBCDE ; More bits to shift

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F665 1C FPRND: INC E ; Round LSB
 F666 C0 RET NZ ; Return if ok
 F667 14 INC D ; Round NMSB
 F668 C0 RET NZ ; Return if ok
 F669 0C INC C ; Round MSB
 F66A C0 RET NZ ; Return if ok
 F66B 0E80 LD C,80H ; Set normal value
 F66D 34 INC (HL) ; Increment exponent
 F66E C0 RET NZ ; Return if ok
 F66F C3BCE3 JP OVERL ; Overflow error

F672 7E PLUCDE: LD A,(HL) ; Get LSB of FPREG
 F673 83 ADD A,E ; Add LSB of BCDE
 F674 5F LD E,A ; Save LSB of BCDE
 F675 23 TNC HL ; Get NMSB of FPREG
 F676 7E LD A,(HL) ; Add NMSB of BCDE
 F677 8A ADC A,D ; Save NMSB of BCDE

F678 57 LD D,A ; Save NMSB of BCDE

F679 23 INC HL ; Get MSB of FPREG
 F67A 7E LD A,(HL) ; Add MSB of BCDE
 F67B 89 ADC A,C ; Save MSB of BCDE

F67D C9 RET ;

F67E 21E810 COMPL: LD HL,SGNRES ; Sign of result
 F681 7E LD A,(HL) ; Get sign of result
 F682 2F CPL ; Negate it

F683 77 LD (HL),A ; Put it back

F684 AF XOR A ; Set L to zero

F685 6F LD L,A ; Negate exponent, set carry

F686 90 SUB B ; Re-save exponent

F687 47 LD B,A ; Load zero

F688 7D LD A,L ; Negate LSB

F689 9B LD E,A ; Re-save LSB

F68A 5F LD A,L ; Load zero

F68B 7D LD A,D ; Negate NMSB

F68C 9A LD D,A ; Re-save NMSB

F68D 57 LD A,L ; Load zero

F68E 7D LD A,C ; Negate MSB

F68F 99 SBC C,A ; Re-save MSB

F690 4F LD C,A ; More bits to shift

F691 C9 RET ;

F692 0600 SCALE: LD B,0 ; Clear underflow
 F694 D608 SCALLP: SUB 8 ; 8 bits (a whole byte)?
 F696 DAA1F6 JP C,SHRTE ; No - Shift right A bits
 F699 43 LD B,E ; <- Shift
 F69A 5A LD E,D ; <- right
 F69B 51 LD D,C ; <- eight
 F69C 0E00 LD C,0 ; <- bits
 F69E C394F6 SCALLP ; More bits to shift

Dis-assembly of NASCOM ROM BASIC Ver 4.7

Dis-assembly of NASCOM ROM BASIC Ver 4.7

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```

F6A1 C609 SHRITE: ADD A,8+1 ; Adjust count
F6A3 6F LD L,A ; Save bits to shift
F6A4 AF XOR A ; Flag for all done
F6A5 2D DEG L ; All shifting done?
F6A6 C8 RET Z ; Yes - Return
F6A7 79 LD A,C ; Get MSB
F6A8 1F SHRT1: RRA ; Shift it right
F6A9 4F LD C,A ; Re-save
F6AA 7A LD A,D ; Get NM$B
F6AB 1F RRA ; Shift right with last bit
F6AC 57 LD D,A ; Re-save it
F6AD 7B LD A,E ; Get LSB
F6AE 1F RRA ; Shift right with last bit
F6AF 5F LD E,A ; Re-save it
F6B0 7B LD A,B ; Get underflow
F6B1 1F RRA ; Shift right with last bit
F6B2 47 LD B,A ; Re-save underflow
F6B3 C3A4F6 JP SHRLP ; More bits to do
F6B6 00000081 UNITY: DEF3 000H,000H,000H,081H ; 1.00000
F6B8 03 LOGTAB: DEF3 3 ; Table used by LOG
F6B9 AA561980 DEF3 OAAH,056H,019H,080H ; 0.59898
F6BF F1227680 DEF3 QF1H,022H,076H,080H ; 0.96147
F6C3 45AA3882 DEF3 045H,OAAH,038H,082H ; 2.88539
F6C7 CD13F8 LOG: CALL TSTSZN ; Test sign of value
F6CA B7 OR A ; 7FC Error 1f <= zero
F6CB EAA0E9 JP PE,FCERR ; Point to exponent
F6CE 21E710 LD HL,FPPXP ; Get exponent
F6D1 7E LD A,(HL) ; BCDE = SQR(1/2)
F6D2 013550 LD BC,8035H
F6D5 11F304 LD DE,04F3H
F6D8 90 SUB B ; Scale value to be < 1
F6D9 F5 PUSH AF ; Save scale factor
F6DA 70 LD (HL),B ; Save new exponent
F6DB D5 PUSH DE ; Save SQR(1/2)
F6DC C5 BC ; Add SQR(1/2) to value
F6DD CDCDF5 CALL FPADD ; Restore SQR(1/2)
F6E0 C1 POP BC ; Make it SQR(2)
F6E1 D1 POP DE ; Divide by SQR(2)
F6E2 04 INC B ; Point to 1.
F6E3 CD69F7 CALL DVBCDE ; Subtract FPREG from 1
F6E6 21B6F6 LD HL,UNTY ; Coefficient table
F6E9 CDC4F5 CALL HL,LOGTAB ; Evaluate sum of series
F6EC 21BAF6 LD SUMSER ; BCDE = -0.5
F6EF CD5BFB CALL LD,8080H
F6F2 018080 LD DE,0000H ; Subtract 0.5 from FPREG
F6F5 110000 CALL FPADD ; Restore scale factor
F6F8 CDCDF5 POP AF ; Re-scale number
F6FB F1 CALL RSCALE ; BCDE = Ln(2)
F6FF 013180 MULLN2: LD BC,8031H ; Skip "POP BC" and "POP DE"
F702 111872 LD DE,7218H ; (LD HL,nn)
F705 21 DEF3 ; Shift partial product left
F706 C1 MULT: POP BC ; Get number from stack
F707 D1 FPMULT: CALL DE ; Test sign of FPREG
F708 CD13F8 TSTSZN ; Return zero if zero
F70B C8 RET Z ; Flag add exponents
F70C 2E00 CALI, ADDEXP ; Add exponents
F70E CDD1F7 LD A,C ; Get MSB of multiplier
F711 79 LD (MULVAL),A ; Save MSB of multiplier
F712 32F610 LD DE,HL ; Save rest of multiplier
F715 EB EX (MULVAL+1),HL ; Partial product (BCDE) = zero
F716 22F710 LD BC,0 ; Address of normalise
F719 010000 LD D,B ; Save for return
F71C 50 LD E,B ; Address of 8 bit multiply
F71D 58 LD HL,MULT8 ; Save for NM$B,MSB
F71E 211EF6 LD HL,FPPREG ; Point to number
F721 E5 PUSH A,(HL) ; Get LSB of number
F722 212AF7 LD HL,MULT8 ; Point to number
F725 E8F5 LD HL,FPPREG ; Get LSB of number
F727 21E410 LD A,(HL) ; Point to NM$B
F728 7E MULT8: INC HL ; Test LSB
F72B 23 OR A ; Zero - shift to next byte
F72C B7 JP Z,BITSFT ; Save address of number
F72D CA56F7 PUSH HL ; 8 bits to multiply by
F730 E5 LD L,8 ; Shift LSB right
F731 2E08 MUL8LP: RRA ; Save LSB
F733 1F LD H,A ; Bit was zero - Don't add
F734 67 LD A,C ; Bit was zero - Don't add
F735 79 LD NC,NOMADD ; Save LSB and count
F736 D244F7 PUSH HL ; Get LSB and NM$B
F739 E5 LD (MULVAL+1) ; Add NM$B and LSB
F740 3AF610 LD DE,HL ; Leave sum in DE
F743 89 ADC A,C ; Restore MSB and count
F744 1F NOMADD: RRA ; Get MSB
F745 4F LD C,A ; Shift MSB right
F746 7A LD A,D ; Re-save MSB
F747 1F LD E,A ; Get NM$B
F748 57 LD A,B ; Shift NM$B right
F749 7B LD D,A ; Re-save NM$B
F74A 1F LD A,E ; Get LSB
F74B 5F LD E,A ; Shift LSB right
F74C 78 LD A,B ; Re-save LSB
F74D 1F RRA ; Get VLSB
F74E 47 LD B,A ; Shift VLSB right
F74F 2D LD L,DEC ; Count bits multiplied
F750 7C LD A,H ; Get LSB of multiplier
F751 C233F7 LD NZ,MUL8LP ; More - Do it
F754 E1 POPHRT: POP HL ; Restore address of number
F755 C9 RET ; Shift partial product left
F756 43 BYTSFT: LD B,E ; Shift sign of number
F757 5A LD E,D ; MSB
F758 51 LD D,C ; Count bits multiplied
F759 4F LD C,A ; Get LSB of multiplier
F75A C9 RET ; More - Do it

```

AUNT AGATHA'S AGONY COLUMN**By David Parkinson****GM809, GM829 compatibility/upgrading**

This is a question that has cropped up several times recently. The answer is that the Gemini GM829 FDC/SASI board can be regarded as a GM809HL. With the 'HL' level of trim you gain software controlled 5.25"/8" switching together with a SASI interface. Other than that the products are identical (ports, software interface etc). For some one currently running a system with GM809, upgrading is a matter of a) getting a GM829; b) Checking the straps; c) Plugging it in. That's all there is to it.

The software controllable 5.25"/8" switching of GM829 is quite useful - even if you don't have 8" drives connected to your system. This is because the Western Digital Floppy disk controller used on GM809/GM829 is limited in the maximum rate it can step the head between tracks on the attached drives. When set for 5.25" drives it can only achieve a 6ms stepping rate, but the modern Japanese drives (e.g. TEAC FD55Es & Fs) can be stepped at 3ms/step. Running them at the slower rate results in reduced performance and a 'graunching' noise from the drive (non destructive!). However, with GM829, whenever a SEEK is required, the 5.25"/8" control can be flipped to 8". The main effect of this is to double the clock frequency to the Controller chip, which results in all stepping times being halved. Thus drives can now be stepped as fast as 3ms/step, leading to increased performance and much quieter stepping. (Once the seek is complete, the control bit is obviously flipped back to the 5.25" setting before doing the read/write operation.)

BASIC mathematics

A letter from Phil Dunglinson on the topic of a BASIC program that doesn't work provides me with my next topic. The listing is shown below:

```

10 FOR N3=1 TO 9
20 FOR N2=0 TO 9
30 FOR N1=0 TO 9
40 A= N1^3 + N2^3 + N3^3
50 B= N1 + N2*10 + N3*100
60 IF A=B THEN 80
70 GOTO 90
80 PRINT B
90 NEXT N1
100 NEXT N2
110 NEXT N3
120 END

```

It should print out the results of 153,370,371,407 but doesn't. Can anyone see why not?

The answer is simple, and reminds me of that old adage about not blindly accepting the answer that comes out of a computer. Just because your computer tells you that $2<>2$ or $3=2$ does not necessarily mean that it is true. Remember that your computer is an idiot and tries to do exactly what you tell it. It can do mundane operations very quickly, but it does have limitations and this example highlights two of them.

Point 1: Derived Arithmetic Functions.

To us normal Human Beings $N1^3$ ($N1$ raised to the power 3) in line 40 of the above program means $N1*N1*N1$. However to Nascom BASIC it means: $\text{EXP}(3*\text{LOG}(N1))$, where both the $\text{LOG}()$ and the $\text{EXP}()$ are calculated by evaluating polynomial approximations of the form:

$$C_0 + C_1*X + C_2*X^2 + C_3*X^3 + \dots + C_N*X^N$$

X is the value passed to the approximation routine, and C_0, C_1, \dots, C_N are constants whose value depend upon the approximation required (EXP , LOG , SQR etc). Usually X has to be scaled to lie within a certain range for the approximation to be valid. The accuracy of the approximation obviously depends upon the degree of the polynomial, and for some functions a polynomial of low degree (of only 3 say) can be surprisingly accurate. From a computational speed

point of view, the shorter the polynomial, the faster it can be evaluated. For those who wish to pursue matters further various books can be found covering the topic. One fairly comprehensive book I have encountered is [1].

So point one is that derived functions such as " \wedge ", LOG, EXP, etc are written for the general case and have approximate results. (They may be accurate, but not necessarily exact.) They do not recognise specific cases (such as the exponent in a " \wedge " expression being a small integer) and do not adjust their algorithms accordingly. This means that the instruction in line 60 (IF A=B THEN...) is almost certainly going to be false. If A=2 and B=1.999999999..... then they are not equal in the eyes of the computer although an engineer would happily accept them as such. (Mind you a Scientist may not, but that leads on to the old joke...) Therefore line 60 has to be rephrased as - IF A EQUALS B FOR ALL PRACTICAL PURPOSES THEN This can best be done by coding it as IF ABS(A-B)<1E-4 THEN.... Here we have said if the two values are within 1/10,000 of each other then take them as equal. After making this change the program above will run successfully.

Alternatively the program can be recoded to make the calculation more accurate. By recoding line 40 as ... $N1*N1*N1 + N2*N2....$ we replace the approximation by an accurate calculation. (Accurate in this case as we are dealing with reasonably sized INTEGERS. In other circumstances - e.g. N1 etc being REAL numbers like 2.345 and 7.916 - there would be rounding errors and possible dynamic range problems affecting the accuracy of the result.) This change also has the side effect of speeding up the program as the two multiplications are faster than the \wedge function. In making this change to line 40, line 60 can be left as IF A=B... (but remember the caveat above).

Point 2: Binary Arithmetic

While we are on the topic of computer accuracy I'll just mention one other point. Computers that use binary arithmetic cannot hold most decimal fractions accurately. (e.g. 2.67 might be held as 'a number very close to 2.67'.) This is why any serious financial program always uses BCD (Binary Coded Decimal) arithmetic - where 2.67 IS 2.67 - rather than pure binary arithmetic. That way the books generally balance exactly rather than approximately as the arithmetic exactly matches the human 'pencil & paper' mode. Details of BCD algorithms can be found in [2].

Software Testing

The example above highlights another important point that we are all frequently guilty of, and that is inadequate testing of programs. This program is not perhaps the best example as it does not process any external data in order to produce its result. The important point though is that Phil Dunglinson knew what the program should do (in its present form) and when the correct results didn't emerge he knew there was a bug to find. Software testing is an art. (Think of the unlimited character combinations possible in the source input file for an assembler or compiler.) There are various books on the topic that you can read if you are interested [3][4]. I don't intend to cover software testing here, but one thing to remember is that any program that processes data, as well as producing correct output from correct input, must not accept incorrect input without complaining, or crash when presented with the unexpected.

Just to give a few examples of what I've encountered: First a minor bug illustrating what can happen to a program presented with the unexpected. With C/80 version 2.0 the compiler carried on compiling a source file past the end-of-file marker if the file ended in a TAB character rather than the usual CR/LF pair. (Easy enough to end up with a TAB if you use an on-screen editor.)

Next a minor bug but slightly more serious. Old habits die hard, and in some C/80 source files I entered a few hexadecimal constants with a trailing 'H' (e.g. 0xAH rather than the correct 0xA). The compiler accepted these without comment and interpreted the 'H' as part of the Hex number so I ended up with an incorrectly evaluated constant. Finally my latest encounter has been with an unforgiveable bug from Microsoft in an 8086 cross assembler. Being relatively inexperienced in 8086 assembly language programming my initial programs contained some glaring errors like loading a segment register with an immediate 16-bit value. (Although you can load normal registers with immediate data, the segment registers can only be loaded with data from another register, or from memory.) The Microsoft assembler accepted my illegal instructions without complaint, and proceeded to generate an opcode for a completely different instruction. It was only by manually dis-assembling the opcodes on the assembly listing that I found the error. (Luckily, as I was just starting to familiarise myself with the 8086, the program was only some 25 lines long and it didn't long to find the error.)

One final comment on this topic - if you do find a bug REPORT IT, not by complaining to your friends, but to the Author. Document the bug thoroughly, for example by writing a four or five line program to illustrate it, and send the print-outs in with your comments. DON'T ASSUME SOMEBODY ELSE MUST HAVE ALREADY REPORTED IT. You might find that your report vanishes into a black hole, or, as I did with the Software Toolworks, get a polite note back and find the bugs corrected in the next release of the software (C/80 version 3.0).

Do-it-yourself Electrocution.(c.f. Richard Beal in the last issue!)

Next a request from Mr E.Jones for an article covering the general principles of converting a domestic TV set to a monitor, and interfacing a NASCOM to it. These sort of articles tend to be few and far between, and I assume that the reason for this is that no magazine editor wishes to be sued by the relatives of a late hobbyist who attempted to follow the article. **VOLTAGES INSIDE A TV SET OR MONITOR ARE LETHAL.** If you do not know what you are doing **LEAVE THE BACK ON THE SET.** If you think you know what you are doing I still advise you to leave the back on the set. With the proliferation of home computers and video recorders that has occurred in recent years it should be possible to pick up a cheap black-and-white monitor, or to buy a TV set that already has a video input socket on the rear. For those who want to progress further I offer the following suggestions/observations.

(1) Before doing anything buy a copy of the service sheet or service manual for the set in question.

(2) Look at the power supply section of the diagram. Many sets directly rectify the mains input and then use the filtered DC supply either directly, (old Valved sets), or via a switch-mode step-down power supply (newer integrated sets). Certainly in the first case, and possibly in the second, you will find that one side of the TV chassis is connected directly to the NEUTRAL of the mains supply, (or directly to the LINE side if the plug has been wired incorrectly). In this case DO NOT PROCEED FURTHER unless you can isolate the TV supply from the mains, or provide a suitable barrier at the video interface into the TV. (A small black-and-white portable offering mains/battery operation will almost certainly have a mains transformer providing the required isolation.)

(3) The signal present at the VIDEO pin on the Nascom 2 circuit board is a conventional composite video signal containing a mixture of Video and the horizontal and vertical sync pulses. This is similar to the signal present at the output of the detector following the IF stages in the TV. I haven't looked

at any TV circuit diagrams recently, but with transistorised sets this point can be found relatively easily. With a modern set full of ICs you may find that the point you're after lies within an IC. You need to inject the composite video signal (of the appropriate amplitude and polarity) at a point just before the video and sync signals are separated.

(4) ALWAYS SWITCH OFF AND ALLOW TIME FOR HIGH VOLTAGES TO DISCHARGE BEFORE MAKING ANY CHANGE TO YOUR CIRCUITS.

(5) If you must make any internal adjustments on a live set always use only one hand, keeping the other well out of the way (like in your pocket). DON'T GROPE ROUND THE BACK OF THE SET. If you need to see the screen while you make the adjustment sit behind the set and use a mirror. ALWAYS KEEP AN EYE ON THE HAND MAKING THE ADJUSTMENTS. As well as high voltages there are usually HOT resistors in the set. If you touch one of those inadvertently who knows what your reflex action may make you come in contact with, and you may end up with far more than a burnt hand.

CP/M Users Group

Some time ago I mentioned the CP/M Users Group. The CP/M Users Group (UK) publishes a magazine (quarterly?) that varies in content. It normally contains news, reviews and comment. In the last issue (March 1984) over half the magazine was a listing of the new UK and SIG/M disks that have recently been added to the library. Currently a years subscription costs £7.50. The CP/M Users Group can be found at 72 Mill Road, Hawley, Dartford, Kent DA2 7RZ.

Using a Nascom as a counter-timer.

Next on the pile is a rather confused letter from someone in Southampton who had better remain nameless. (Anyway we reckon the name was a pseudonym.) He is wanting to use the interrupt line on a Nascom to measure the duration and frequency of a pulse stream. For some obscure reason (not stated) he wants to use interrupt mode 1. This effectively executes a 'RST 38' in response to an interrupt, vectoring to address 38H. However Nas-Sys has its "RDEL" routine at that address which is not much use in measuring anything! As a result he has got tied up in knots trying to get a RAM based version of Nas-Sys going. He is trying to do this so that he can patch the RST 38 location to jump to his own routine. I'll ignore the mire he has got into with his attempt to get Nas-Sys into RAM and point out the large errors he has made and a possible approach to the problem.

1) The interrupt problem.

Use Mode 2. There is nothing mysterious about it, it just needs a little thought as there are two levels of indirection to go through before you arrive at your service routine which can be anywhere in memory. If you must use mode 1 why not use NMI instead of INT? In response to an NMI, Nas-Sys 3 vectors via a JP stored in RAM at 0C7D. (i.e. At address 66 - the NMI execution address - Nas-Sys has a JP 0C7D. It initialises 0C7D to a JP <register display routine>.) Thus anyone wanting to use the NMI for another purpose can change the JP stored in the workspace area so that control passes to their routine, rather than back into Nas-Sys. (If necessary you connect your interrupt signal via a 2-input OR gate (e.g. 74LS32) to the NMI input. By connecting the other input of the OR gate to a spare bit on the PIO you can have a 'maskable' NMI).

2) Why there is no point in doing it anyway.

To go back to his original requirement. "To input a stream of variable length pulses direct into the Z80 pin 16 /INT, and thereafter analyse their lengths & groupings by program". He has started off by making a fundamental

error. The INT input to the Z80 is level sensitive. i.e. While the /INT signal is low an interrupt will occur immediately interrupts are enabled. I cannot conceive of how the /INT pin could be reasonably used to measure the duration of pulses. (The only thing that comes to mind is a service routine that starts EI, NOP, followed by code that counts the number of return addresses that have been pushed onto the stack by the successive interrupts - hardly a practical proposition.) The situation with the NMI input is virtually the same. The only difference is that it is an edge triggered input. The NMI is generated on the high-to-low transition of the signal on this pin and the low-to-high transition has no effect. Therefore a program could collect statistics of the frequency of occurrence of the pulses, but could not determine anything about their duration. Neither pin is suitable for the measurement of pulse widths.

3) Try the PIO.

The obvious candidate for this task is the PIO. It can be set into mode 3 (control mode) and the levels present on one or more of the external inputs can be monitored by the control software. But before anything can be done we need to know something about the characteristics of the pulses that are to be monitored. Obviously if they are very short (of the order of 1uS) and very frequent there is no way that the Z80 can be used for this purpose. Pulses of milliseconds or seconds duration can be measured relatively easily, but a few calculations should be done to determine the relative accuracy obtainable, and whether it is acceptable.

A simple timing loop could be:

```

ld   h1,0      ; Clear counter
loop: inc  h1     ; Bump counter
      in   a,(pio)  ; Read port
      rlca          ; Put bit into carry
      jp   c,loop   ; Loop if still ON
      ....         ; Check count & save data
    
```

With this there is obviously an uncertainty equal to the software loop time in determining when the pulse goes OFF. (It could go OFF just before or just after the IN instruction is executed. In the latter case another complete loop is executed although the time difference between the two cases could only be nanoseconds.) Similarly there can be a similar uncertainty in recognising the ON instant. The basic unit measurement here is the 'LOOP' (above) and we can measure to an accuracy of +/- 2 LOOPS.

The above is a very simplified view of matters as there is also the problem of measuring the intervals between pulses. (Note also the above loop assumes that a 16-bit counter is adequate as the loop counter. For long pulses this is unlikely to be so, and the loop will have to be expanded to include an overflow check of the counter. However, as the pulse is longer, we can accept a greater absolute error as the relative error will still probably be small.)

The only thing to do is to sit down and write some of the software and start counting clock cycles. You may find it advantageous to add dummy instructions into some conditional paths so that all routes from point A in the software to point B take about the same amount of time. That way a software timer-counter can be updated with a fixed number to compensate for the execution of a particular service routine. (e.g. The routine that takes the last pulse measurement and updates a histogram table based upon the pulse width in 'loop counts'.)

Life may be easier if you use an I/O board which includes a CTC (Counter Timer Circuit).

NASCOM(?) 1.5(?)

Finally a letter from Steve Waites which I reproduce below:

"Back in the dark annals of history I had a perfectly good and working Nascom 1 + RAM A card. Simple but effective. Then I left to work in America so the poor old computer got left behind. Two years later I returned, and found that the old Nascom had pined itself away to an early demise. Checking the boards I found several of the 2102 RAMs and a few TTL chips had gone to the great semiconductor heaven. In desperation, and because I only had two weeks in England, I grabbed all the relavent software and made the decision to rebuild and upgrade. Several months of hard effort later I now welcome the Nascom 1.5. Why 1.5? Well its no longer a Nascom 1, nor is it quite a Nascom 2. My additions are an enhanced RS232 port, 18K of CMOS RAM on the main board plus 4118 RAMs for the video and user areas. Oh yes it also has a sound chip, a battery backed RAM area, graphics, and the memory is fully decoded in 2K blocks by a 74LS154.

"So far so good - everything works wonderfully - now to my question. Is there anyway that I can get my ZEAP (level 2.1 in ROM) to output continuously to the UART port as I usually use my computer with a slave CRT connected to the RS232 interface rather than the internal video. Also, but not quite so important, I have the Bits & PCs programmers toolkit. It won't accept commands from the remote CRT keyboard. Both Nas-Sys and BASIC have no problems in this respect, so what is the difference?"

Perhaps some of you ZEAP hackers can write in with a solution for Steve. I believe that ZEAP writes directly into the bottom line of the NASCOM display. This is done for speed, as outputing a character at a time via Nas-Sys would have an impact on the already slow assembly speed of ZEAP. How much of ZEAP's output goes this way I have no idea. As for the Bits & PCs toolkit, this uses its own input and output tables, and also utilises UIN and UOUT. I assume that a small error must exist in this somewhere which prevents it scanning the serial input port. Perhaps some enterprising person who is using the toolkit can send in a suitable patch that can be published in a subsequent issue of the NEWS?

Ta Ta.

References:

1. HART J.F., CHENEY E.W. et al, "Computer Approximations", Pub: John Wiley 1978
2. SCHMID H., "Decimal Computation", Pub: John Wiley 1974
3. MYERS G.J., "The Art of Software Testing", Pub: John Wiley 1979.
4. BRUCE R.C., "Software debugging for Microcomputers", Reston Publ. Co. 1980

Arfon Speech Board - #50. 16K RAM A board - #15. 32K RAM B board - #40. Easicomp PSG board (minus AY-3-8910 chip) - #10. Machine Code Programming for the Nascom Book - #2. Phone Kevin on 0224-36160 (evenings).

IBM Selectric KBD printer, ex. 2741, with hardware/sofware interface for Nascom & Nas-Sys. Uses 8 bits of PIO. Previously IBM maintained, in excellent working order. Sensible offers please to Ian on Ipswich (0473) 831353.

Polydos File Update**By M.J.R. Gibbs**

This is a program for directly updating files on disk for a Gemini GM809/GM815 system with Polydos 2.0.

This allows the user to update copies of a file stored on several disks without having to enter all the normal commands which can cause problems especially if you are like me and sometimes forget to use the 'NEW' command (I have spent many hours trying to sort out the chaos that this causes). The required program/data is loaded into RAM between #1000 and #C000 either by using the Polydos 'Read' command or by Assembling the program directly into RAM.

This program is loaded into RAM at #OC80 and executed at #OC80, the user is asked for the file name, file extension and the RAM location for the new code. The Program asks you to insert the disks and press the 'Enter' key, the disk directory is read and the filename checked and, should it exist, the file is replaced with the new version. The old file is completely overwritten and this means that the number of sectors used by the old file are replaced by the program stored in RAM. The user should be careful in ensuring that the new version is not going to require more sectors on disk than the original otherwise the whole of the new program will not be saved. I normally save programs that are common to several disks with a few sectors more than required to allow for expansion, this does not normally cause a problem as I have found that the maximum number of fifty files means that the disk is not often full before the directory is used up. This program can update a disk with a full directory because the directory is left unaltered.

Should the program not exist on the disk inserted a suitable message is printed out telling the user and the disk is left unaltered.

The Program informs the user of the start disk sector and the number of sectors updated and the RAM location used for the update. Should there be any disk errors then a message is printed out and the program repeats the initial menu asking the user to insert a new disk. The one thing that I have found is that it is easy to forget to remove any write protect tabs and this does not cause a problem as you simply take the disk out remove the tab and reinsert the disk (do not forget to replace the tab afterwards).

A word of warning when testing this program especially when entering it from the dump listing, which is always prone to errors, you are strongly advised to test it on a copy of an existing disk because it can overwrite irreplaceable portions of disk if anything goes wrong.

I suggest that you proceed as follows:-

- 1) Enter the program and save it on disk using Polydos. Use the 'Read' routine to load a file from the disk to say #1000 in RAM.
- 2) Examine and make a note of the number of sectors that this file uses.
- 3) Use SZAP to examine the next file on the disk and make a note of the first few bytes.
- 4) Change the first few bytes of the file loaded into RAM with the NAS-SYS 'M' (modify command).
- 5) Run this program to update the disk and check the user information for the correct sector location, number of sectors and RAM location.
- 6) Use SZAP to verify that the original file has been updated correctly. Also that the file following is not corrupted.

Below is a full Assembly listing, a sorted symbol table and a dump of the program using a modified version of the disk dump published in Vol.1 iss. 2 of 80-BUS NEWS (the numbers on the left hand side are the RAM locations).

```

Z2 ASSEMBLY LISTING ..... PAGE 2
-----
OC21      NUMV    EQU    #OC21
          CLEAR   EQU    #OC
          CR     EQU    #0D
          000D   EQU    #1B
          001B   EQU    #1B
          001B   ORG    #0C80
          OC80   LOAD   #8000
          OC80   ;----- DISK UPDATE -----
          OC80   ;----- DISK UPDATE -----
          OC80   CD230E  UPDATE CALL  HEAD
          OC83   EF     RST    PRS
          OC84   4469736B
          OC88   20557064
          OC8C   61745520
          OC90   46698C65
          OC94   204E616D
          OC98   6520812E
          OC9C   2E3A2D20
          OCA0   00     DB    'Disk Update File Name ...:- ',0
          OCA1   DF     RST   MASSYS
          OCA2   63     DB    ZINLIN
          OCA3   211C00 LD    HL,28
          OCA6   19     ADD   HL,DE
          OCA7   115560 LD    DE,FNAM
          OCAA   010800 LD    BC,8
          OCAA   010800 LD    DE,FNAM
          OCAA   010800 LD    BC,8

```

Z2 ASSEMBLY LISTING PAGE 3

Z2 ASSEMBLY LISTING PAGE 4

```

OCFA 00      DB    'RAM Start Address .....:= ',0
              LD    HL,(CURSOR) ;HL = CURSOR LOCN
OCFB 2A290C   ERR10   LD    (CURSOR),HL ;RESET CURSOR
              PUSH   HL
              ;KEEP CURSOR LOCN
OD01 E5      RST    NASSYS
              DB    TNLIN
              ;GET REPLY
OD02 DF      RST    NASSYS
              DB    DE
              ;DECODE REPLY
OD03 63      POP    DE
              PUSH   HL
              ;NUM MODS HL
OD04 D1      RST    NASSYS
              DB    NUM
              ;SORT OUT HEX NUMBER
OD05 E5      RST    NASSYS
              DB    NUM
              ;RESTORE HL
              ;ERROR??
OD06 DF      RST    C,ERR10
              POP    JR
              ;HL = NUMBER
OD07 64      RST    NASSYS
              DB    (NUMV)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;KEEP REPLY
OD08 E1      RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD09 38F3    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD0A 2A210C   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD0B 2A210C   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD0C 22350F   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD0D 0E      RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD11 CD630E   LOOP   CALL   HEAD2
              RST    NASSYS
              DB    BLINK
              CP    ESC
              JP    Z,DSKEND
              ;STOP
OD12 3EFF    LD    A,#FF
              LD    (TDRV),A ;FORCE DIRECTORY READ
              LD    C,0 ;DIRECTORY DRIVE 0
              RST    NASSYS
              DB    ZDIR ;READ DIRECTORY
              LD    NZ,LOOP ;ERROR MAYBE NO DISK
              LD    HL,S1FCB ;HL = A(FCB)
              LD    B,#30 ;LOAD & LOCKED FILES
              RST    NASSYS
              DB    ZLOOK
              JR    Z,LOADIT
              RST    PRS
              DB    CR,CR
OD13 0D0D    RST    NASSYS
              DB    CR,CR
OD14 DF      RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD15 7B      RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD16 FE1B    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD17 CA1C0E   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD18 3201C0   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD19 0E00    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD20 0E00    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD21 0E00    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD22 DF      RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD23 83      RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD24 20EB    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD25 2155C0   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD26 2155C0   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD27 0630    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD28 DF      RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD29 0630    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD30 0D0D    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD31 20202020 RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD32 20202020 RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD33 3D3D3D   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD34 2046A94C RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD35 4524E4F  RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD36 2046A94C RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD37 5420464F RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD38 554E4420 RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD39 3D3D3D   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD40 3E00    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD41 2003    RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD42 4524E4F  RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD43 C3110D   LOADT CALL  HEAD
              LD    HL,(FSEC)
              ;LOAD DISK
              ;HL = SECTOR LOCATION
OD44 2A61C0   RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD45 F5      ERROD  NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD46 F5      ERROD  NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES
OD47 EF      RST    NASSYS
              DB    (RAMPOS)
              LD    (RAMPOS),HL ;HL = RAM POSN-
              ;MAIN LOOP FOR LOADING FILES

```

OD5E EF RST PRS
 OD5F 20202020 RST
 OD63 20202044 RST
 OD67 69736B20 RST
 OD6B 5365674 RST
 OD6F 6F7222E RST
 OD73 2E2E23E RST
 OD77 2E2E23A RST
 OD7B 3D2000 DB
 OD7E DF RST
 OD7F 66 DB
 OD80 2A350F LD
 OD83 EF RST
 OD84 0D0D DB
 OD86 20202020 DB
 OD8A 20202046 DB
 OD8E 726F6120 DB
 OD92 52616D20 DB
 OD96 2E2E2EE DB
 OD9A 2E2E2E2 DB
 OD9E 2E2E2E3A DB
 ODA2 3D2000 DB
 ODA5 DF RST
 ODA6 66 DB
 ODA7 3A63C0 LD
 ODA8 47 LD
 ODA9 47 LD
 ODAE EF RST
 ODAC 0D0D DB
 ODAE 66 DB
 ODAF 3A63C0 LD
 ODB1 20202046 LD
 ODB2 20202046 LD
 ODB6 756D6265 LD
 ODBA 72206F66 LD
 ODBE 20536563 LD
 ODC2 746F7273 LD
 ODC6 202E2E3A LD
 ODCA 3D202020 DB
 ODCE 00 DB
 ODCE 78 LD
 ODD1 DF RST
 ODD1 68 DB
 ODD2 C5 PUSH BC
 ODD3 DF RST
 ODD4 5D DB
 ODD5 C1 POP BC
 ODD6 0B00 LD
 ODD8 ED5B61C0 LD
 ODDC 2A350F LD
 ODDF DF RST
 ODE0 82 DB
 ODE1 2003 LD
 ODE3 C3110D LD
 ODE6 F5 JP
 ODE7 EF PUSH AF
 ODE7 EF RST

Number of Sectors :::: ,0
 ;A = NUMBER OF SECTORS

;B = NUMBER OF SECTORS

;WAIT TO SHOW

;RECOVER BC

;C,0 = DISK DRIVE

;DE, (FSEC)

;IDE = A (SECTOR)

;HL, (RAMPOS)

;HL = A (RAM)

;WRITE IT OUT

;NEXT

;KEEP ERROR NUMBER

Z2 ASSEMBLY LISTING PAGE 5

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```

0D8 0D04572          DB CR,CR,"Error > ",0
0DE5 726F7220         DB
0DF0 3D3D3D3E         DB
0DF4 2000             DB CR,CR,"Error > ",0
0DF6 F1               AF
0DF7 DF               POP
0DF8 88               RST
0DF9 450D7367         DB ZCOV ;LOAD ERROR OVERLAY
0DFD EF               DB "Emsg" ;PRINT MESSAGE
0DFE 0D0D             DB PRS
0E00 20202020         DB CR,CR
0E04 20202050         DB
0E08 72657373         DB
0EOF 2022456E         DB
0E10 74657222         DB
0E14 202000             Press "Enter" ,0
0E17 DF               RST
0E18 7B               DB
0E19 C3110D            DB
0E1C 3EFF              DSKEND LD
0E1E 3201C0            LD
0E21 DF               RST
0E22 5B               DB
0E23 EF               HEAD
0E24 OC               DB
0E25 20202020         DB
0E29 20202020         DB
0E2D 20202020         DB
0E31 20444953         DB
0E35 AB205550         DB
0E39 44415445         DB
0E3D 20555449         DB
0E41 4C495459         DB
0E45 OD               DB
0E46 20202020         DB
0E4A 20202020         DB
0E4E 20202020         DB
0E52 20202020         DB
0E56 2D2D2D2D         DB
0E5A 2D2D2D2D         DB
0E5E 2D2D2D2D         DB
0E62 2D2D2D2D         DB
0E66 0D0D0D00         DB CR,CR,CR,0
0E6A C9               RET
0E6B CD230E            HEAD2
0E6E EF               CALL
0E6F 20202020         DB
0E73 20202046         DB
0E77 6966C6320         DB
0E7B 4E616D65         DB
0E7F 202E2B2E         DB
0E83 2E2E2B2E         DB
0E87 2E2E2E3A         DB

0E8B 2D2000             DB
0E8E 2155C0             LD
0E91 0608             LD B,8 ;PRINT FILE NAME
0E93 7E               HEAD10 A,(HL)
0E94 F7               RST
0E95 23               INC HL ;INDEX ALONG
0E96 10FB             DJNZ HEAD10
0E98 EF               RST
0E99 2B00             PRS
0E9B 0602             DB
0E9D 7E               HEAD20 A,(HL)
0E9E F7               RST
0E9F 23               INC HL ;PRINT FILE EXT
0EA0 10FB             HEAD20
0EA2 EF               RST
0EA3 0D0D             DB CR,CR
0EA5 20202020         DB
0EA9 2020204C         DB
0EAD 6F616465         DB
0EB1 64204672         DB
0EB5 6F6D2052         DB
0EB9 414D202E         DB
0EBD 2E2EE3A           DB
0EC1 2D2000             Loaded From RAM . . . . . ,0
0EC4 2A350F             LD HI,(RAMPOS)
0EC7 DF               RST
0EC8 66               DB
0EC9 EF               TBCD3 ;PRINT IT
0ECA 0D0D             DB CR,CR
0ECC 20202020         DB
0ED0 20202049         DB
0ED4 6E736572         DB
0ED8 742B4469         DB
0EDC 736B2069         DB
0EE0 6E746F20         DB
0EE4 44726976         DB
0EE8 6523302E         DB
0EEC 0D               DB CR
0EDD 20202020         DB
0EF1 20202050         DB
0EF5 72557373         DB
0EF9 2022456E         DB
0EFD 74657222         DB
0E01 20776865         DB
0E05 6B207265         DB
0E09 6164792E         DB
0E0D OD               DB CR
0F0E 20202020         DB
0F12 20202050         DB
0F16 72657373         DB
0F1A 20224573         DB
0F1E 63222020         DB
0F22 20746F20         DB

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```


REVIEW OF COMPASS, ZAP, AND RAVEN ASSEMBLERS**By Stephen Weir**

COMPASS (not to be confused with COMPAS Pascal), ZAP and RAVEN are tape based assemblers for use with the Nascom range of microcomputers.

COMPASS

COMPASS stands for COMPression ASSEMBler and is written by Level 9, the same people who write those excellent adventures. As you would expect therefore, COMPASS is a good quality piece of software with no bugs. It comes on a TDK cassette with a neatly printed label together with a user manual bound in the usual "Level 9 blue". On the tape there is the assembler itself within a relocator program followed by a program to convert ZEAP files to COMPASS files should you wish to change over to COMPASS from ZEAP but still use your ZEAP files. Finally, there is the source for the ZEAP convertor so that you can see how it works and also use it to try out the editing facilities.

The relocator is very useful as you can relocate COMPASS to the top of your RAM and then you have the rest of RAM from 1000h upwards free. Once relocated, COMPASS takes up about 7K. On cold start it is necessary to specify start addresses for both source file and object code (this is a default value only and can be changed at any time by an ORG directive in the source). It is also possible to specify the symbol table and workspace area. So, providing you have enough memory it should be possible to arrange things so that you can always keep the required area of memory free for the object code.

When you are testing the assembled program while the assembler is still in RAM it is often the case that the program will not do as intended and may corrupt part of the assembler. On return to COMPASS, however, a checksum routine is carried out so that if COMPASS has been corrupted a message is displayed. It is then necessary to reload COMPASS.

As I have already mentioned, COMPASS compresses the source code. It does this by using a 1-byte code for each keyword. Assembly speed is good and Level 9 claim 3000 lines per minute, but since I can't count that fast, I'll just take their word for it! During assembly, any errors are displayed on the listing and assembly is resumed (unlike ZAP80 and RAVEN which abort assembly at the first error).

Lines are entered with line numbers and editing is carried out using the NAS-SYS screen-editing. The line-numbering system adopted by Level 9 is a little strange to say the least. Firstly, numbers are in HEX and they appear on the far left of the assembly listing next to the address field. Two columns of Hex numbers make reading a little confusing. Secondly, it is not possible to enter lines in increments other than one but it is possible to insert lines using the Insert command, which also automatically numbers the inserted lines and renbers all succeeding lines.

Commands are entered using single characters and include listing (number of lines listed at a time may be set), string search, assemble (with listing options), and tape read, write and verify which merely use the NAS-SYS routines and therefore do not allow file names. NAS-SYS restart instructions and subroutine calls are not supported. For listing to a printer the NAS-SYS 'X' or 'U' commands have to be used.

The manual is mainly just a guide to the facilities available and does not attempt to teach the use of an assembler. This is also true of the other two manuals. However, there is some useful information given which should allow you to add extra commands to COMPASS.

ZAP

I ordered my copy of ZAP from an old copy of 80-BUS NEWS where the price quoted was #15. When it arrived, I was pleasantly surprised to find a refund of #8.50 since the price had since been reduced to #6.50. It comes on a good quality BASF tape and the manual is just 11 A4 pages stapled together. ZAP takes up about 7K and also compresses the source code, so a minimum of 16K RAM is sufficient. On cold starting you are greeted with a cheery message and a report on the amount of free memory.

The editing facilities are the worst feature of this assembler and are similar to what you find on most BASICs of a few years vintage. In particular there is no pause control on listing to the screen, so you can only list in blocks of 14 lines. Also, there is no string search/change command which, together with the poor listing facilities, makes editing rather tedious. Once you have succeeded in getting the required line on the screen it can be edited using the NAS-SYS screen-editing commands. All commands must be entered in full (usually four letters). This is far less convenient than single key entry. Filenames are not used when saving the source on tape.

There are a couple of useful commands however, such as OBEY "c" where c can be any NAS-SYS command, so you can use NAS-SYS commands without having to leave the assembler. DUMP will write the assembled object code to tape and if you put a RET instruction at the end of the program, RUN will execute the program and return control to ZAP when it is finished.

ZAP also compresses the source by using 1-byte codes for mnemonics, labels and macros, and by removing all unnecessary spaces. There is no space between the line number and the label and only one space is left between label and mnemonic, and mnemonic and comment. This makes it impossible to lay out the source in neat columns for readability, since the resultant listing is always a scruffy mess!

Conditional assembly is possible by enclosing the source within an IF (expression) . . . FI statement. If the expression is evaluated to true (non-zero) then the code within the block is assembled, otherwise it is ignored and assembly continues from the line following the FI. Macros are also supported - a macro is just a group of instructions which is given a suitable name. When the macro name appears in the source, the assembler inserts the machine code which makes up the macro. A macro would normally be used where a small group of instructions are used many times, but where a subroutine would not be appropriate. For example a macro to push registers at the start of each subroutine. Parameters may be passed to the macro so that the same macro may operate on different data, which may be registers, labels, expressions, etc. In the ZAP assembly listing the macro appears in full with all the mnemonics, so that it is still readable by anyone not familiar with macros. Labels and macros may be listed at any time but if you have deleted any from the source during programming, they still appear in the tables and it is impossible to get rid of them! Assembly is aborted at the first error, however the error messages given are reasonably full and clear.

Multistatement lines may be used with mnemonics separated by colons. The NAS-SYS restart mnemonics are supported (although BRKPT and RDEL are incorrectly represented by BREAK and KDEL respectively). In addition, "for convenience" as the manual puts it, there are alternative mnemonics for some of the more common instructions. e.g. CLA (clear acc) for XOR A, JSR (jump to subroutine) for CALL. I really don't see the point of including alternatives since their use will only lead to lack of standardisation and the Zilog mnemonics are quite logical and clear enough. Another unusual, but more interesting feature is the inclusion of the Z80 unknown opcodes, or most of them anyway.

RAVEN

At #30, RAVEN is the most expensive of the three and about the same price as ZEAP. However, when you see the sort of things it can do, I'm sure that you will agree that it is an excellent piece of software and quite reasonably priced at that. The TDK tape has the assembler itself on one side and the Z80 macro library on the other (see later for an explanation of the macro library). The manual is a loose-leaf ring-binder and has been printed directly by a dot-matrix printer. Unfortunately, the paper used is a bit thin and the holes are punched close to the edge so after a few hours' use the holes tear and you literally have a loose-leaf manual! Raven takes up about 16K of memory and it does not perform any text compression (apart from the use of TABs to save spaces), so 32K RAM is needed even for a fairly small program.

It is possible to interface RAVEN to your own system, in particular a printer routine, optional form feeds, cursor character code and repeat speed, and finally the start address for the source code. The adapted version may then be saved on tape and used as the working copy. The many facilities available on this assembler are well explained with clear examples. The only ambiguity I came across was the procedure for attaching your own printer routine. It didn't work for me so I had to devise my own method.

The editor is one of the best features of RAVEN. It is a full screen editor and I have found it to be far superior to the usual line-based editor that is supplied. The screen is best thought of as a 48 character by 15 line window on the text file, which is moved around the file by the cursor keys. Because any part of the file may be viewed at any time just by using the cursor keys, line numbers are not necessary. Lines can be up to 255 characters long but this is probably too long for most uses. An option of 80 (or 132) characters would be useful since this would be the same as most printers.

There is a comprehensive range of commands, selected by a single letter:- change and insert text, find and replace strings, delete and copy lines or blocks of text. As well as read, write and verifying of named files, it is possible to join a file from tape to one already in memory so that you can build up the source program from library files (for example). Tabs may be positioned anywhere along the 255 character line width and you may have as many as you wish. If you forget the function of a particular key, the Help command will give a short description. However it is usually necessary to consult the manual anyway since most commands have several options which require further input. It was while I was playing around with the help facility that I discovered a command that was not mentioned in the manual, this was to "update tabs". I've been trying it out but it doesn't seem to do anything useful!

The top line (line 16) is used to display information to the user. Cursor position is given by column number (1 to 255) but I would also like to have seen line position so that you would have a better idea of what part of the file you are at. This display may be changed to give the length of the file in bytes. Other information is displayed during tape transfers, and string search/replace etc.

Whereas assemblers for micros usually have one fixed instruction set, RAVEN does not have any instruction set built in at all. Before it can be used as an assembler it has to be supplied with the instruction set for the particular CPU. All instructions and most pseudo-ops are defined as macros. The list of macros defining the instruction set is treated just like any other source file, however for normal use it can be incorporated into the assembler semi-permanently so that it is loaded into RAM along with the assembler. The instruction set supplied is, of course, for the Z80 but you could just as easily replace it with say a 6502 set (although you would have to write this

yourself). RAVEN then becomes a cross-assembler for the 6502. Other uses for this powerful macro facility would be to add extra instructions to the Z80, e.g. LD (HL),HL etc. Or if you are involved in a particular field of interest such as robotics, music or graphics, then you could write your own personal programming language where each program instruction would in fact be a macro name. RAVEN then effectively becomes a compiler for your new language.

One notable omission from the Z80 library was the NAS-SYS restart instructions. They can be easily added by writing a few extra macros using the methods described very clearly in the manual. The comma in the jump conditional instructions was missing, and as I was used to putting it in, I altered the macros accordingly. Another much more serious error was only discovered after spending many hours puzzling over apparently perfect programs which would just keep on crashing. The problem was that the assembler was allowing relative jumps up to +/- 255 instead of +129 to -126. So, for instance, if the jump was between +130 and +255 the error would not be detected and the byte put into the object code would in fact work out to be a negative jump and the program would therefore crash. I managed to overcome this problem by rewriting the macro definition dealing with relative jumps. Being rather surprised that this problem had not been spotted sooner, I wrote to the author, so he is now aware of it.

At first you might think that there has to be a macro definition for every possible permutation of instruction (about 750 I think), but RAVEN provides facilities for much more concise definitions so that each instruction "type" (e.g. 8-bit register load) may be defined by one macro. The parameters supplied to the macro determine the machine code generated. It is necessary to read the manual to fully understand the method, but I will give an example which may give some idea. e.g. the macro to define 8-bit register to register loads:

```
DEFMAC ("LD*,*",R8,R8)
        DB 40H + #0*8 + #1
END.
```

"LD , " is the name of the macro. The asterixes show the position that the parameters must be in the macro name (separated by a comma in this case). R8 stipulates that the parameters must belong to a previously defined set, R8, which is a set of the 8-bit register names A to L and A is assigned a value of 7, B a value of 0 etc. DB is the define byte pseudo-op. #0 and #1 refer to the first and second parameters respectively. The expression thus generates the required bit pattern according to the registers specified (see the Z80 Technical Manual for a breakdown of the bit patterns). e.g. for LD A,B the expression is evaluated like so:

40H:	01000000
(Reg A = 7) #0*8:	00111000
(Reg B = 0) #1 :	00000000

Total	01111000 = 78H

There are also several PASCAL-like control structures for the generation of loops within macros. These may be used for the production of data tables. e.g. a macro to clear a block of memory of any size:

```

DEFMAC ("CLEAR * BYTES", NUM) ;NUM is a set of integers
                                ; 0 - 65535
    COUNTER = #0 ;Loop counter set to
                  ;number of bytes
    $WHILE COUNTER > 0
    DB 0
    COUNTER = COUNTER - 1
    END
END.

```

So the macro call CLEAR 24 BYTES will set the next 24 bytes to zero. Notice that there may be spaces within the macro name thus increasing legibility. Other constructs are IF THEN .. ELSE , and \$REPEAT .. UNTIL. Expression handling is comprehensive with 18 operators and 4 number bases to work with.

As well as the usual assembler options you can also specify user defined options for such things as conditional assembly. i.e. the value of the boolean argument in an IF .. THEN block may be set by an option at assembly time. Assembly stops at the first error with a single word error message which is not always very helpful. RAVEN is rather slower than the other two assemblers but this is to be expected due to its design. I had the idea to add all the EQUATES that I use (e.g. port addresses, video ram addresses, keyboard control codes) to the permanent macro library to save having to put any in at the beginning of a program. While this worked perfectly OK, the macro library was by now so large that assembly time was painfully long. But I am using a 2 MHz NASCOM 1 so the speed is obviously half of what it could be.

Conclusion

So there we are, three more assemblers for the NASCOM. It would be a little unfair to say which is best since prices vary. Each has its good points - COMPASS is very fast, ZAP offers a macro/conditional assembler at a very low price, and RAVEN can be used very much depending on your own imagination.

The three assemblers are supplied by:

COMPASS v1.3	Level 9 Computing 229 Hughendon Rd High Wycombe Bucks. HP13 5PG	\$12.00
ZAP v1.6	Syrtis Software 23 Quantock Rd Bridgewater Somerset TA6 7EG	\$6.50
RAVEN	P. Harvey 30 Jericho St Oxford OX2 6BU	\$30.00

RANDOM RUMOURS (& TRUTHS?)**By S. Monger**

Ah well, I've never claimed to be perfect, have I? In the last issue I mentioned the Gemini GM886 board containing an iAPX186. Well it seems that this idea was dropped a while ago, and in fact it is the GM888 that you will see in coming months. What is the difference - well the GM886 board was to be a board interfaced to the 80-BUS via a couple of I/O ports (like the GM812 IVC, GM832 SVC and I0828 Pluto). It contained its own 256K RAM and consequently would have been very fast, and expensive! It would also have had the disadvantage of not being able to drive any I/O peripherals directly, such as Pluto, and would have to have passed on the task via the Z80 on the host processor board, consequently slowing down the operation quite considerably. After a little research Gemini discovered that they could produce a board that would drive the 80-BUS directly, and thus be able to use all the I/O on the bus. More importantly, from a cost viewpoint, the board would be able to have NO memory on it, and would therefore almost certainly be under #200. Of course in interfacing to the 80-BUS the processor used has had to be changed to one with an external 8-bit interface, and so the 8MHz 8088 has been chosen.

So how do you use it? Well the system boots as normal under the Z80, you then flip a bit of an 80-BUS I/O port, the 8088 puts out a bus request, the Z80 finishes the instruction that it was executing, and then the 8088 takes over control of the entire system. If at any stage you wish to pass control back to the Z80 then the 8088 flips the I/O bit back, and the Z80 carries on where it left off. In theory, therefore, you can write code that is of mixed type, and switch to and fro between processors - must be of some use!

The GM888 board will also contain another of those blasted Real Time Clock thingies, with battery back-up. This serves two purposes. First of all certain 16 bit operating systems allow time and date stamping of files, and secondly the actual chip chosen provides oodles of interrupts if required, and this is necessary for task switching with Concurrent-DOS (previously called Concurrent-CP/M) and presumably with Multi-tasking MS-DOS. There is also a socket for the 8087 high speed arithmetic co-processor. Board availability? Don't know!!

And how about the extra memory that the 8088 can support? Well Gemini have launched a 256K RAM board that supports the 80-BUS extended address lines. It has several modes of operation:

- 1) 4 pages of 64K from address 0 (for use with Nascom 2s and Gemini GM811s).
- 2) 4 pages of 64K from any extended address (EA).
- 3) 1 page (any) of 256K from any extended address.
- 4) 3 pages of 64K from 0 and 1 page of 64K from EA1 (allows GM813 users to have 4 pages of 64K at 0 without contention).

In addition to the above there is a 'Common Area' mode which puts a common area of memory (selectable between 4K and 8K) in all four pages.

With all these modes it is possible to set up virtually any permutation of page mode and extended addressing that any one may want, and systems with the GM813 or GM888 will accept up to 8 boards = 2Mbytes! (4 pages of 512K.) And the price of all this flexibility ? - #325 + VAT. Available now!!

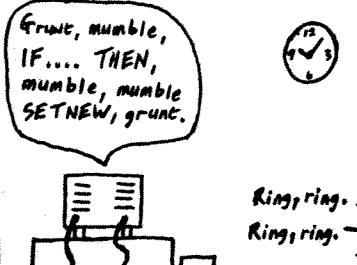
World Domination!

With a distinct lack of signs of activity from Lucas, Gemini continues its 80-BUS domination campaign! The Climax vector graphics colour board has been taken over by Gemini, as the GM837. It is now only available in its fully populated form (modulated and R-G-B outputs) and the price has been reduced to #165 + VAT. Gemini has also taken over the IO Research 8-bit A-D board, now the GM824, and the price remains the same at #125 + VAT. One 80-BUS price has risen, the Belectra arithmetic board. It is now a totally horrendous #268 + VAT!!

Lawrence lends a helping hand.



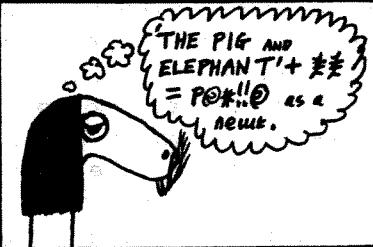
Lawrence's train of thought is derailed by the telephone.



In the interest of good taste, this section of profane verbiage has been censored by the editor.



Lawrence decides to lumber Heloise Fortran with the problem while he attends to more important things....



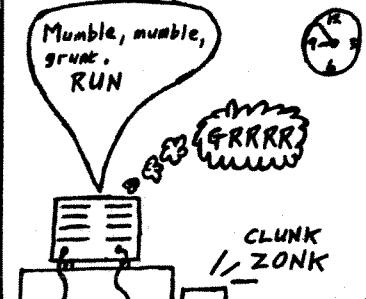
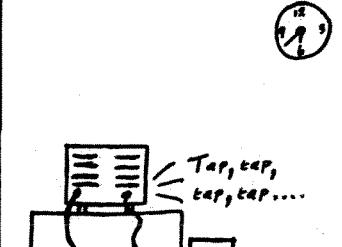
.... But.



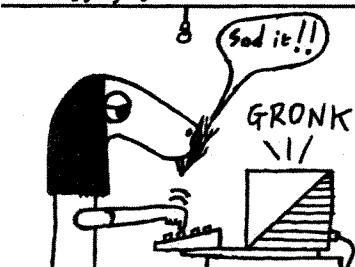
Like all good programmers, our hero thinks the problem through first....



.... then starts writing the program.



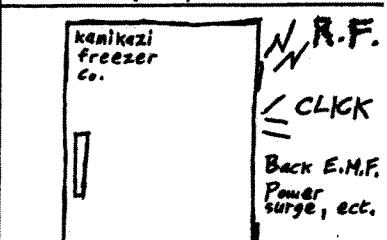
Part of the fun of computing is de-bugging your software....



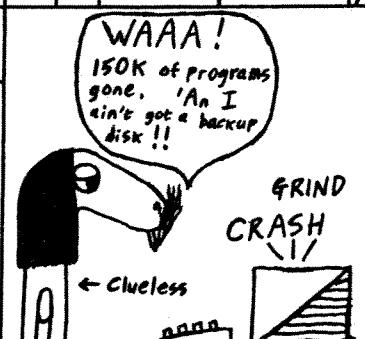
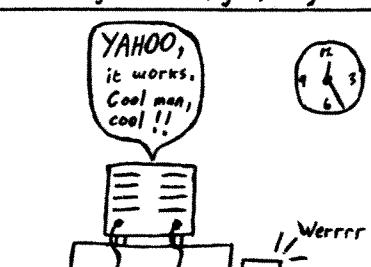
.... which in most cases is easier said than done!



Meanwhile in the kitchen, the refrigerator's thermostat decides it's time to go 'ape'.



Lawrence is unaware of the mains borne mayhem heading his way.



It's always a good idea to make backup copies of your disks in case you get caught out like Lawrence.

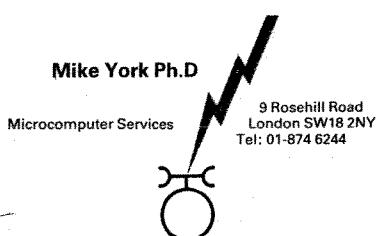
By D. G. Richards.
Tonyrefail. Mid. Glamorgan.
1983.

ADS

Ram-A board, 32k RAM, 2716 conversion (no EPROMs). With INMC mods, but still won't work at 4MHz without waits (dunno why...) #45 ono. HS1N twin-drive digital cassette system. As reviewed 80-Bus News Vol 1 iss. 4. With original ROMs (2708s, assembled for #D000 and ports #F8 to #FF) or my enhanced OS (2716 - assembled for #D000, and ports #78 to #7F); 4118s, connecting leads and some tapes. #170 ono. Adrian Perkins. Tel. Bracknell (0344) 485816

Nascom 1, port select corrected, card frame, buffer PCB, 32K RAM fully debugged, PSU, 4800 baud cassette interface, TTY interface, keyboard, Harris terminal with 12" Motorola VDU, TTY printer, BASIC & assembler on tape. All documentation. #140 the lot or prepared to split. Jim Taylor Boldon 36215.

Floppy disk system. GM815/2, GM809 etc. Twin Pertec drives (350K per drive) in cabinet with power supply. Double density controller card, CP/MZ 2.2 operating system, and lots of software including Pascal, BASIC and C languages, Utilities, Adventure, Games and Business software. Totally reliable system. #450. Also, twin single sided drives in cabinet with power supply and Henelec Singler Density Controller. Including all above software and Vers. 2.2 operating system. Haggis around #275. Call John on 01-380-0191 between 10am and 7.30pm.

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This gives a variable disc format capability to any 80-Bus CP/M computer. With this you can define your own formats, set up a library of standard formats and interchange files between nearly all CP/M computers. Any mix of 5", 8" or 3" allowed. You can also read/write 48 tpi discs on 96 tpi drives.

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UCSD provides the best way to protect your software investment when upgrading your CPU. Now that 16-bit cards are coming what are you going to do with that Z80 CP/M stuff? A UCSD version of AllDisc is supplied free. The RAMDisc option and high-capacity drives make this one of the most powerful UCSD computers around.

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The HSA-88B is easily used from within assembly language programs. High level language programs require a compiler with modified run-time

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routines. We are offering with every HSA-88B a FREE latest Hisoft HP5 Pascal compiler which has been specially adapted to compile HSA-88B-oriented code. This compiler is already extremely fast and with the HSA-88B it outperforms all other Z80 Pascal compilers, in many cases by an order of magnitude. The standard Pascal variable types plus 32 bit integers (ideal for financial applications) are supported together with a full range of maths functions rarely seen in Pascals or Basics. The size of the run-time routines is greatly reduced over other compilers because the HSA-88B performs the arithmetic functions in hardware.

The complete package consists of the HSA-88B processor card, HP5 compiler on Gemini 5½" DSDD disc (other formats available including Nascom 5½" and IBM 8" SSSD) and HSA-88B and HP5 documentation and programming examples. Package price £268 plus VAT, UK postage free. Not suitable for Nascom 1.

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NEW SUPER DISKPEN (PENVG:3)

DISKPEN has been rewritten and revised. This popular text editor/formatter now includes a 'HELP' facility, and new features for the print control of the most popular printers, underline, bold, etc (also user patchable for the less popular types). New features include block delete, better move commands, new cursor control, optional hyphenation, visible indentation setting and lots more. A major enhancement is the ability to handle overlay files so that PEN can use auxiliary packages such as the MAXIFILE free field file searching utility or the print spooling utility.

The new DISKPEN is useable on all Gemini multiboard computers (Galaxy, Kenilworth, Quantum) and Nascom/Gemini hybrids, (MAPPEN is available for

users of the MAP video card). DISKPEN is available as an upgrade to earlier DISKPENs and GEMPPENs at 17.25 inc. VAT, or to new purchasers at 57.50 inc. VAT. (Please state disk format when ordering.)

MAXIFILE overlay 23.00 inc. VAT (20.00 + VAT)

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ALLDISC is a new approach to the problem of lots of different machines all with different disk formats. Designed for use with the Gemini and Nascom CP/M computers, ALLDISC replaces the existing disk drivers and the CP/M disk parameter headers from an archive of different formats. Up to 6 drives and two controller cards are supported, the drives may be a mixture of 8", 5.25 or 3.5" types in either single or double density and may be single or double sided. 96 tpi 5.25" drives can be made to 'double step' to read and write 48 tpi disks. The Gemini GM833 512K virtual disk RAM is also supported. All this adds up to a very powerful system where the drives fitted to the system may be reconfigured to read and write disks of different formats. The limitations are that the disks must be to IBM3740 CRC standards and must be soft sectored (or in other words, it will cope with most disks).

The archive is supplied with nine useful formats when supplied, with others being made available to registered users free of charge for the first six months from registration. The archive can be edited and new formats created. Disks can be formatted from the archive so there is no need for preformatted disks when transferring software.

ALLDISC is supplied with full documentation and hints and pointers to discovering unknown disk formats. ALLDISC costs 172.50 inc VAT (150.00 + VAT).

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BDOSZ

Yes, you guessed it. Some enterprising person has now 'disconboobered' the BDOS in CP/M and rewritten it as a Z80 program. Its fully compatible with the original with no bugs found to date. Because it's written in Z80 code it's smaller, this has allowed room for tidying up all the annoying stupids in the original BDOS so that errors like:

BDOS ERROR ON x: R/O

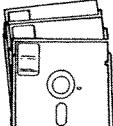
which usually causes you to lose everything you've just done, becomes the far more helpful:

Disk x: is set R/O

Do it anyway? (Y/N/^C)

Which, of course, means you don't lose anything. It even allows you to change disks when they are full without loss of data. In all, a lovely piece of software. Available in most popular 5.25" formats (please state when ordering) at 11.50 inc. VAT.

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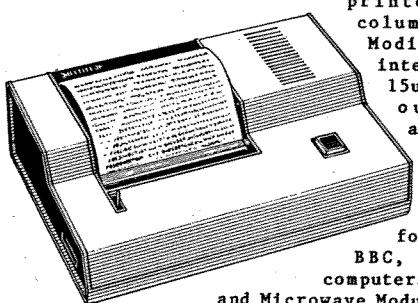
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