

11.2.1 PIO Controller A Multiplexing

Table 11-2. Multiplexing on PIO Controller A (PIOA)

I/O Line	Peripheral A	Peripheral B	Peripheral C	Extra Function	System Function	Comments
PA0	PWMH0	TIOA0	A17	WKUP0		
PA1	PWMH1	TIOB0	A18	WKUP1		
PA2	PWMH2	SCK0	DATRG	WKUP2		
PA3	TWD0	NPCS3				
PA4	TWCK0	TCLK0		WKUP3		
PA5	RXD0	NPCS3		WKUP4		
PA6	TXD0	PCK0				
PA7	RTS0	PWMH3			XIN32	
PA8	CTS0	ADTRG		WKUP5	XOUT32	
PA9	URXD0	NPCS1	PWMFIO	WKUP6		
PA10	UTXD0	NPCS2				
PA11	NPCS0	PWMH0		WKUP7		
PA12	MISO	PWMH1				
PA13	MOSI	PWMH2				
PA14	SPCK	PWMH3		WKUP8		
PA15	TF	TIOA1	PWML3	WKUP14/PIODCEN1		
PA16	TK	TIOB1	PWML2	WKUP15/PIODCEN2		
PA17	TD	PCK1	PWMH3	AD0		
PA18	RD	PCK2	A14	AD1		
PA19	RK	PWML0	A15	AD2/WKUP9		
PA20	RF	PWML1	A16	AD3/WKUP10		
PA21	RXD1	PCK1		AD8		64/100 pins versions
PA22	TXD1	NPCS3	NCS2	AD9		64/100 pins versions
PA23	SCK1	PWMH0	A19	PIODCCLK		64/100 pins versions
PA24	RTS1	PWMH1	A20	PIODC0		64/100 pins versions
PA25	CTS1	PWMH2	A23	PIODC1		64/100 pins versions
PA26	DCD1	TIOA2	MCDA2	PIODC2		64/100 pins versions
PA27	DTR1	TIOB2	MCDA3	PIODC3		64/100 pins versions
PA28	DSR1	TCLK1	MCCDA	PIODC4		64/100 pins versions
PA29	RI1	TCLK2	MCCK	PIODC5		64/100 pins versions
PA30	PWML2	NPCS2	MCDA0	WKUP11/PIODC6		64/100 pins versions
PA31	NPCS1	PCK2	MCDA1	PIODC7		64/100 pins versions

11.2.2 PIO Controller B Multiplexing

Table 11-3. Multiplexing on PIO Controller B (PIOB)

I/O Line	Peripheral A	Peripheral B	Peripheral C	Extra Function	System Function	Comments
PB0	PWMH0			AD4/RTCOUT0		
PB1	PWMH1			AD5/RTCOUT1		
PB2	URXD1	NPCS2		AD6/WKUP12		
PB3	UTXD1	PCK2		AD7		
PB4	TWD1	PWMH2			TDI	
PB5	TWCK1	PWML0		WKUP13	TDO/TRACESWO	
PB6					TMS/SWDIO	
PB7					TCK/SWCLK	
PB8					XOUT	
PB9					XIN	
PB10					DDM	
PB11					DDP	
PB12	PWML1				ERASE	
PB13	PWML2	PCK0		DAC0		64/100 pins versions
PB14	NPCS1	PWMH3		DAC1		64/100 pins versions