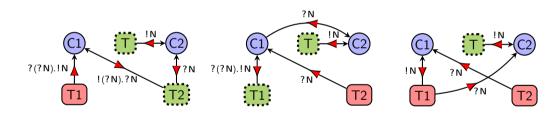
Connectivity Graphs: A Method for Proving Deadlock Freedom Based on Separation Logic

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POPL'22/plunch

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Session types

Message passing concurrency with first-class channels (Honda [1993])

c:!Nat.?Bool.!(?String.!Nat.End).End

Session types

Message passing concurrency with first-class channels (Honda [1993])

GV: functional programming with session types

(Gay and Vasconcelos [2010], Wadler [2012])

fork:
$$(s \xrightarrow{lin} 1) \rightarrow \overline{s}$$
 send: $(!t.s) \times t \xrightarrow{lin} s$
close: End $\xrightarrow{lin} 1$ receive: $?t.s \xrightarrow{lin} s \times t$

let
$$c = \text{fork}(\lambda c'. ... \text{receive}(c')...)$$
 in $\text{send}(c, 23)...$

What makes session types interesting

Linear session types: cannot copy or delete a channel reference before you are done

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▶ Required for **type safety** (mechanized by Castro-Perez et al. [2020], Ciccone and Padovani [2020], Goto et al. [2016], Hinrichsen et al. [2021], Rouvoet et al. [2020], Thiemann [2019], ...)

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- But also guarantees deadlock freedom, global progress
 (well-known property Caires & Pfenning, Wadler, Carbone but not yet
 mechanized for first-class channels, i.e. dynamically allocated and higher order)

Why session types give deadlock freedom

Two owners per channel

- Duality of channel types → no simple deadlocks
- ► Linear typing maintains acyclicity of ownership structure → no cyclic deadlocks

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- sent as messages over channels
- stored in data structures
- captured by closures
- in Turing-complete language (→ termination argument doesn't work)

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Difficult to reason about typing & graph structure simultaneously

This work: connectivity graphs

- ► Method for factoring out graph reasoning from reasoning about typing
- ► Mechanized in the Coq proof assistant
- ► Applied to prove deadlock freedom for feature-rich session-typed language
- ► Abstract representation of run-time configuration

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Run-time configuration ρ

```
Threads: \{T_1 \mapsto e_1, ..., T_6 \mapsto e_6\}
Channels: \{C_1 \mapsto \text{buf}_1, ..., C_5 \mapsto \text{buf}_5\}
```

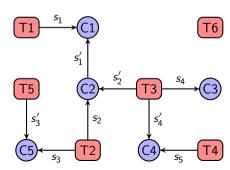
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Connectivity graph G



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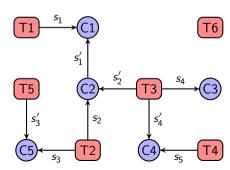
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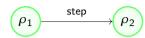
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$$G \vDash
ho$$
 $wf(
ho) := \exists G.G \vDash
ho$

Connectivity graph G



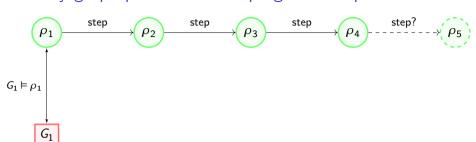


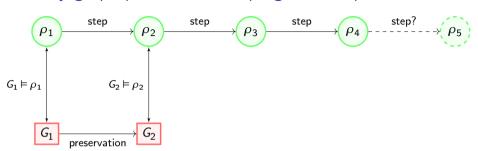


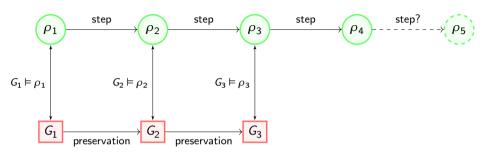


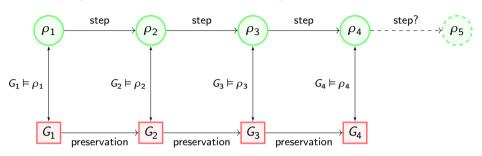


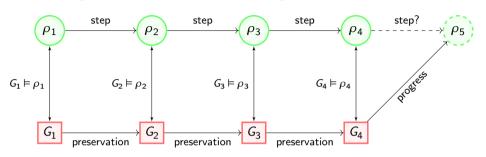


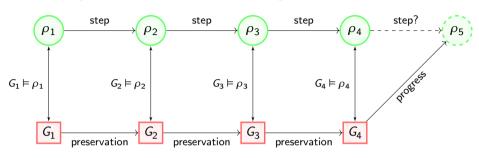












Connectivity graph framework:

- Cgraph(V, L) data type for acyclic labeled graphs
- Generic construction for $wf(\rho) := \overline{wf}(P_{\rho})$
 - ▶ Parameterized by local separation logic predicate $P_{\rho}(v)$ for each vertex $v \in G$
- ▶ Preservation: graph transformations in separation logic
- ▶ Progress: waiting induction principle for Cgraph(V, L)

All generic over vertices V and labels L

$$\frac{\Sigma_1 \vdash e_1 : \tau_1 \qquad \Sigma_2 \vdash e_2 : \tau_2 \qquad \Sigma_1 \cap \Sigma_2 = \emptyset}{\Sigma_1 \cup \Sigma_2 \vdash \left(e_1, e_2\right) : \tau_1 \times \tau_2}$$

$$\frac{\Sigma_{1} \vdash e_{1} : \tau_{1} \qquad \Sigma_{2} \vdash e_{2} : \tau_{2} \qquad \Sigma_{1} \cap \Sigma_{2} = \emptyset}{\Sigma_{1} \cup \Sigma_{2} \vdash (e_{1}, e_{2}) : \tau_{1} \times \tau_{2}} \qquad \Rightarrow \qquad \frac{e_{1} : \tau_{1} \quad * \quad e_{2} : \tau_{2}}{(e_{1}, e_{2}) : \tau_{1} \times \tau_{2}}$$

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For vertex v in the graph, separation logic resource $\Sigma = \text{OutEdges}(v)$

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For vertex v in the graph, separation logic resource $\Sigma = \text{OutEdges}(v)$

Lemmas in separation logic:

$$(\Sigma \vdash K[e] : B) \iff \exists A, \Sigma_1, \Sigma_2. \ (\Sigma_1 \cap \Sigma_2 = \emptyset) \land (\Sigma = \Sigma_1 \cup \Sigma_2) \land (\Sigma_1 \vdash e : A) \land$$
$$\forall e', \Sigma_3. \ (\Sigma_2 \cap \Sigma_3 = \emptyset) \land (\Sigma_2 \vdash e' : A) \rightarrow (\Sigma_2 \cup \Sigma_3 \vdash K[e'] : B)$$

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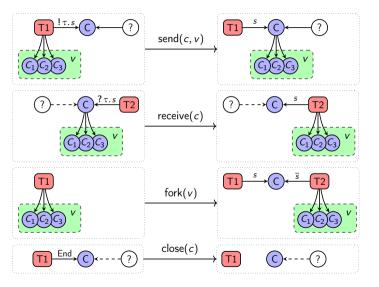
$$\forall e', \Sigma_{3}. \ (\Sigma_{2} \cap \Sigma_{3} = \emptyset) \land (\Sigma_{2} \vdash e' : A) \rightarrow (\Sigma_{2} \cup \Sigma_{3} \vdash K[e'] : B)$$

$$\Rightarrow$$

$$(K[e] : B) \dashv \vdash \exists A. \ (e : A) * \forall e'. \ (e' : A) \rightarrow (K[e'] : B)$$

We use the Iris proof mode to reason in separation logic (Krebbers et al. [2017])

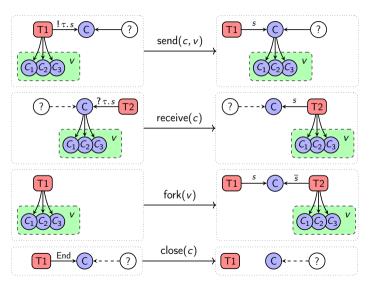
Preservation via local graph transformations



Preserves:

- Acyclicity
- Local predicates $P_{\rho}(v)$ used for $\overline{wf}(P_{\rho})$

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In separation logic: if

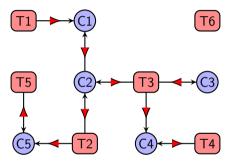
$$P_{\rho}(T_1) * (\operatorname{own}(C \mapsto \ell) \twoheadrightarrow P_{\rho}(C)) \\ \vdash \\ (\operatorname{own}(C \mapsto \ell') \twoheadrightarrow P_{\rho'}(T_1)) * P_{\rho'}(C)$$

then:
$$\overline{wf}(P_{\rho}) \rightarrow \overline{wf}(P_{\rho'})$$

Explained in our paper!

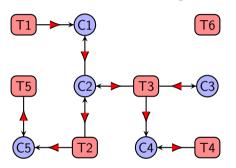
Progress via waiting induction

Connectivity graph with *waiting dependencies* (►) derived from run-time configuration



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Lemma (Waiting induction)

Let R(v, w) be any relation on the vertices. To prove P(v), we may assume P(w) for all w such that $v \to w$ and R(v, w), or $w \to v$ and $\neg R(w, v)$

Our language

Functional language + session-typed channels (extension of Wadler [2012]'s GV)

Unrestricted and linear types

- ightharpoonup Unrestricted: numbers, sums, products, unrestricted function type (\rightarrow)
- ▶ Linear: channels, sums, products, linear function type (→)

General recursive types:

- ▶ Recursive session types, including through the message (example: μX . !X.End)
- ► Algebraic data types using recursion + sums + products
- ► Recursive types mechanized using coinduction (Gay et al. [2020])

Stronger deadlock and leak freedom result

Global progress is the standard notion that people use Our POPL reviewers: Can your method prove something stronger?

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Partial deadlock: a set *S* of threads and channels such that:

- 1. All threads in S are blocked on a channel in S
- 2. No references to channels in S from outside S

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Strong reachability:

- 1. A channel is reachable if it is referenced by a reachable channel or thread
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Lemma. All threads and channels are reachable \iff no partial deadlock

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Lemma. All threads and channels are reachable \iff no partial deadlock **Lemma.** Any thread or channel is reachable \implies global progress

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Lemma. All threads and channels are reachable ⇔ no partial deadlock
 Lemma. Any thread or channel is reachable ⇒ global progress
 Theorem. For well-typed initial programs, no partial deadlock occurs

Mechanization

Mechanization in Coq:

- ► Generic *Cgraph*(*V*, *L*) library: 4999 LOC
- ► GV language definition: 451 LOC
- Language specific deadlock and leak freedom proof: 1688 LOC

https://github.com/julesjacobs/cgraphs



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Initial direct attempt: proofs goals got too complex.

Graph reasoning intertwined with language specifics.

Encapsulating the graph reasoning made it manageable.



μ GV

- Linear lambda calculus + fork with single-shot atomic exchange
- ► Global progress & deadlock freedom in Coq (1478 LOC)

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Multiparty session types

► Multiparty session types > binary session types?

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 - 1. Single-session deadlock freedom
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► MPGV:

- 1. Deadlock freedom for all well-typed programs
- 2. MPGV multiparty session types > binary session types
- 3. Global progress & deadlock freedom in Coq (10400 LOC)

Questions?

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