

# ECE3301L SESSION 2

## LABS 7 & 8

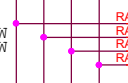
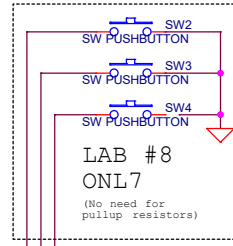
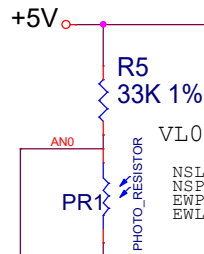
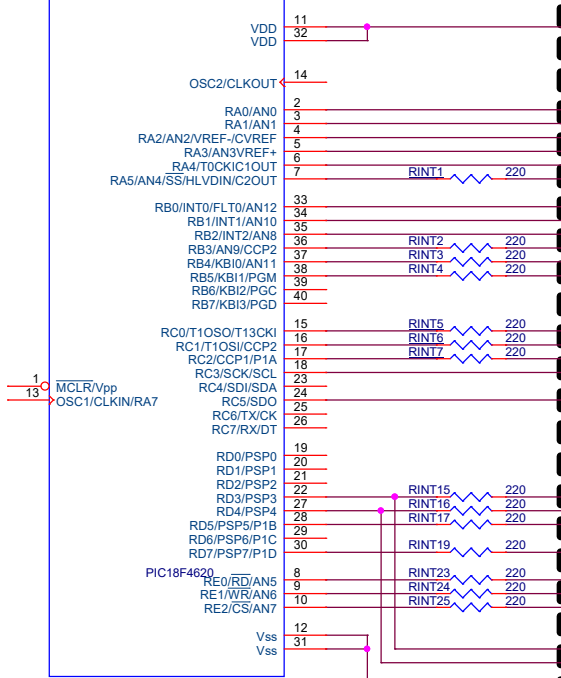
### DEVELOPMENT BOARD

+5V R1 33 VREF = 4.096V

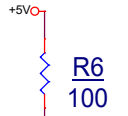
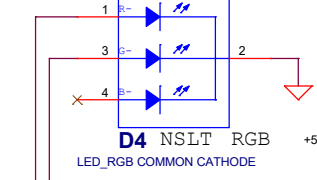
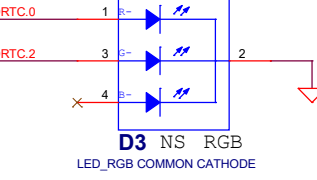
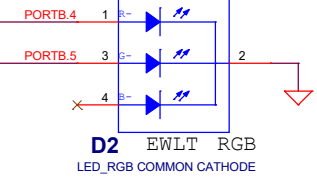
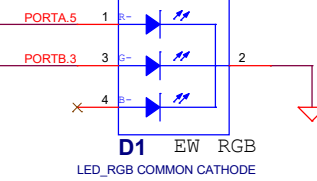
Z1  
LM4040

JP3  
HEADER 3

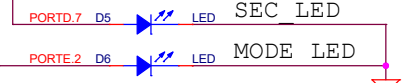
U2



LA.CH4 INT0  
LA.CH5 INT1  
LA.CH6 INT2



Q1  
2N3904  
or 2N2222



LA.CH0 FOR SCK  
LA.CH1 FOR SDO  
LA.CH2 FOR TFT\_D/C  
LA.CH3 FOR TFT\_CS#

