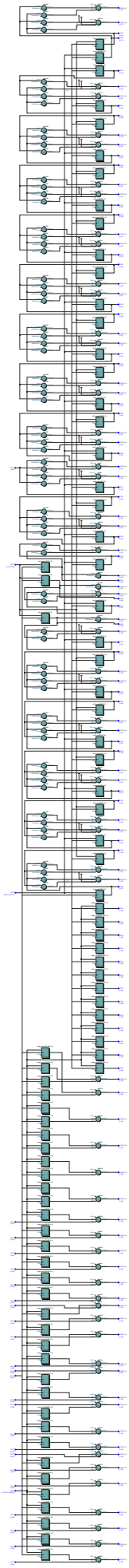

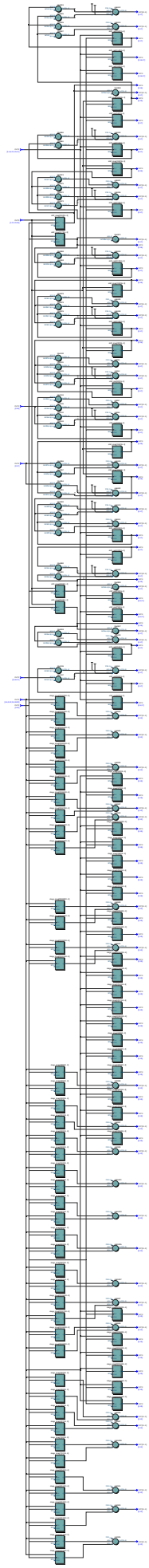

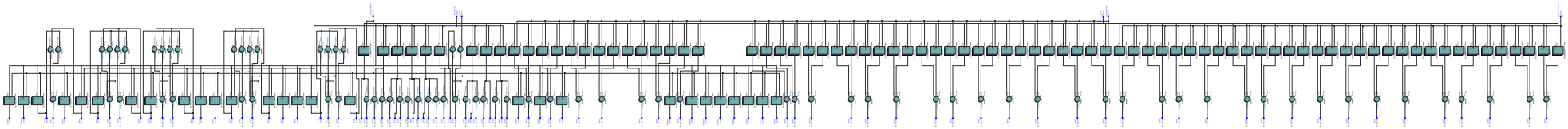

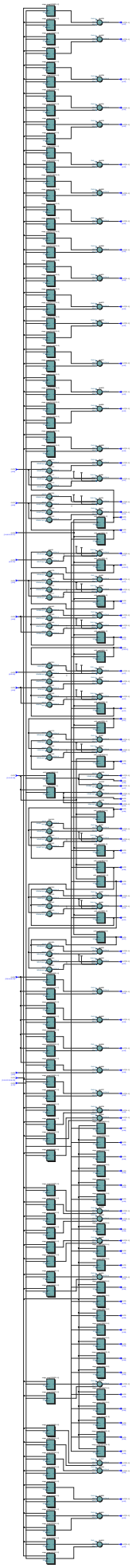

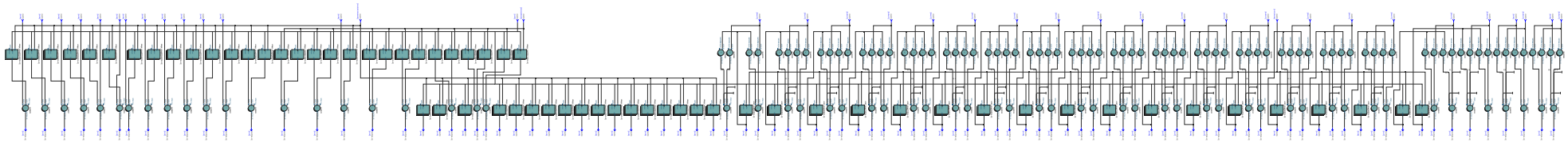

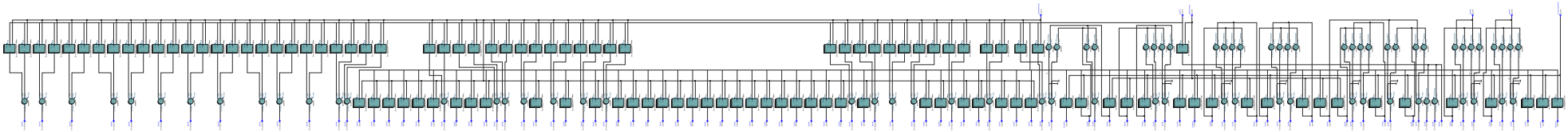

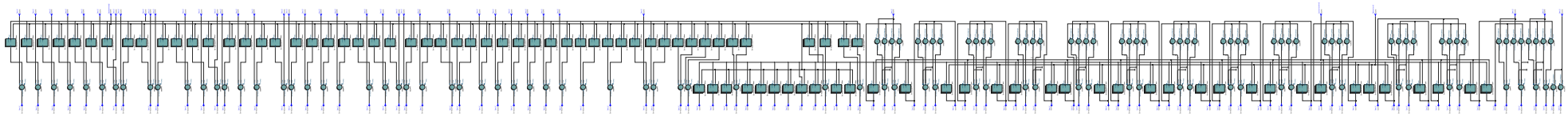

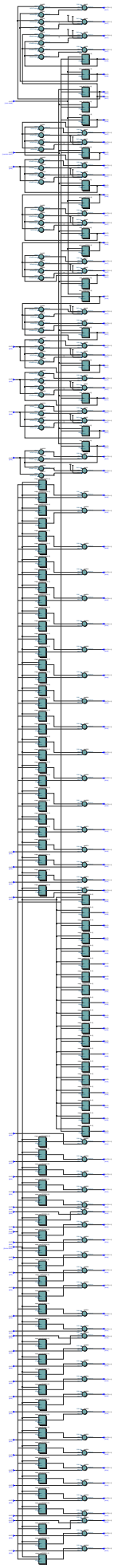

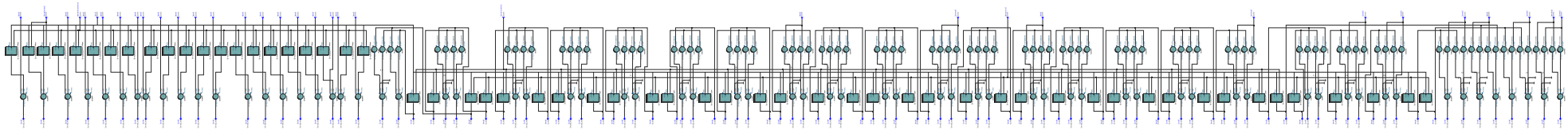

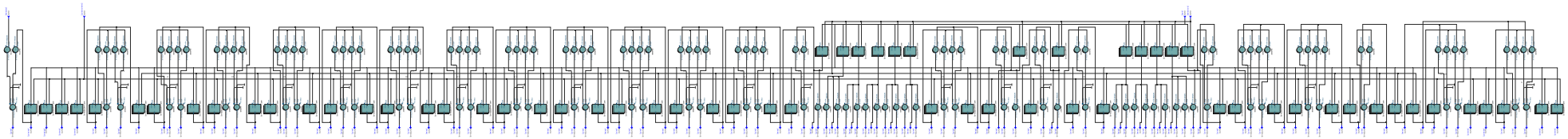

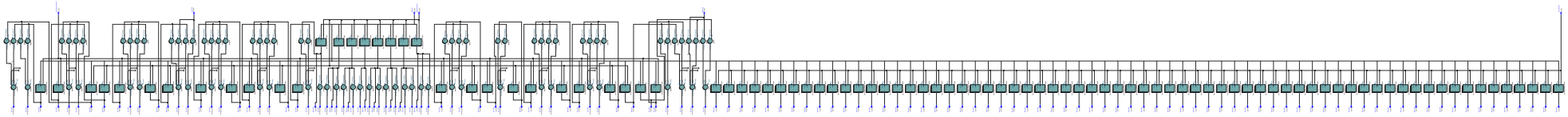

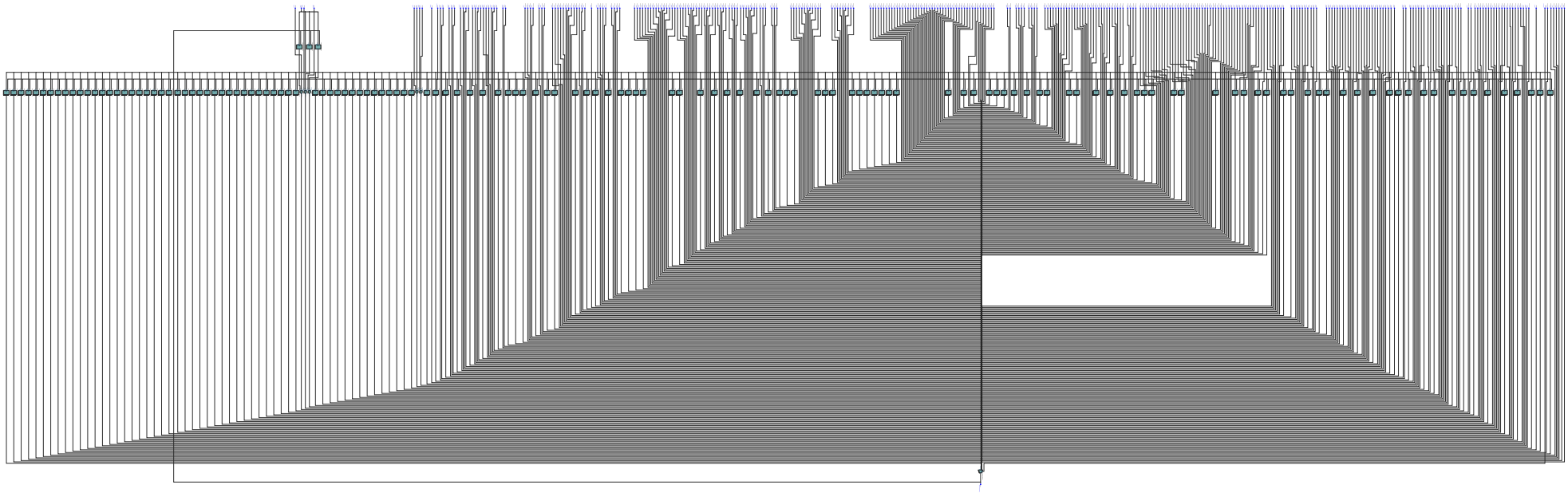

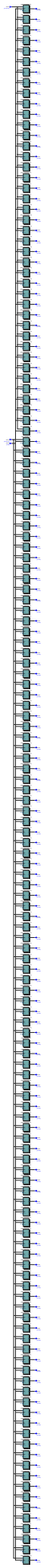

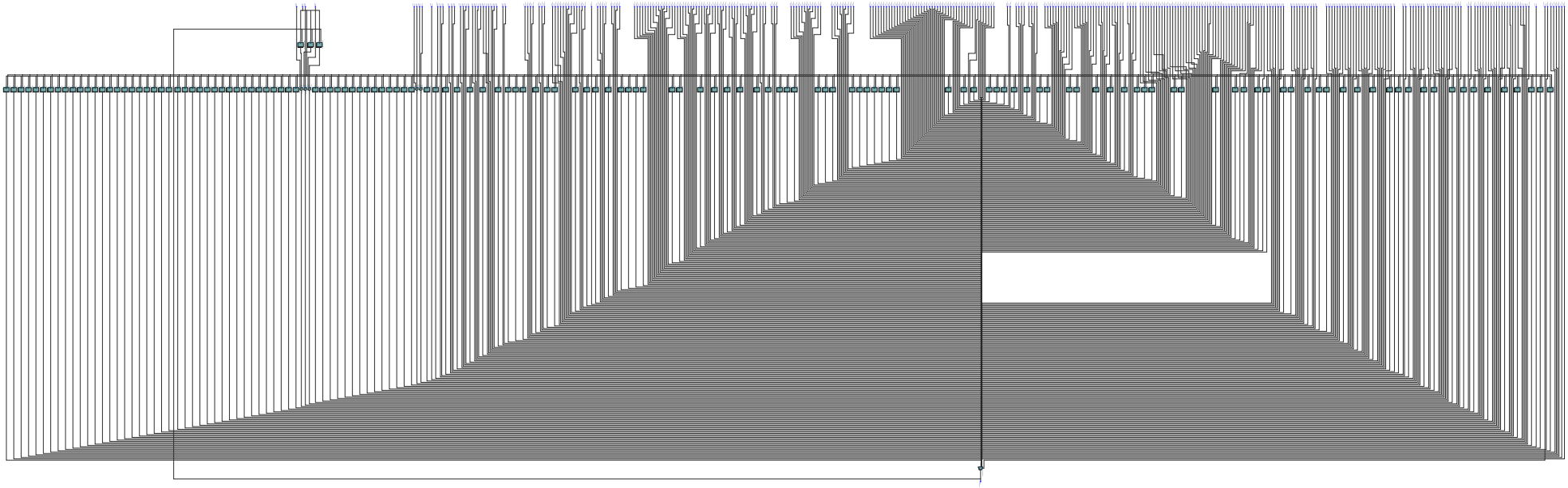

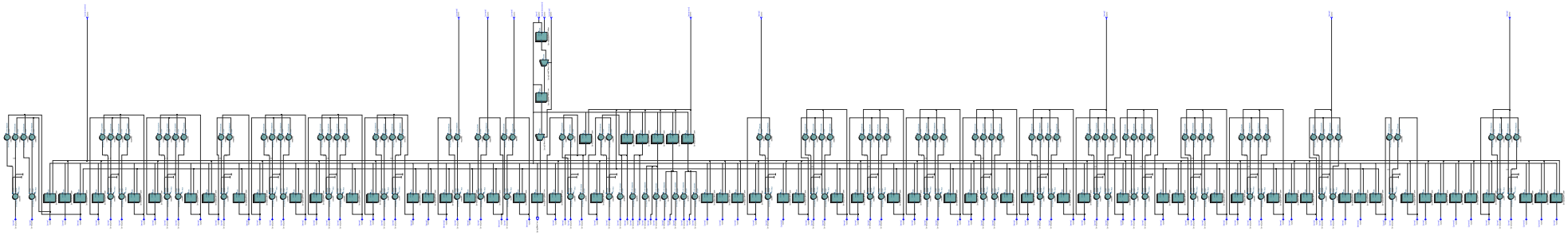

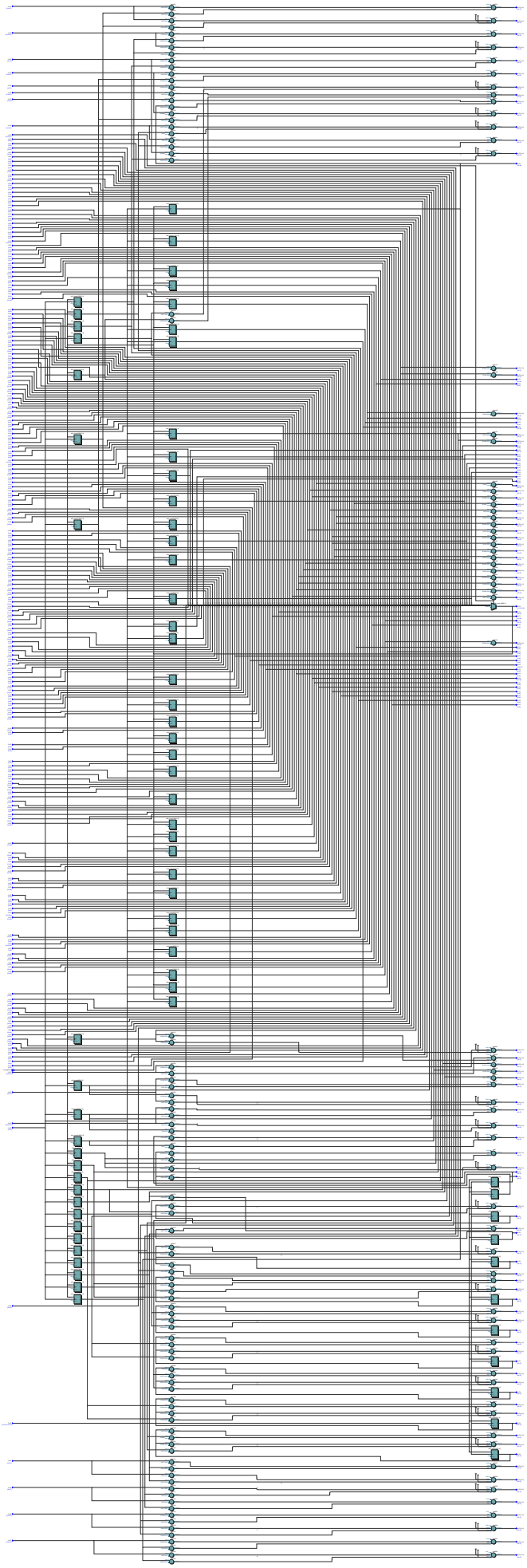

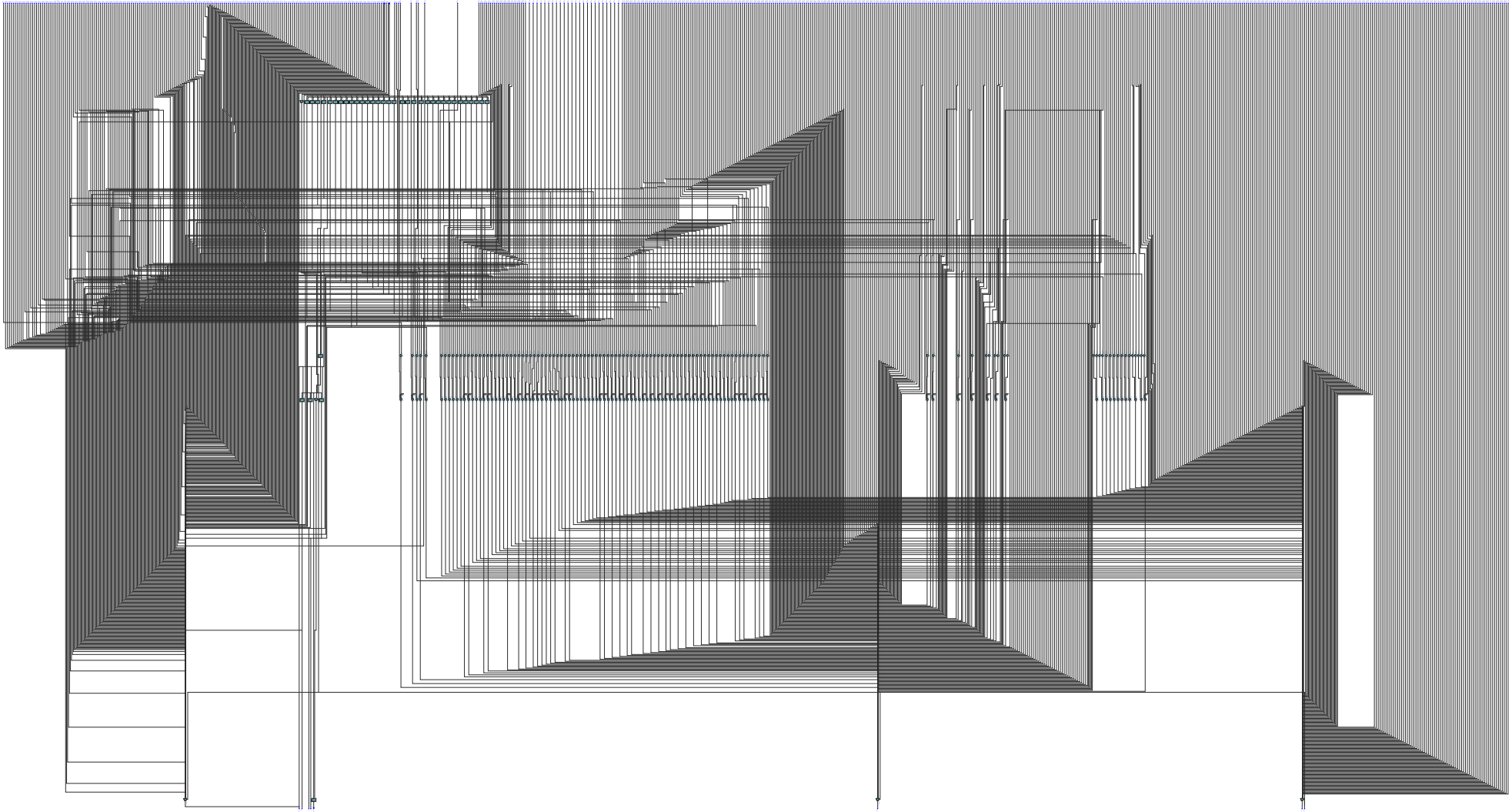

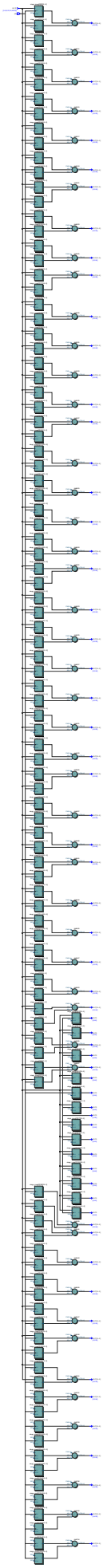

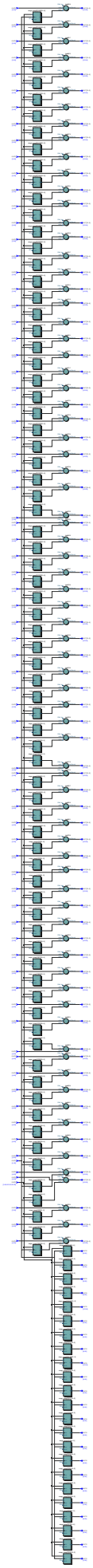

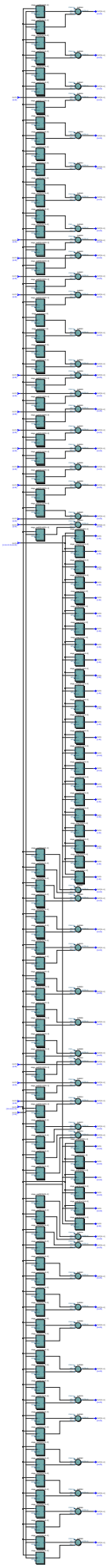

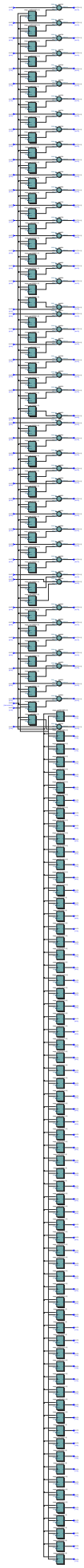

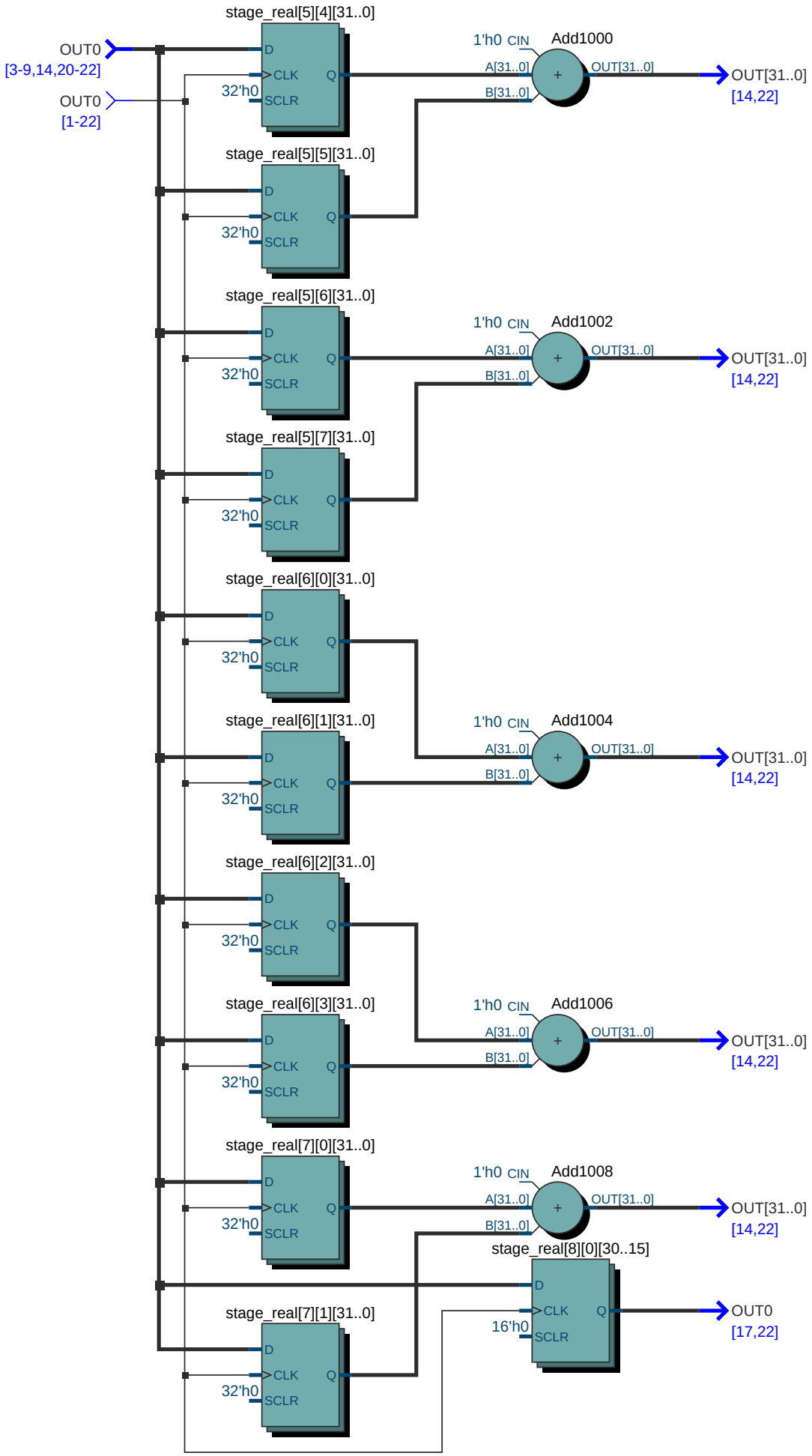
OUT0
[3-9,14,20-22]
OUT0
[1-22]
stage_real[5][4][31..0]
stage_real[5][5][31..0]
stage_real[5][6][31..0]
stage_real[5][7][31..0]
stage_real[6][0][31..0]
stage_real[6][1][31..0]
stage_real[6][2][31..0]
stage_real[6][3][31..0]
stage_real[7][0][31..0]
stage_real[7][1][31..0]
stage_real[8][0][30..15]
D
CLK
Q
SCLR
32'h0
16'h0
1'h0 CIN
Add1000
Add1002
Add1004
Add1006
Add1008
A[31..0]
B[31..0]
OUT[31..0]
OUT[31..0]
[14,22]
OUT0
[17,22]